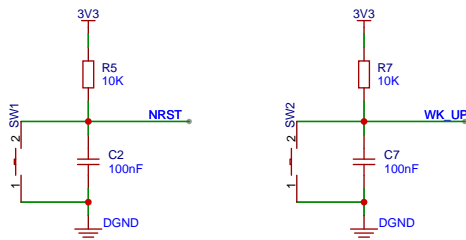


去耦电容
每个供电引脚旁边放俩

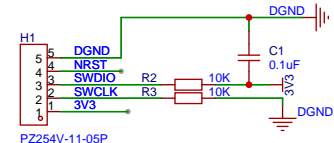


初始化复位

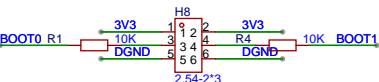
唤醒

MCU GD32F470VET6

SWD

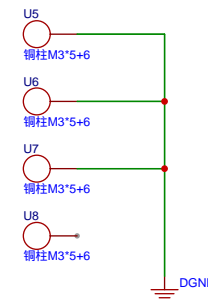
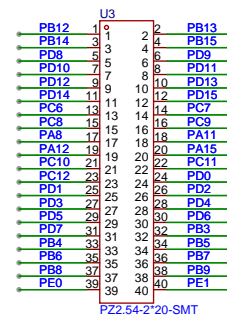
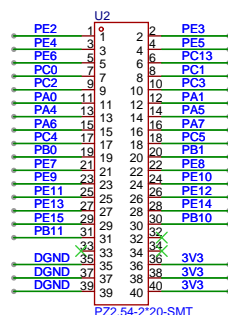


SWDIO上拉电阻：抑制噪声，防止随机翻转，提升可靠性。
SWCLK下拉电阻：防止浮空导致的噪声，提升可靠性。



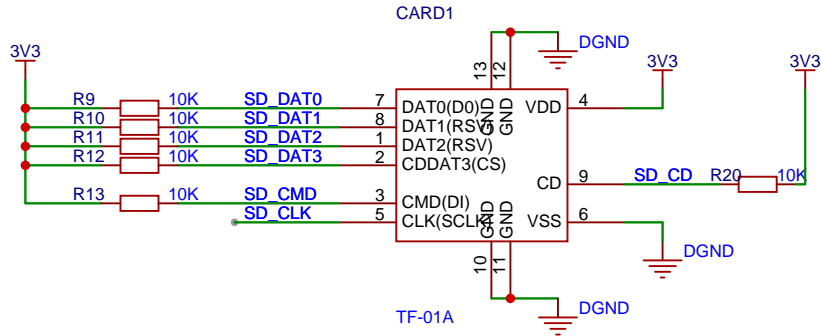
BOOT

BOOT	BOOT	启动方式
0	x	内部FLASH
1	1	内部SRAM
1	0	系统存储器

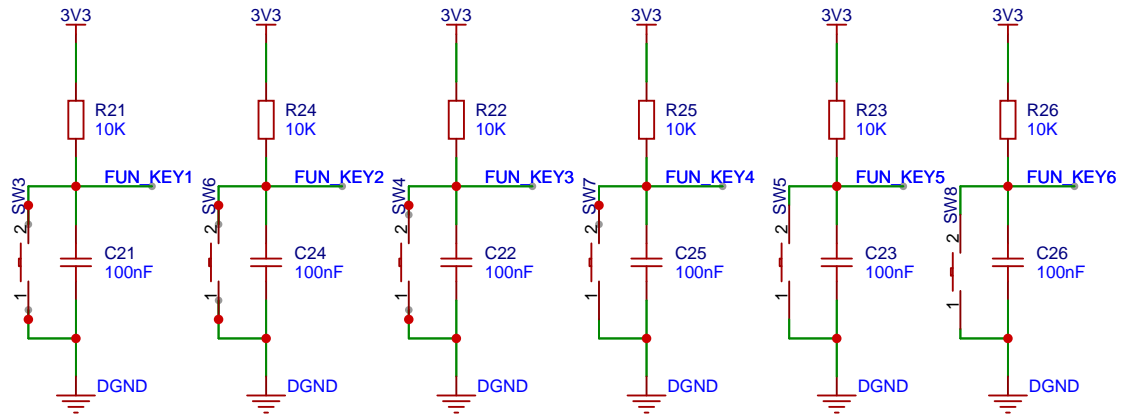


Pin Out

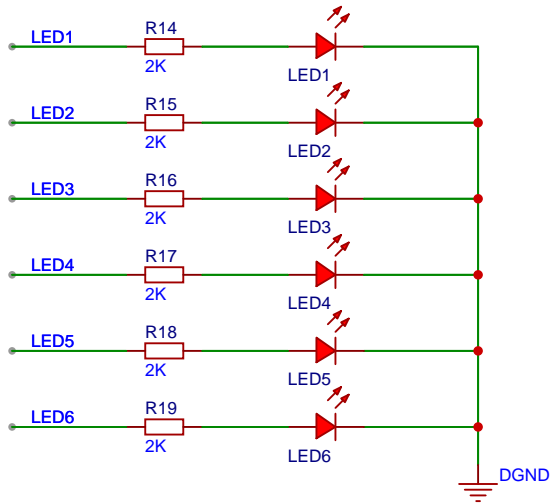
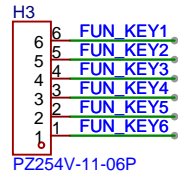
原理图	CoreBoard	创建日期	2025-03-01
板子	CIMC-IHD-Project	更新日期	2025-03-22
绘制	Lingyu Meng	图页	MCU Mainboard
审阅	Lingyu Meng		
		版本	尺寸
		V1.0	A4
		页	1 共 4
			CIMC “西门子杯” 智能制造挑战赛



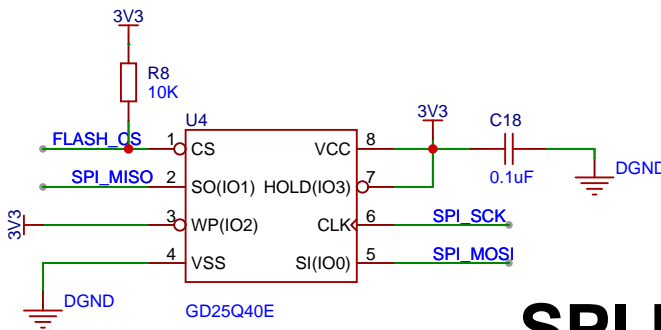
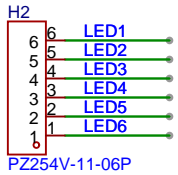
TF



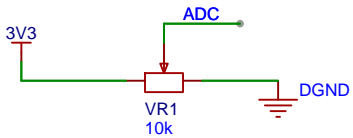
KEY



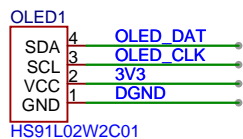
LED



SPI FLASH



ADC



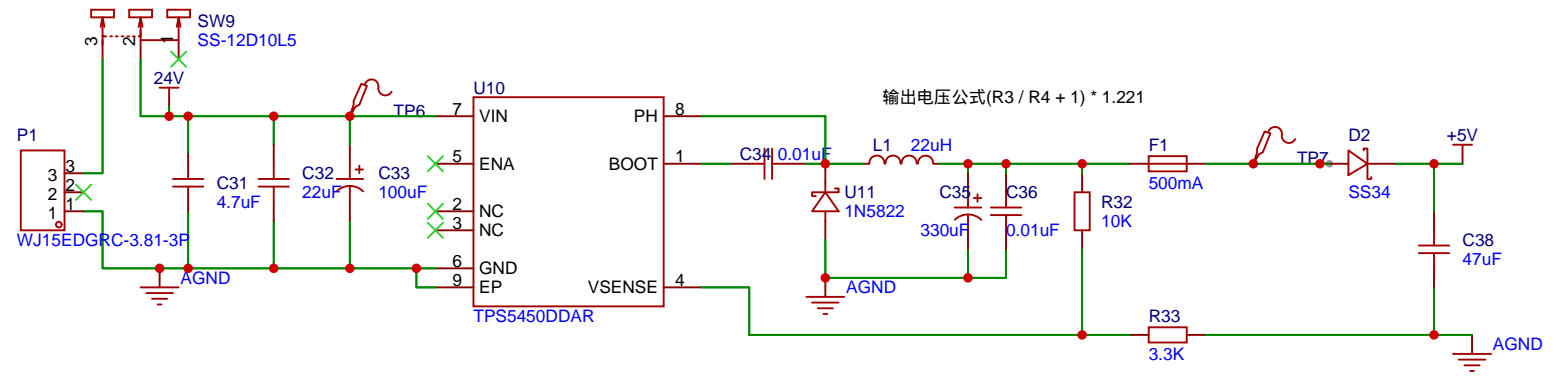
OLED

原理图	CoreBoard			创建日期	2025-03-01
板子	CIMC-IHD-Project			更新日期	2025-03-16
绘制	Lingyu Meng	CIMC-IHD-v04			
审阅	Lingyu Meng				
		版本	尺寸	页 2 共 4	
嘉立创EDA		V1.0	A4	CIMC “西门子杯” 智能制造挑战赛	

24V - 5V

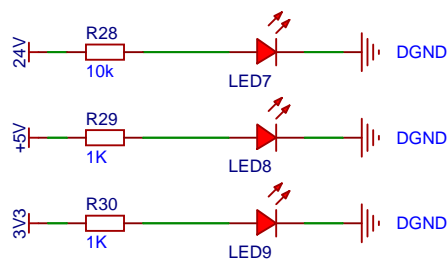
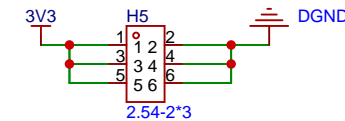
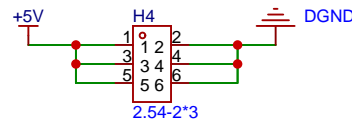
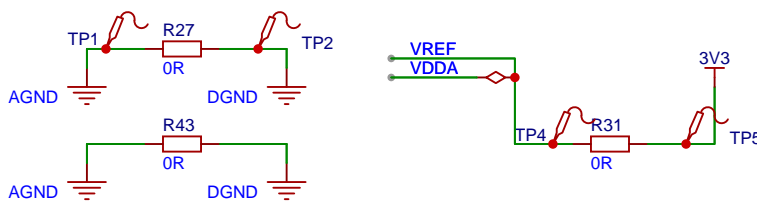
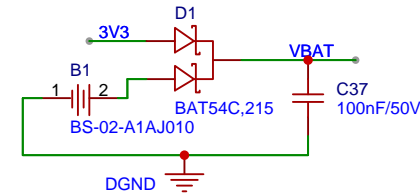
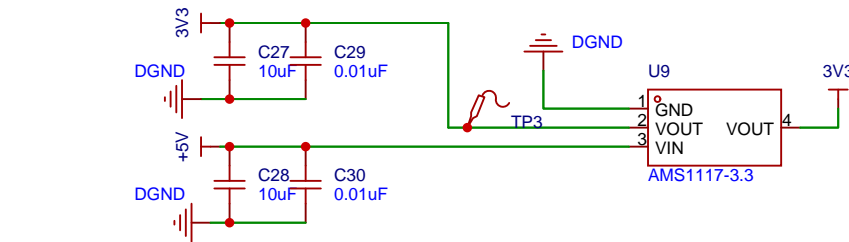
EN悬空默认为芯片工作状态
输入电压10~31V

EN悬空默认为芯片工作状态



5V - 3V3

Vbat

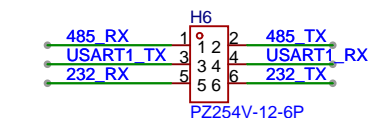
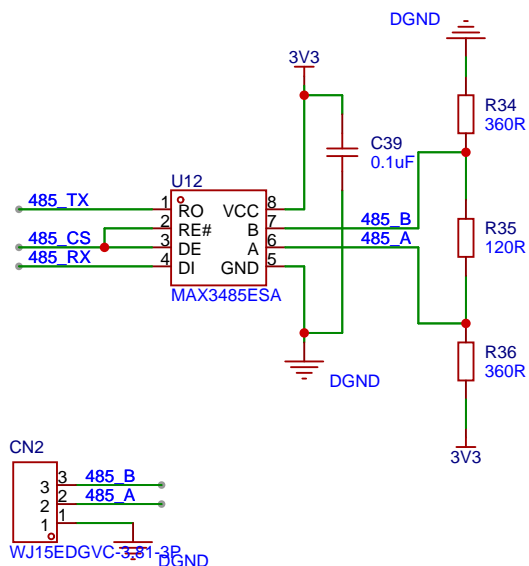


24V 电源灯

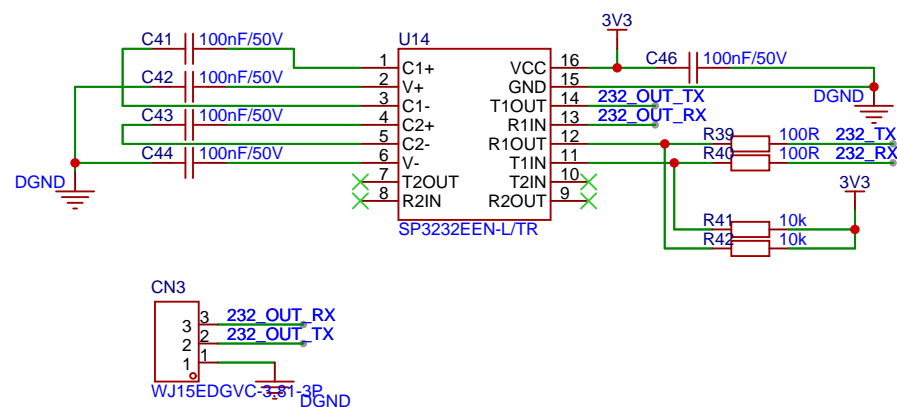
5V 电源灯

3.3V 电源灯

原理图	CoreBoard		创建日期	2025-03-01
板子	CIMC-IHD-Project		更新日期	2025-03-22
绘制	Lingyu Meng	CIMC-IHD-v04		
审阅	Lingyu Meng			
		版本	尺寸	页 3 共 4
嘉立创EDA		V1.0	A4	CIMC“西门子杯”智能制造挑战赛

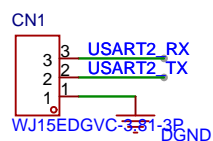


RS485

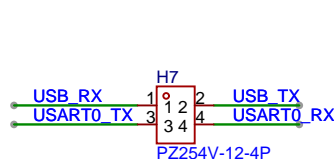


RS232

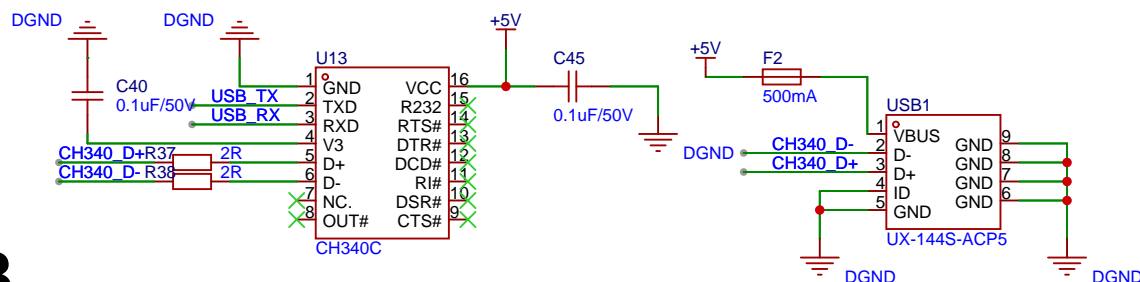
USART1



USART2



USB



原理图		CoreBoard		创建日期	2025-03-01
板子		CIMC-IHD-Project		更新日期	2025-03-11
绘制		Lingyu Meng		图页	commInterface
审阅		Lingyu Meng		CIMC-IHD-v04	
		版本	尺寸	页 4 共 4	
嘉立创EDA		V1.0	A4	CIMC “西门子杯” 智能制造挑战赛	