/pipeline_tb/uut/clk	1								
/pipeline_tb/uut/rst									
/pipeline_tb/uut/IdEx_pc	64'hXXXXXXX	XXXXXXXX			64'h0000000000000000	64'h0000000000	0000004		64'h000000000000000
/pipeline_tb/uut/WD	64'hXXXXXXX	XXXXXXXX						64'h00000000000000001	64'h0000000000000000
/pipeline_tb/uut/RD1	64'h00000000	00000000			64'h00000000000000001			64'h00000000000000001	64'h0000000000000000
/pipeline_tb/uut/RD2	64'h00000000	00000000	64'h0000000	000000001				64'h00000000000000001	
/pipeline_tb/uut/Signextendout	64'h00000000	00000000							
/pipeline_tb/uut/ALUresult	64'hXXXXXXX	XXXXXXXX			64'h0000000000000000	64'h0000000000	0000000	64'h00000000000000000	64'h00000000000000004
/pipeline_tb/uut/ExMem_Addin						64'h0000000000	0000000	64'h0000000000000000	
/pipeline_tb/uut/ExMem_Aluresult	64'hXXXXXXX	XXXXXXXX				64'h0000000000	0000000		64'h0000000000000000
/pipeline_tb/uut/ExMem_RD2	64h)0000000000000000	64'h0000000000000000				64'h0000000000	0000001	64'h0000000000000000	64'h00000000000000001
/pipeline_tb/uut/ReadData	64'hXXXXXXX	XXXXXXXX				64'h0000000000	0000001		
/pipeline_tb/uut/MemWb_Aluresult	64'hXXXXXXX	XXXXXXXX						64'h0000000000000000	
/pipeline_tb/uut/MemWb_RD	64'hXXXXXXX	XXXXXXXX						64'h00000000000000001	
/pipeline_tb/uut/IdEx_signextend	64'h00000000	00000000							
/pipeline_tb/uut/IfId_pc_out_out	64'hXXXXXXX	XXXXXXXX	64'h0000000	000000000	64'h0000000000000000			64'h0000000000000000	64'h000000000000000
/pipeline_tb/uut/MemWb_RegWrite								1	
/pipeline_tb/uut/IdEx_RegWrite					_				
/pipeline_tb/uut/ExMem_RegWrite									
/pipeline_tb/uut/IfId_instr_out_out	32'hXXXXXXX	X	32'hF840000	9	32'h8B090129			32'h8B09012A	32'hCB09014B
/pipeline_tb/uut/ForwardA1	2'h0					2'h2		2'h1	2'h2
/pipeline_tb/uut/ForwardB1	2'h0					2'h2		2'h1	2'h2
/pipeline_tb/uut/ALUOp	2'hX		2'h0		2'h0	2'h2			
/pipeline_tb/uut/IdEx_ALUOp	2'hX				2'h0			2'h2	
/pipeline_tb/uut/MemWb_Rd_out	5'hXX							5'h09	
/pipeline_tb/uut/ExMem_Rd_out	5'hXX					5'h09			
/pipeline_tb/uut/IdEx_Rd_out	5'hXX				5'h09				5'h0A
/pipeline_tb/uut/IdEx_Rn_out	5'hXX				5'h00	5'h09			
		400 ns		 		5 NO9		1200 ns	1400 ns

/pipeline_tb/uut/IdEx_Rm_out	5'hXX					5'h00		5'h09	T]
/pipeline_tb/uut/Readr	-			5'h09										
/pipeline_tb/uut/WR	-									5'h09				+
/pipeline_tb/uut/ExMem_instr5	-							5'h09						
/pipeline_tb/uut/IdEx_instr5	+					5'h09						5'h0A		
/pipeline_tb/uut/ALUcontrolout						4'h2		-	-				-	-
/pipeline_tb/uut/IdEx_sturins_out		-				11'h7C2		11'h458	-				-	}
/pipeline_tb/uut/IdEx_instr11	+	-				11'h7C2		11'h458	-			-	-	1
/ pipelinie_tb/ ddd/ ldEx_moti 11	111000					1111702		1111130						
		400) ns	600) ns	800) ns	100	0 ns	120	0 ns	140	0 ns	