| #instructions (assumed to be single-cycle) per operation | x86 SSE4, AVX2 | | | | | | | | | | arm64 NEON | | | | | | | | | | Wasm | | | | | | | | | | ppc (POWER8/9) w/ VSX | | | | | | | | | | intersection | | | | | | | | | | Myriad SHAVE | | | | | | | | | | Number of instructions by type and platform |
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