

10. Type Confusion & Control Flow Integrity

Seongil Wi

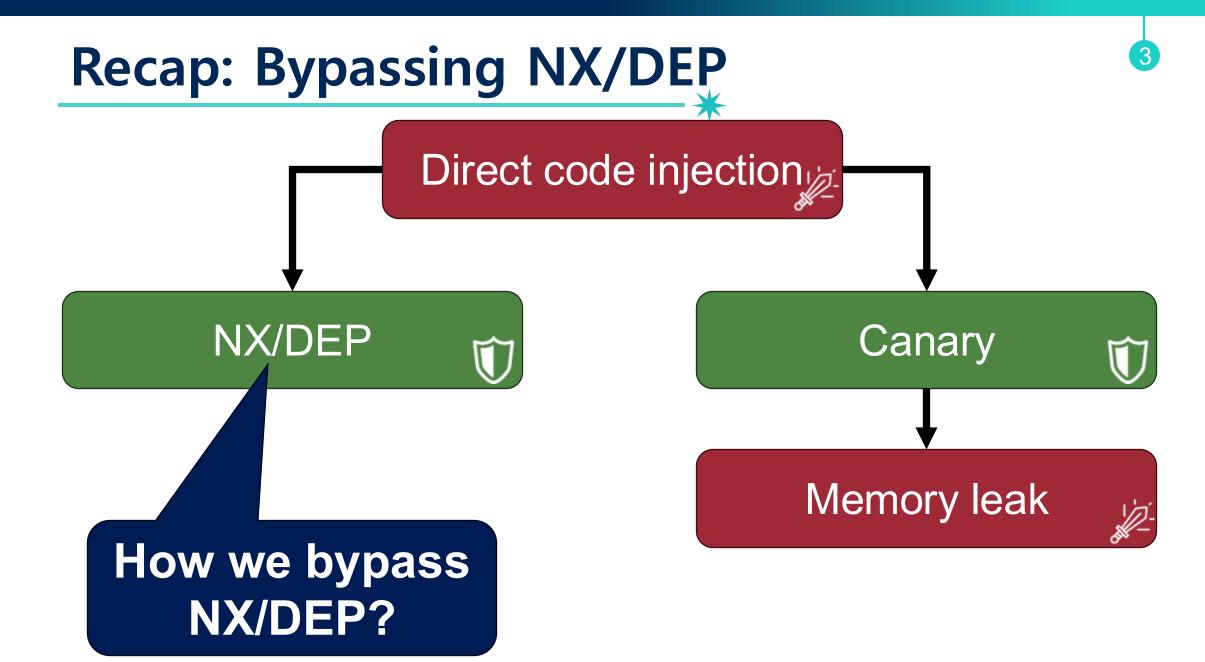






- Write a critique for the two papers:
 - WYSINWYX: What you see is not what you eXecute, *TOPLAS 2005*
 - The Geometry of Innocent Flesh on the Bone: Return-into-libc without Function Calls (on the x86), *CCS 20107*

• Due: November 11, 11:59PM



Recap: Bypassing DEP



- Return-to-stack exploit is disabled
- But, we can still jump to an arbitrary address of existing code
 (= Code Reuse Attack)

Recap: ROP (Return-oriented Programming)

Generalized Code Reuse Attack

Formally introduced by Hovav in CCS 2007

"The Geometry of Innocent Flesh on the Bone: Return-to-libc

without Function Calls (on the x86)"

The Geometry of Innocent Flesh on the Bone: Return-into-libc without Function Calls (on the x86)

> Hovav Shacham* hovav@cs.ucsd.edu

Abstract

We present new techniques that allow a return-into-libc attack to be mounted on x86 executables that calls no functions at all. Our attack combines a large number of short instruction sequences to build gadgets that allow arbitrary computation. We show how to discover such instruction sequences by means of static analysis. We make use, in an essential way, of the properties of the x86 instruction set.

1 Introduction

We present new techniques that allow a return-into-libc attack to be mounted on x86 executables that is every bit as powerful as code injection. We thus demonstrate that the widely deployed "W \oplus X" defense, which rules out code injection but allows return-into-libc attacks, is much less useful than previously thought.

Attacks using our technique call no functions whatsoever. In fact, the use instruction sequences from libc that weren't placed there by the assembler. This makes our attack resilient to defenses that remove certain functions from libc or change the assembler's code generation choices.

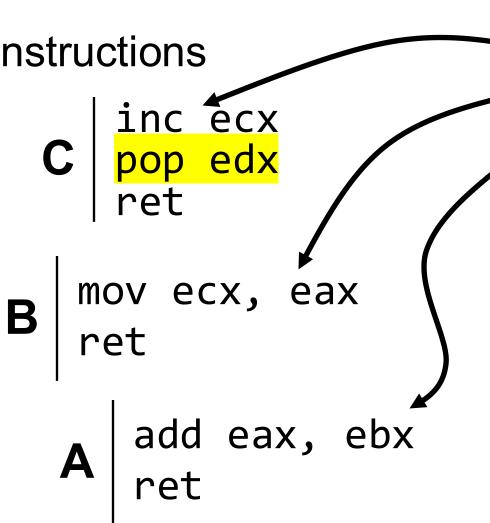
Unlike previous attacks, ours combines a large number of short instruction sequences to build

Recap: Return (ret) Chaining

Attacker's goal:

execute following instructions

add eax, ebx
mov ecx, eax
inc ecx
mov edx, 42



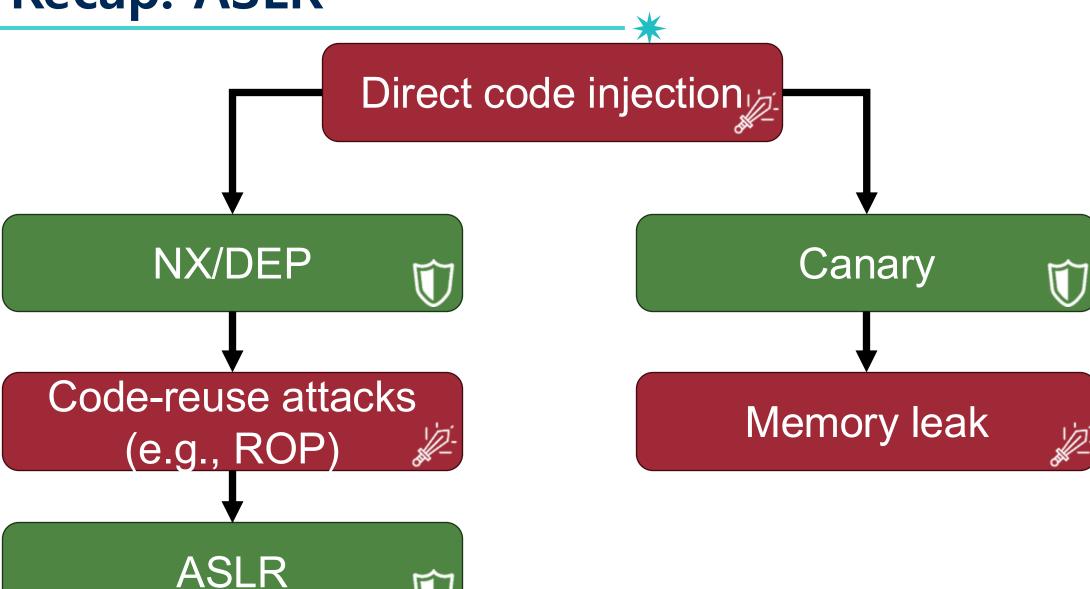
esp

Address of C
Address of B

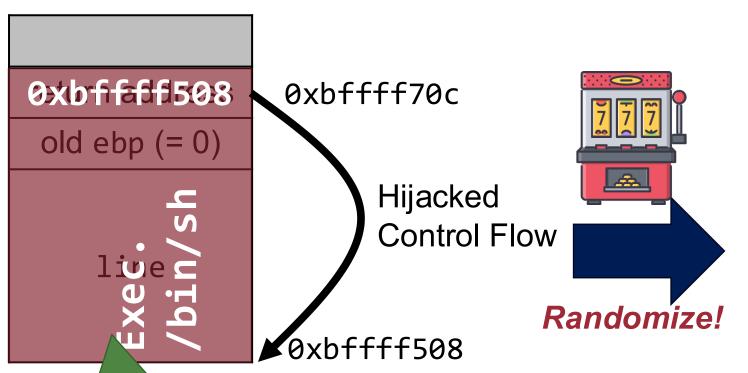
rAddress lof A

old ebp (= 0)

Dummy line value Recap: ASLR

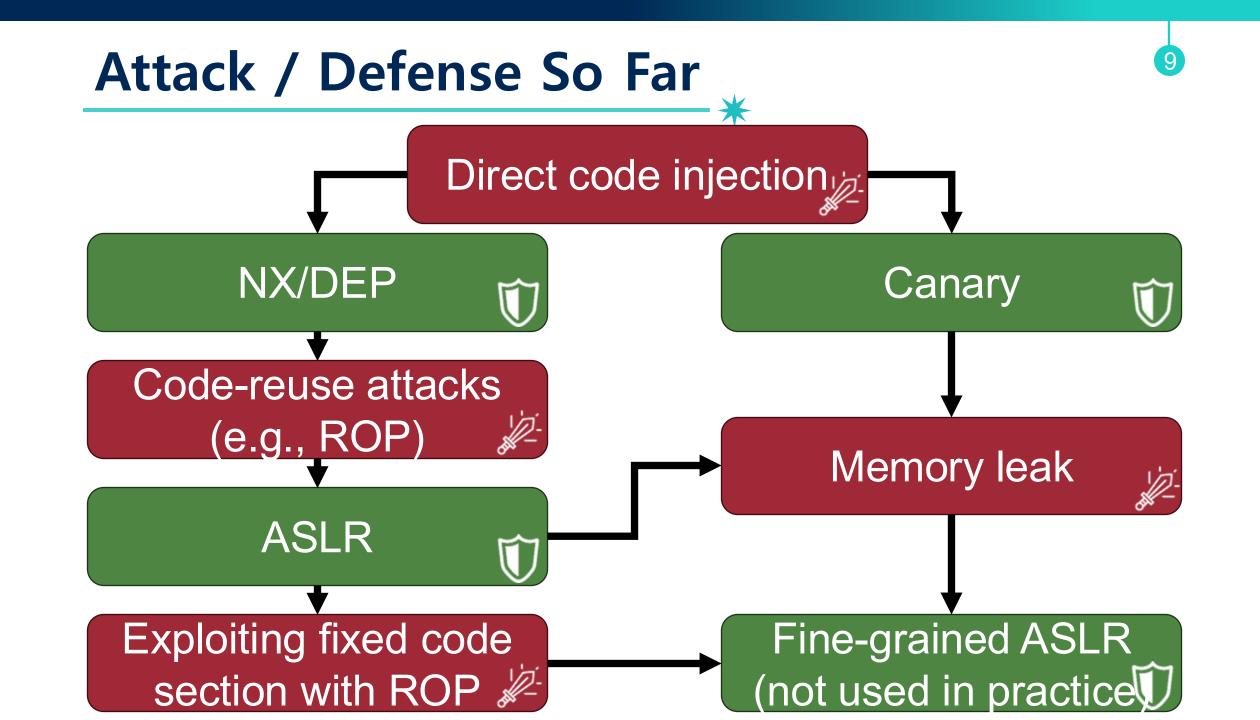


Recap: ASLR



0xbffff508 0xbffff428 old ebp (= 0)live 0xbffff62c 0xbffff508

DEP: Make this region non-executable!



Memory Corruption So Far

10

Buffer overflows

What is another major attack vector to corrupt memory?

Type





A classification of data which tells the compiler or interpreter how the programmer intends to use the data

Type Safety



Types prevent unintended errors

```
>>> 1 + "1"
```

TypeError: unsupported operand type(s) for +: 'int' and 'str'



Mistaking <u>a memory location for certain type</u> as <u>a memory for different type</u>

Type confusion happens when the type-safety is violated

```
Dog class

Dog class

Normal

Dog class

d->bark();
```

Dog *d = (Dog*) some_ptr; d->bark(); //??? Person class

```
Dog class

Dog class

Dog class

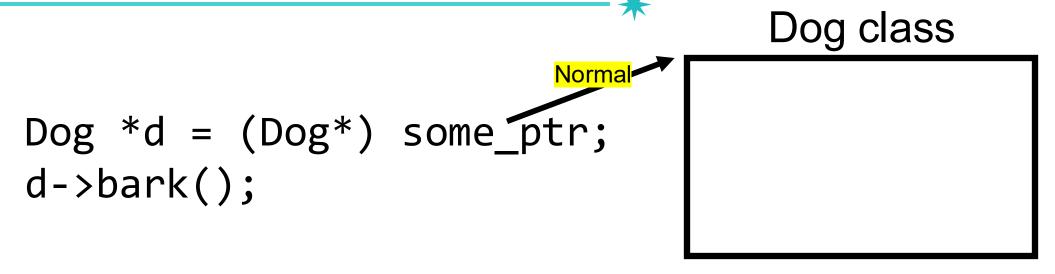
d->bark();
```

Abnormal -

Type Confusion

Person class





Abnormal

Type Confusion

Dog *d = (Dog*) some_ptr;
d->bark(); //???

Invoke person's something

Person class

Type Confusion Attack (Implication)

Dog class

bark()

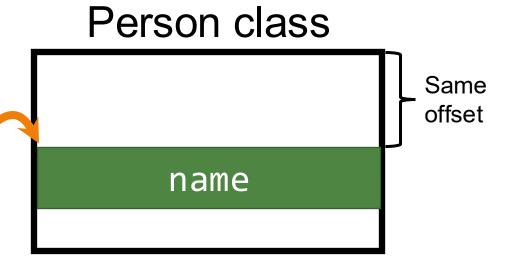
Person class

name

Type Confusion Attack (Implication)

Dog class
Same offset
bark()

Dog *d = (Dog*) some_ptr;
d->bark(); //???
Control flow



Type Confusion Attack (Implication)



bark()

```
some_ptr->name="[shellcode]"
...
Dog *d = (Dog*) some_ptr;
d->bark(); //???
```

Person class

Addr. of shellcode

20

Same

Same

offset

Type Confusion Attack (Implication)

Dog *d = (Dog*) some_ptr;
d->bark();

Control flow

some_ptr->name="[shellcode]"

Person class

Dog class

•••

Addr. of shellcode

Same offset

Type Confusion Example: Downcasting

```
class Ancestor {
    public:
        int mAncestor;
class Descendant: public Ancestor {
    public:
        int mDescendant;
                                     Inherit
                                Ancestor class
```



Type Confusion Example: Downcasting

```
class Ancestor {
    public:
        int mAncestor;
class Descendant: public Ancestor {
    public:
        int mDescendant;
};
     Vulnerable code
```

vtable mAncestor

vtable
mAncestor
mDescendant

```
Ancestor* a = new Ancestor();
Descendant* d = static_cast<Descendant*>(a);
d->mDescendant = 42;
```

Type Confusion Example: Downcasting

```
class Ancestor {
    public:
        int mAncestor;
class Descendant: public Ancestor {
    public:
 Downcasted
                cendant;
    pointer
               e code
     Vulnera
```

vtable mAncestor

vtable
mAncestor
mDescendant

```
Ancestor* a = new Ancestor();
Descendant* d = static_cast<Descendant*>(a);
d->mDescendant = 42;
```

Type Confusion Example: Downcasting

```
class Ancestor {
                                                   vtable
    public:
                                                mAncestor
         Memory corruption:
      It can now access a memory
};
     region that was not allocated!
class Descendant: public
                            estor
                                                   vtable
    public:
 Downcasted
                cendant;
                                                mAncestor
    pointer
                                              mDescendant
              e code
     Vulnera
     Ancestor* a = new Ancestor()
     Descendant* d = static cast<Descendant*>(a);
     d->mDescendant = 42;
```

Question: But, Why Get Confused?

Suppose there is a huge gap between these lines (e.g., separated in two different libraries)

Vulnerable code

```
Ancestor* a = new Ancestor();
Descendant* d = static_cast<Descendant*>(a);
d->mDescendant = 42;
```

Implication of the Downcasting

What if a user can write an arbitrary value to the confused pointer?

vtable mAncestor

42

vtable
mAncestor
mDescendant

Vulnerable code

```
Ancestor* a = new Ancestor();
Descendant* d = static_cast<Descendant*>(a);
d->mDescendant = 42;
```

Attacker's Perspective



Unlike other attack vectors, we can **reliably** corrupt a certain memory field, *i.e.*, we don't need to know the actual address of mDescendant

vtable mAncestor mDescendant

vtable

mAncestor

42

Vulnerable code

```
Ancestor* a = new Ancestor();
Descendant* d = static_cast<Descendant*>(a);
d->mDescendant = 42;
```

Real-world Example (CVE-2013-0912)

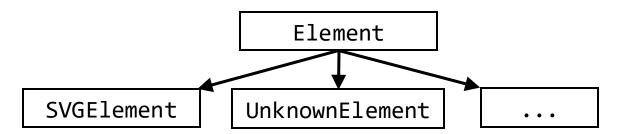
Type Confusion in WebKit (Used in Pwn2Own 2013)

```
SVGElement* SVGViewSpec::viewTarget() {
  if (!m contextElement)
    return 0;
  return static_cast<SVGElement*>(
    m contextElement->treeScope()->getElementById(
      m viewTargetString
```

Real-world Example (CVE-2013-0912)

• Type Confusion in WebKit (Used in Pwn20)

```
Developer thought this
SVGElement* SVGViewSpec::viewTarget() {
                                               must always be
  if (!m contextElement)
                                               SVGElement type
    return 0;
                                               (but it turned out to be not!)
  return static_cast<SVGElement*>(
   m contextElement->treeScope()->getElementById(
      m viewTargetString
```



Real-world Example (CVE-2013-0912)

• Type Confusion in WebKit (Used in Pwn20)

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Developer thought this
 SVGElement* SVGViewSpec::viewTarget() {
                                                     must always be
    if (!m contextElement)
                                                     SVGElement type
                                                     (but it turned out to be not!)
      return 0;
    return static_cast<SVGElement*>(
     m contextElement->treeScope()->getElementById(
        m viewTargetString
                                        PoC.html
                                      <svg xmlns="..."
                                        <foreignobject>
                                          <body xmlns="...">
                Element
                                             <feColorMatrix viewTarget feColorMatrix>
                                          </body>
                                        </foreignobject>
SVGElement
             UnknownElement
```

</svg>





• Type Confusion in WebKit (Used in Pwn20)

```
Developer thought this
 SVGElement* SVGViewSpec::viewTarget() {
                                                     must always be
    if (!m contextElement)
                                                     SVGElement type
      return 0;
                                                     (but it turned out to be not!)
    return static_cast<SVGElement*>(
      m contextElement->treeScope()->getElementById(
                                                                   Return
        m viewTargetString
                                                                   UnknownElement
                                        PoC.html
                                       <svg xmlns="..."
                                       <foreignobject>
                                          <body xmlns="...">
                Element
                                              <feColorMatrix viewTarget feColorMatrix>
                                          </body>
                                        </foreignobject>
SVGElement
             UnknownElement
                                       </svg>
```

Patch: Use dynamic_cast

Limitations:

Slow

• Compiler options such as --fno-rtti can disable it!

Use After Free (A popular source of type confusion)

Use After Free



• If after <u>freeing a memory location</u>, a program <u>does not clear the pointer to that memory</u>, an attacker can use it to hack the program

Use After Free Example

```
*
```

```
Foo * f = new Foo();
Foo * ptr = f;
ptr->x = 42;
delete f;
f = NULL;
Bar * b = new Bar();
b->y = "hello world";
cout << ptr->x << endl;</pre>
```

Class information

```
class Foo {
   public:
      int x;
};
class Bar {
   public:
      const char* y;
};
```



Use After Free Example



```
Foo * f = new Foo();
Foo * ptr = f;
ptr->x = 42;
delete f;
f = NULL;
Bar * b = new Bar();
b->y = "hello world";
```

f

Class Foo

cout << ptr->x << endl;</pre>

Class information

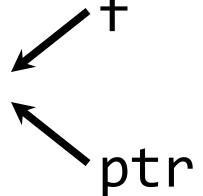
```
class Foo {
   public:
      int x;
};
class Bar {
   public:
      const char* y;
};
```

Use After Free Example



```
Foo * f = new Foo();
Foo * ptr = f;
ptr->x = 42;
delete f;
f = NULL;
Bar * b = new Bar();
b->y = "hello world";
cout << ptr->x << endl;</pre>
```

Class Foo



Class information

```
class Foo {
   public:
      int x;
};
class Bar {
   public:
      const char* y;
};
```

Use After Free Example



```
Foo * f = new Foo();
Foo * ptr = f;
ptr->x = 42;
delete f;
f = NULL;
Bar * b = new Bar();
b->y = "hello world";
cout << ptr->x << endl;</pre>
```

/f \ ptr

Class information

```
class Foo {
  public:
    int x;
};
class Bar {
  public:
    const char* y;
};
```

Class Foo

Foo.x = 42

Use After Free Example



```
Foo * f = new Foo();
Foo * ptr = f;
ptr->x = 42;
              Return the block to the
delete f;
                      free list
f = NULL;
Bar * b = new Bar();
b->y = "hello world";
cout << ptr->x << endl;</pre>
    Class Foo
     Foo.x = 42
```

Class information

```
class Foo {
   public:
      int x;
};
class Bar {
   public:
      const char* y;
};
```

Use After Free Example

```
Foo * f = new Foo();
Foo * ptr = f;
ptr->x = 42;
delete f;
f = NULL;
Bar * b = new Bar();
b->y = "hello world";
cout << ptr->x << endl;</pre>
```

Class information

```
| class Foo {
| public:
| int x;
| };
| class Bar {
| public:
| const char* y;
| };
```

Class Foo

Foo.x = 42

Often called "Dangling Pointer"

Use After Free Example

```
42
```



```
Foo * f = new Foo();
Foo * ptr = f;
ptr->x = 42;
find an appropriate block
delete f;
f = NULL;
Bar * b = new Bar();
b->y = "hello world";
cout << ptr->x << endl;</pre>
```

Class information

```
class Foo {
   public:
      int x;
};
class Bar {
   public:
      const char* y;
};
```

Class Bar

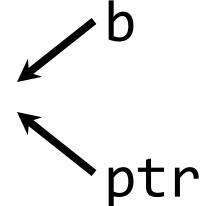


Use After Free Example

```
• *
```

```
Foo * f = new Foo();
Foo * ptr = f;
ptr->x = 42;
delete f;
f = NULL;
Bar * b = new Bar();
b->y = "hello world";
cout << ptr->x << endl;</pre>
```

Class Bar Bar.y="hello world"



Class information

```
class Foo {
   public:
      int x;
};
class Bar {
   public:
      const char* y;
};
```

Use After Free Example

```
Foo * f = new Foo();
Foo * ptr = f;
ptr->x = 42;
delete f;
f = NULL;
Bar * b = new Bar();
b->y = "hello world";
cout << ptr->x << endl;</pre>
```

Class information

```
class Foo {
   public:
      int x;
};
class Bar {
   public:
      const char* y;
};
```

Class Bar
Bar.y="hello world"

Print the address of the Bar.y

Use After Free Example

```
Foo * f = new Foo();
Foo * ptr = f;
ptr->x = 42;
delete f;
f = NULL;
Bar * b = new Bar();
b->y = "hello world";
cout << ptr->x << endl;</pre>
```

Class information

```
class Foo {
  public:
    int x;
```

We *use*d this pointer after free ar* y;

Class Bar Bar.y="hello world" Print the address of the Bar.y

Use After Free can Trigger Type Confusion Type Confusion

 A dangling pointer's type and the corresponding reallocated data's type can be different => Trigger type confusion!

Example: OpenSSL UAF Bug

```
dtls1_hm_fragment_free(frag);
pitem_free(item);
if (al==0) {
    *ok = 1;
    return frag->msg_header.frag_len;
}
```

Example: OpenSSL UAF Bug

```
dtls1_hm_fragment_free(frag);
frag is freed
pitem_free(item);
if (al==0) {
    *ok = 1;
    return frag->msg_header.frag_len;
}
```

Example: OpenSSL UAF Bug

```
frag is freed
dtls1_hm_fragment_free(frag)
pitem free(item);
if (al==0) {
    *ok = 1;
    return frag->msg_header.frag len;
```

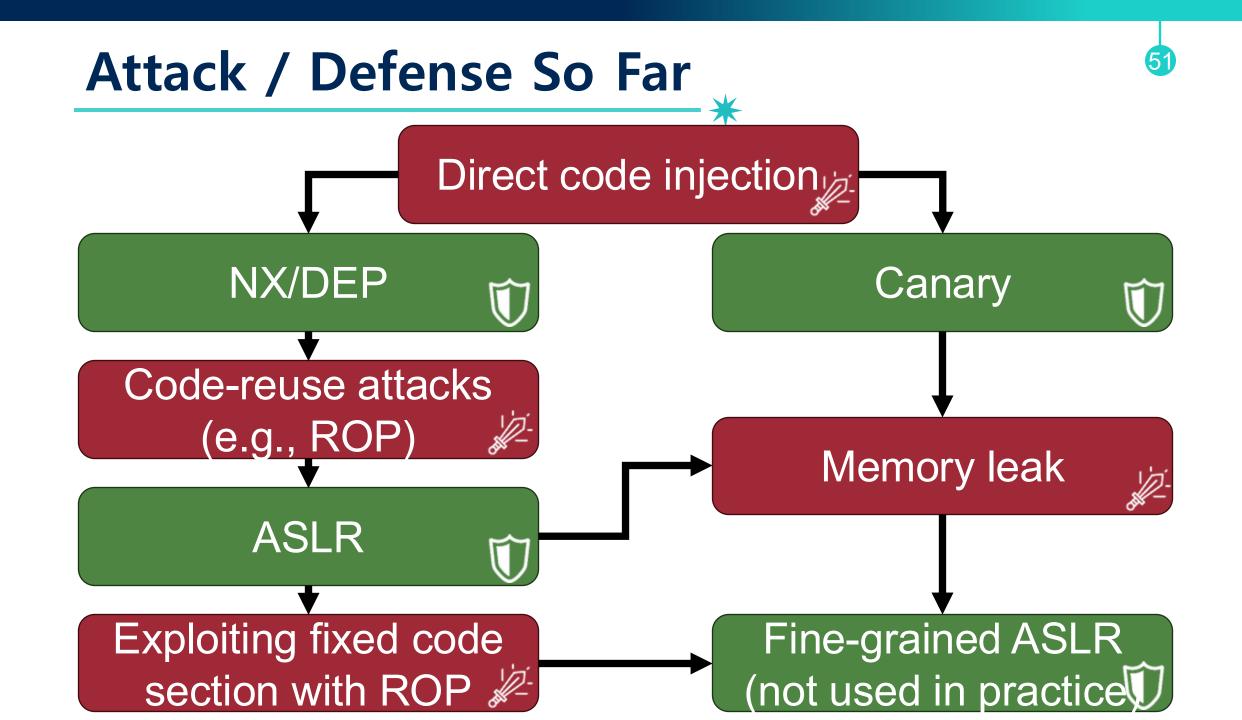
Read after the free

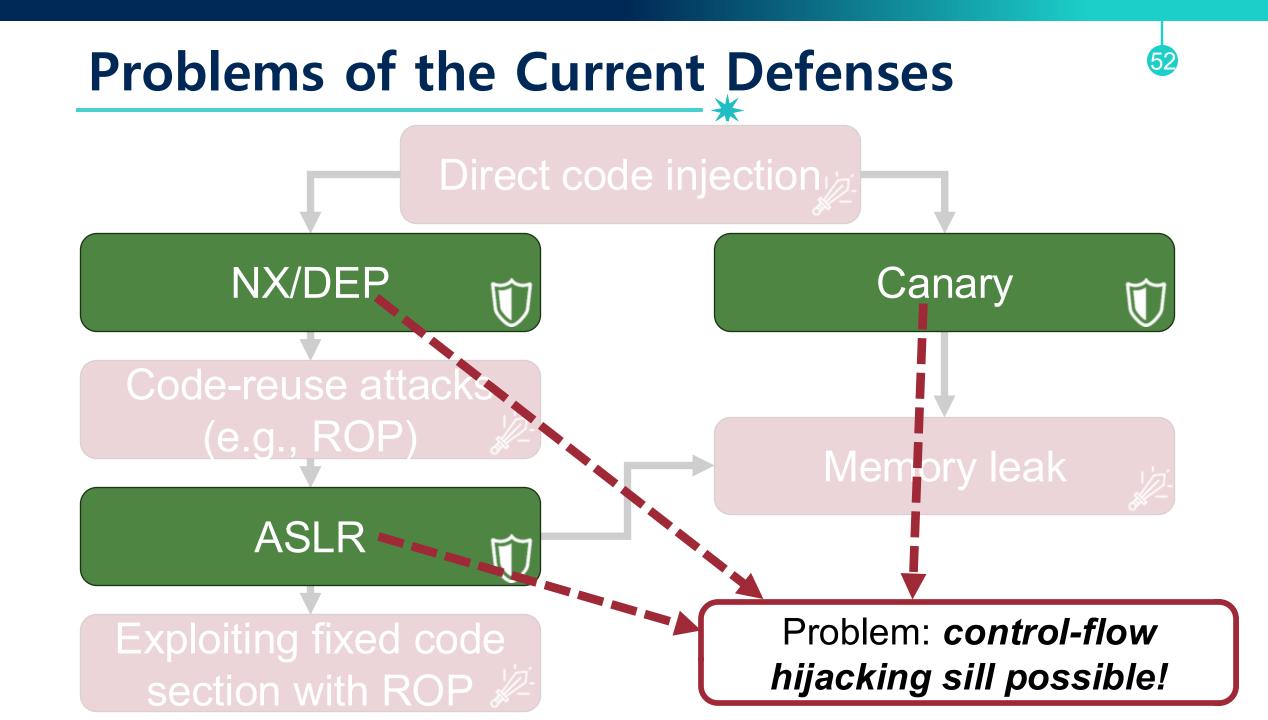
Research: Type Confusion Detection

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 Static Detection of C++ vtable Escape Vulnerabilities in Binary Code, NDSS 2012

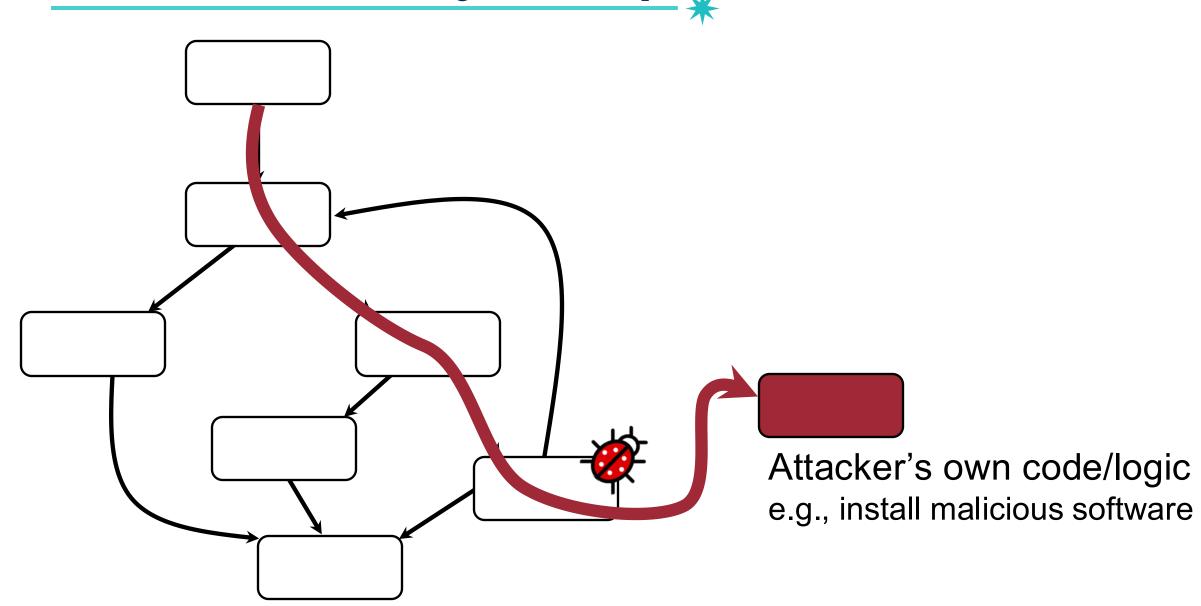
 Type Casting Verification: Stopping an Emerging Attack Vector, USENIX Security 2015





Control Flow Hijack Exploit







Can we enforce control-flow integrity?

Control Flow Integrity (CFI)

CFI Policy



The CFI security policy dictates that software execution must follow a path of a Control-Flow Graph (CFG) determined *ahead of time*.

Control-flow Integrity, CCS 2005

Control-Flow Integrity

Principles, Implementations, and Applications

Martín Abadi Computer Science Dept. University of California Santa Cruz Mihai Budiu Úlfar Erlingsson Microsoft Research Silicon Valley

Jay Ligatti
Dept. of Computer Science
Princeton University

ABSTRACT

Current software attacks often build on exploits that subvert machine-code execution. The enforcement of a basic safety property, Control-Flow Integrity (CFI), can prevent such attacks from arbitrarily controlling program behavior. CFI enforcement is simple, and its guarantees can be established formally, even with respect to powerful adversaries. Moreover, CFI enforcement is practical: it is compatible with existing software and can be done efficiently using software rewriting in commodity systems. Finally, CFI provides a useful foundation for enforcing further security policies, as we demonstrate with efficient software implementations of a protected shadow call stack and of access control for memory regions.

bined effects of these attacks make them one of the most pressing challenges in computer security.

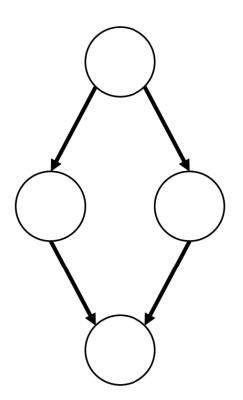
In recent years, many ingenious vulnerability mitigations have been proposed for defending against these attacks; these include stack canaries [14], runtime elimination of buffer overflows [46], randomization and artificial heterogeneity [41, 62], and tainting of suspect data [55]. Some of these mitigations are widely used, while others may be impractical, for example because they rely on hardware modifications or impose a high performance penalty. In any case, their security benefits are open to debate: mitigations are usually of limited scope, and attackers have found ways to circumvent each deployed mitigation mechanism [42, 49, 61].

The limitations of these mechanisms stem, in part, from the lack

Background: Control Flow Graph (CFG)

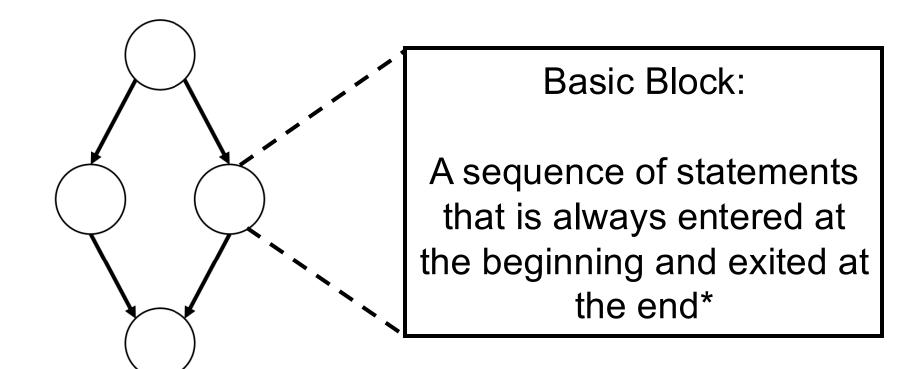
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 A CFG is a graph that represents all paths that might be traversed through a program execution



Background: Control Flow Graph (CFG)

- A CFG is a graph that represents all paths that might be traversed through a program execution
- Each node in a CFG represents a basic block



Background: Basic Block

```
push
                                    ebp
 0:
     55
 1: 89 e5
                                    ebp, esp
                             mov
     83 ec 10
                             sub
 3:
                                    esp,0x10
     c7 45 f8 00 00 00 00
                                    DWORD PTR [ebp-0x8],0x0
                             mov
    c7 45 fc 0a 00 00 00
                                    DWORD PTR [ebp-0x4],0xa
d:
                             mov
14: eb 08
                             jmp
                                    1e < v + 0x1e >
16: 83 45 f8 01
                             add
                                    DWORD PTR [ebp-0x8],0x1
1a: 83 6d fc 01
                                    DWORD PTR [ebp-0x4],0x1
                             sub
    83 7d fc 00
                                 DWORD PTR [ebp-0x4],0x0
1e:
     7f f2
22:
                             jg
                                    16 <v+0x16>
    8b 45 f8
                             mov eax, DWORD PTR [ebp-0x8]
24:
27:
   c9
                             leave
28:
    c3
                             ret
```

Background: Basic Block

```
0:
     55
                               push
                                      ebp
     89 e5
                                      ebp, esp
                               mov
     83 ec 10
                               sub
                                      esp,0x10
     c7 45 f8 00 00 00 00
                                      DWORD PTR [ebp-0x8],0x0
                               mov
 d:
     c7 45 fc 0a 00 00 00
                                      DWORD PTR [ebp-0x4],0xa
                               mov
     eb 08
                                      1e <v+0x1e>
14:
                               jmp
     83 45 f8 01
                               add
                                      DWORD PTR [ebp-0x8],0x1
16:
1a:
     83 6d fc 01
                                      DWORD PTR [ebp-0x4],0x1
                               sub
     83 7d fc 00
                                   DWORD PTR [ebp-0x4],0x0
1e:
     7f f2
22:
                               jg
                                      16 < v + 0 \times 16 >
     8b 45 f8
                               mov eax, DWORD PTR [ebp-0x8]
24:
27:
     c9
                               leave
28:
     c3
                               ret
```

Background: Basic Block



		0:	55							push	n ebp
		1:	89	e5						mov	ebp,esp
		3:	83	ec	10					sub	esp,0x10
		6:	c 7	45	f8	00	00	00	00	mov	DWORD PTR [ebp-0x8],0x0
		d:	c 7	45	fc	0a	00	00	00	mov	DWORD PTR [ebp-0x4],0xa
F		14:	eb	08						jmp	1e <v+0x1e></v+0x1e>
		16:	83	45	f8	01				add	DWORD PTR [ebp-0x8],0x1 ←
		1a:	83	6d	fc	01				sub	DWORD PTR [ebp-0x4],0x1
		1e:	83	7d	fc	00				cmp	DWORD PTR [ebp-0x4],0x0
		22:	7 f	f2						jg_	16 <v+0x16></v+0x16>
		24:	8b	45	f8					mov	eax, DWORD PTR [ebp-0x8]
		27:	c 9							leav	'e
		28:	c 3							ret	

Background: Basic Block

28:

c3

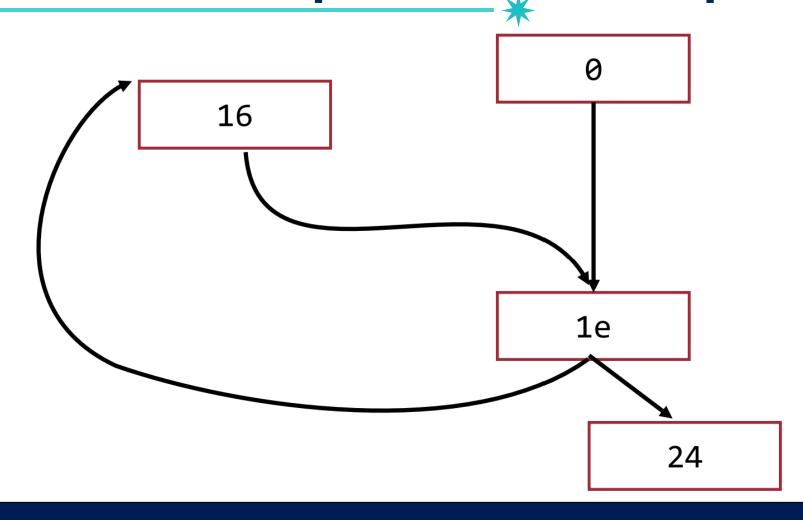


	0:	55	push ebp
	1:	89 e5	mov ebp,esp
	3:	83 ec 10	sub esp,0x10
	6:	c7 45 f8 00 00 00 00	mov DWORD PTR [ebp-0x8],0x0
	d:	c7 45 fc 0a 00 00 00	mov DWORD PTR [ebp-0x4],0xa
_	14:	eb 08	jmp 1e <v+0x1e></v+0x1e>
	16:	83 45 f8 01	add DWORD PTR [ebp-0x8],0x1
	1a:	83 6d fc 01	sub DWORD PTR [ebp-0x4],0x1
	1e:	83 7d fc 00	cmp DWORD PTR [ebp-0x4],0x0
	22:	7f f2	jg 16 <v+0x16></v+0x16>
	24:	8b 45 f8	mov eax,DWORD PTR [ebp-0x8]
	27:	c9	leave

ret



Control Flow Graph of the Example



CFI Intuition: Any execution should follow control paths of this CFG!

CFI Assumptions



*

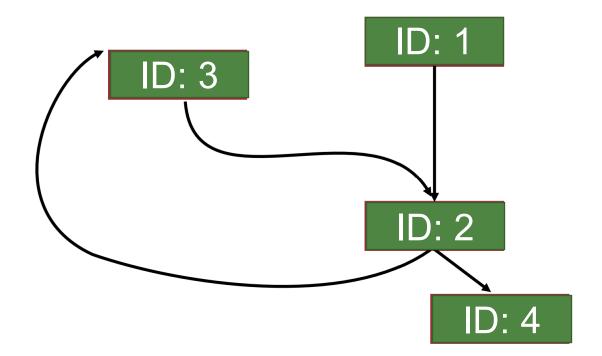
Attackers cannot execute data (DEP is enabled)

Programs cannot change themselves (no self-modifying code)

How to Enforce CFI?



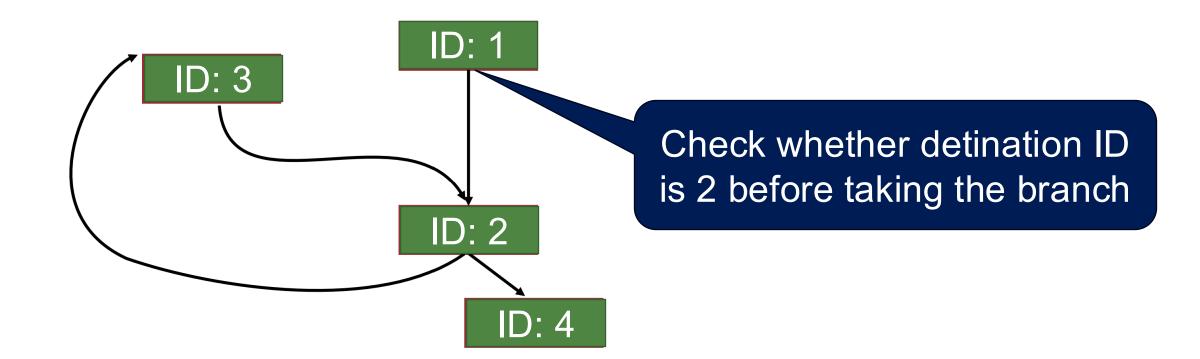
Give unique IDs at destinations



How to Enforce CFI?

*

- Give unique IDs at destinations
- For all branch instructions, check destination IDs before taking the branch



How to Instrument?



Opcode bytes	Source Instructions	5	Opcode bytes	Destination Instructions	
FF E1	jmp ecx	; computed jump	8B 44 24 04	mov eax, [esp+4]	; dst
		can be instrumented as (a):		
81 39 78 56 34 12 75 13 8D 49 04 FF E1	<pre>cmp [ecx], 1234567 jne error_label lea ecx, [ecx+4] imp ecx</pre>	78h ; comp ID & dst ; if != fail ; skip ID at dst ; iump to dst	78 56 34 12 8B 44 24 04	; data 12345678h mov eax, [esp+4]	; ID ; dst

CFI Challenge

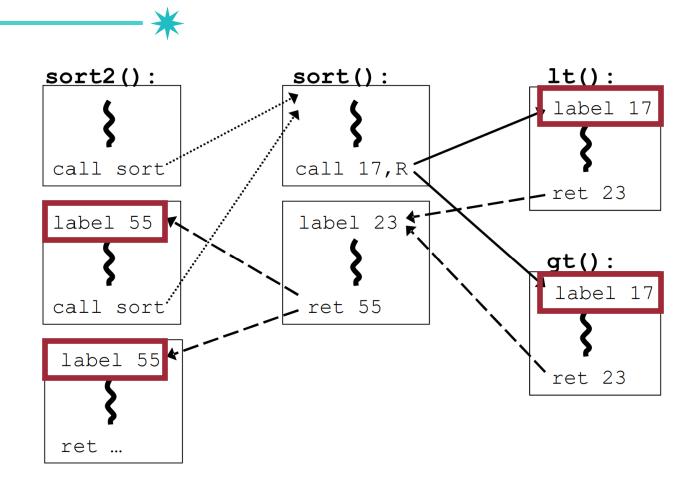




What if a single branch instruction can jump to multiple addresses? (e.g., call eax)

Example

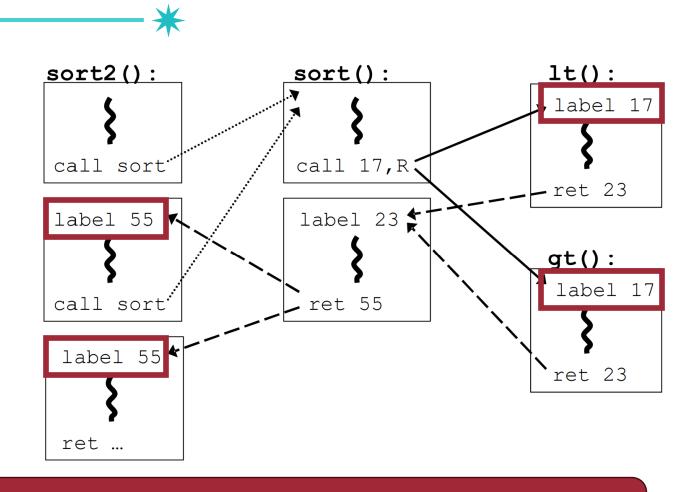
```
bool lt(int x, int y) {
    return x < y;
bool gt(int x, int y) {
    return x > y;
sort2(int a[], int b[], int len)
    sort( a, len, lt );
    sort( b, len, gt );
```



Example

```
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```

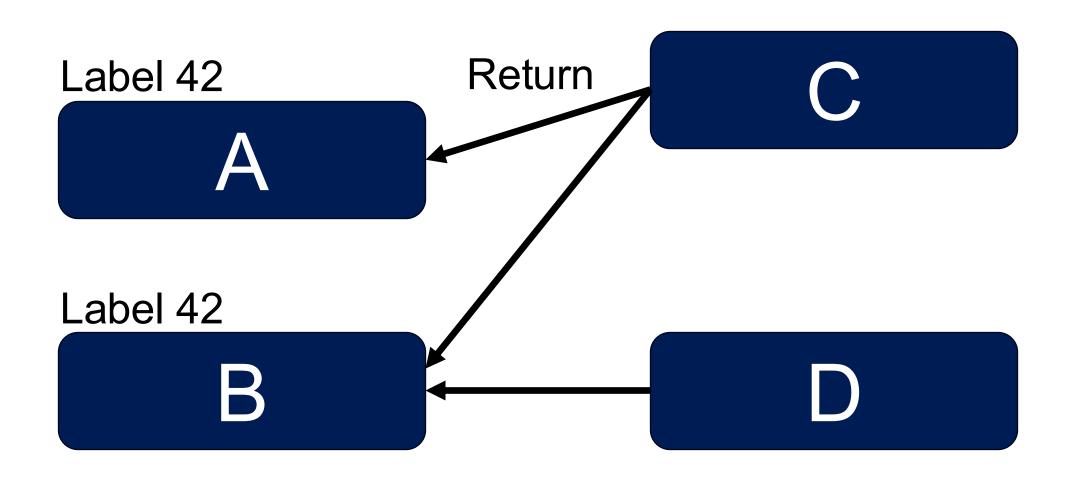
```
bool lt(int x, int y) {
    return x < y;
bool gt(int x, int y) {
    return x > y;
sort2(int a[], int b[], int len)
    sort( a, len, lt );
    sort( b, len, gt );
```



Can you spot labeling problems?

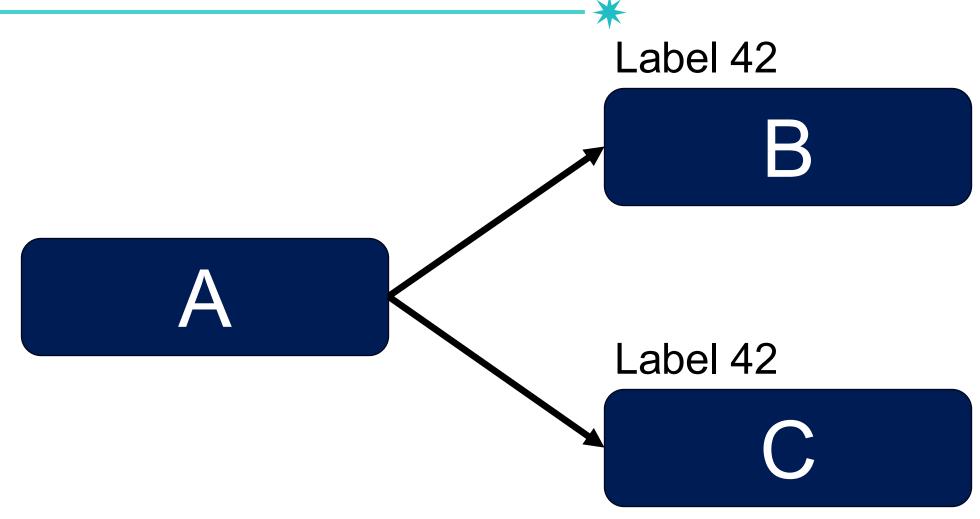
Problem #1: What if D returns to A?











Potential Solutions



- Multiple tags
 - -Q. What's the problem?

Shadow call stack

Shadow Call Stack



• In function prologues, store the return address in another area of memory

• In function epilogues, check if we are returning to the proper address

A Binary Rewriting Defense against Stack based Buffer Overflow Attacks, *USENIX ATC 2003*

CFI with Shadow Call Stack

```
call eax
                           ; call func ptr
                                                                            ; return
                                                        ret
       with a CFI-based implementation of a protected shadow call stack using hardware segments, can become:
           gs:[0h], 4h
                                                            ecx, gs:[0h]
                           ; inc stack by 4
                                                                            ; get top offset
                                                        mov
                                                            ecx, gs:[ecx] ; pop return dst
           ecx, gs:[0h]; get top offset
      mov
                                                        mov
                                                       sub gs:[0h], 4h add esp, 4h
           gs:[ecx], LRET; push ret dst
                                                                            ; dec stack by 4
      mov
          [eax+4], ID ; comp fptr w/ID
                                                                            ; skip extra ret
      cmp
      jne error_label ; if != fail
                                                                            ; jump return dst
                                                            ecx
                                                        jmp
      call eax
                           ; call func ptr
LRET:
```

CFI with Shadow Call Stack

Push return address to

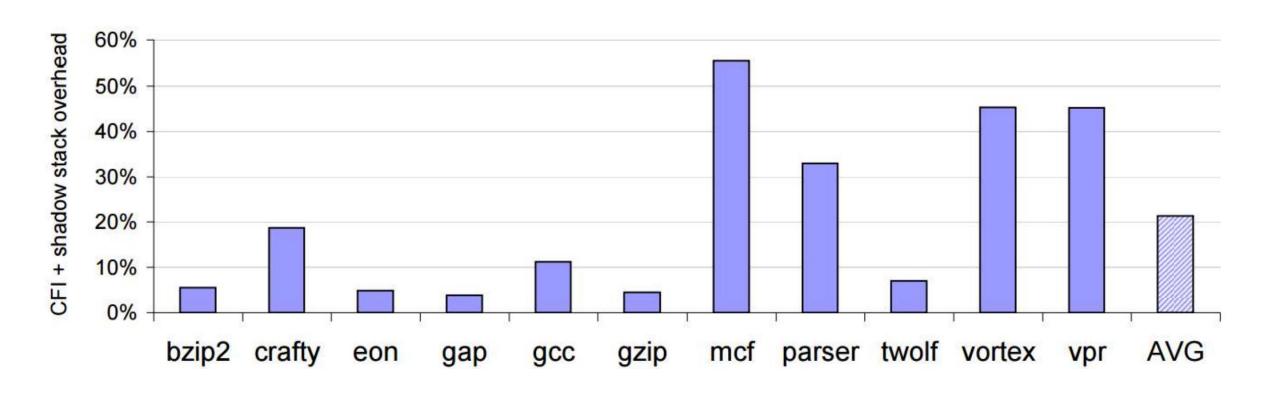
the shadow call stack

```
call eax
                            ; call func ptr
                                                          ret
                                                                                ; return
        with a CFI-based implementation of a protected shadow call stack using hardware segments, can become:
            gs:[0h], 4h
                            ; inc stack by 4
                                                               ecx, gs:[0h]
       add
                                                                                ; get top offset
                                                          mov
                                                               ecx, gs:[ecx]
            ecx, gs:[0h]
                            ; get top offset
                                                                               ; pop return dst
                                                          mov
                                                          sub gs:[0h], 4h
            gs:[ecx], LRET; push ret dst
                                                                                ; dec stack by 4
       mov
            [eax+4], ID
                                                               esp, 4h
                            ; comp fptr w/ID
                                                                                ; skip extra ret
                                                          add
       cmp
                              if != fail
           error_label
                                                                                ; jump return dst
       jne
                                                               ecx
                                                          jmp
       call eax
                            ; call func ptr
LRET: ...
```











- CFI on binary code is difficult
 - -Subtlety of Vulcan

CFI is slow

CFI on Binary: Bypassing CFI

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- Dynamically generated code
 - -Self modifying code (e.g., packing)
 - -JIT compiled code

CFI is not perfect anyways

CFI Practicality: Coarse-Grained CFI

- Practical Control Flow Integrity and Randomization for Binary Executables, *Oakland 2013*
- Control Flow Integrity for COTS binaries, USENIX Security 2013
- Transparent ROP Exploit Mitigation Using Indirect Branch Tracing, USENIX Security 2013
- ROPecker: A Generic and Practical Approach for Defending against ROP attacks, NDSS 2014

Attacking Coarse-Grained CFI

 Stitching the Gadgets: On the Ineffectiveness of Coarse-Grained Control-Flow Integrity Protection, *USENIX Security* 2014

• Size Does Matter: Why Using Gadget-Chain Length to Prevent Code-Reuse Attacks is Hard, *USENIX Security 2014*

Out of Control: Overcoming Control-Flow Integrity, Oakland
 2014

Summary



*

 The CFI security policy dictates that software execution must follow a path of a Control-Flow Graph (CFG) determined ahead of time.

Type confusion bugs happen when a program misuses types

Type confusion allows attackers to trigger memory corruption or disclosure

Use After Free is one of the major causes of type confusion

Question?