

# UTC UNISONIC TECHNOLOGIES CO., LTD

# 2SC1384

## NPN SILICON TRANSISTOR

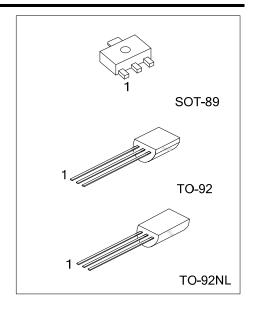
# NPN SILICON TRANSISTOR

#### **DESCRIPTION**

The UTC 2SC1384 is power amplifier and driver.

#### **FEATURES**

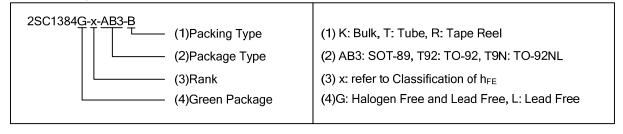
- \* Low V<sub>CE(SAT)</sub>
- \* 2~3W output in complementary pair with 2SA684



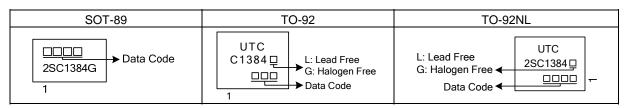
#### ORDERING INFORMATION

Ordering Number		Dookogo	Pin Assignment			De alsie e	
Lead Free	Halogen-Free	Package	1	2	3	Packing	
-	2SC1384G-x-AB3-R	SOT-89	В	С	Е	Tape Reel	
2SC1384L-x-T92-B	2SC1384G-x-T92-B	TO-92	Е	С	В	Tape Box	
2SC1384L-x-T92-K	2SC1384G-x-T92-K	TO-92	Е	С	В	Bulk	
2SC1384L-x-T9N-B	2SC1384G-x-T9N-B	TO-92NL	Е	С	В	Tape Box	
2SC1384L-x-T9N-K	2SC1384G-x-T9N-K	TO-92NL	E	С	В	Bulk	

Note: Pin Assignment: B: Base C: Collector E: Emitter



#### **MARKING**



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### ■ ABSOLUTE MAXIMUM RATINGS (T<sub>A</sub>=25°C, unless otherwise specified)

PARAMETER		SYMBOL	RATINGS	UNIT
Collector-Base Voltage		$V_{CBO}$	60	V
Collector-Emitter Voltage		$V_{CEO}$	50	V
Emitter-Base Voltage		$V_{EBO}$	5	V
Peak Collector Current		I <sub>CP</sub>	1.5	Α
Collector Current (DC)		Ic	1	Α
O. II. ( D (T. 0500)	SOT-89	ם	500	mW
Collector Dissipation (T <sub>A</sub> =25°C)	TO-92/TO-92NL	Pc	1000	mW
Junction Temperature		TJ	125	°C
Operating Temperature		T <sub>OPR</sub>	-20 ~ +85	°C
Storage Temperature		T <sub>STG</sub>	-40 ~ +150	°C

Note: Absolute maximum ratings are those values beyond which the device could be permanently damaged. Absolute maximum ratings are stress ratings only and functional device operation is not implied.

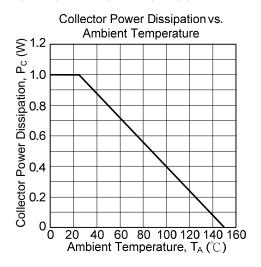
# ■ ELECTRICAL CHARACTERISTICS (T<sub>A</sub>=25°C, unless otherwise specified)

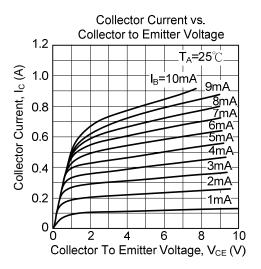
PARAMETER	SYMBOL	TEST CONDITIONS	MIN	TYP	MAX	UNIT
Collector-Base Breakdown Voltage	$BV_CBO$	$I_C=10\mu A, I_E=0$	60			V
Collector-Emitter Breakdown Voltage	$BV_CEO$	I <sub>C</sub> =2mA, I <sub>B</sub> =0	50			V
Emitter-Base Breakdown Voltage	$BV_{EBO}$	$I_E=10\mu A, I_C=0$	5			V
Collector Cut-Off Current	I <sub>CBO</sub>	V <sub>CB</sub> =20V, I <sub>E</sub> =0			0.1	μА
DC Current Gain	h <sub>FE1</sub>	V <sub>CE</sub> =10V, I <sub>C</sub> =500mA	85	160	340	
	h <sub>FE2</sub>	$V_{CE}=5V$ , $I_{C}=1A$	50	100		
Collector-Emitter Saturation Voltage	$V_{CE(SAT)}$	I <sub>C</sub> =0.5A, I <sub>B</sub> =50mA		0.2	0.4	V
Base-Emitter Saturation Voltage	$V_{BE(SAT)}$	I <sub>C</sub> =0.5A, I <sub>B</sub> =50mA		0.85	1.2	V
Current Gain Bandwidth Product	$f_T$	V <sub>CE</sub> =10V, I <sub>B</sub> =50mA		200		MHz
Output Capacitance	$C_OB$	V <sub>CB</sub> =10V, I <sub>E</sub> =0, f=1MHz		11	20	pF

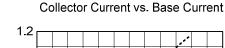
# ■ CLASSIFICATION OF h<sub>FE</sub>

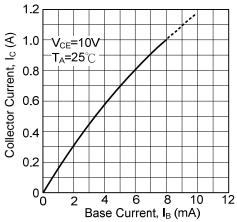
RANK	Q	R	S
RANGE	85-170	120-240	170-340

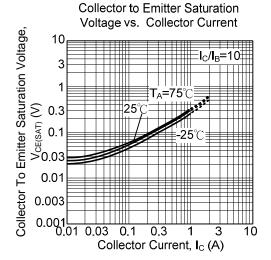
#### TYPICAL CHARACTERISTICS

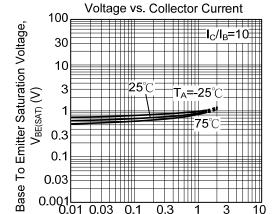












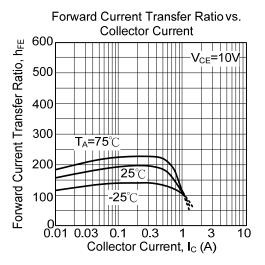
0.1

0.3

Collector Current, Ic (A)

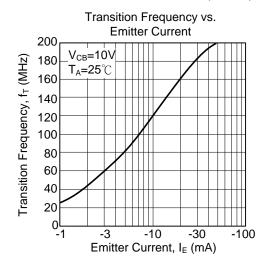
10

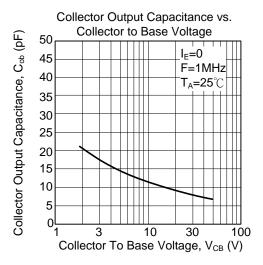
Base to Emitter Saturation

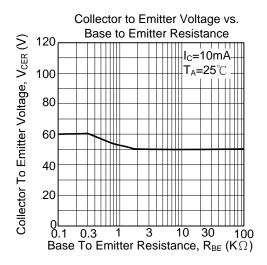


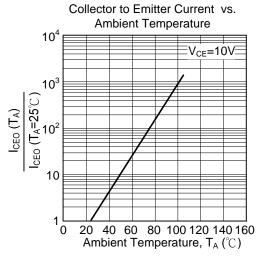
0.03

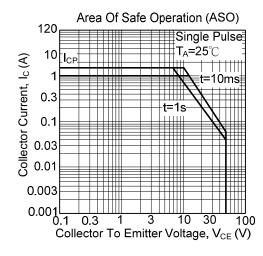
■ TYPICAL CHARACTERISTICS(Cont.)











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