Look-Ahead SLP: Auto-vectorization in the presence of commutative operations

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EuroLLVM 2018













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 - Superword Level Parallelism [Larsen PLDI'00]
 - Bottom-Up SLP







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- SLP and loop-vectorizer complement each other:
 - Even if loop vectorizer fails, SLP could partly succeed
- Compilers usually run SLP after the Loop Vectorizer







Why commutative operations matter?

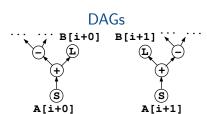
- Operands can be reordered
- It can change the shape of the DAG
- Since SLP operates on DAGs, it affects the SLP's vectorization effectiveness

```
sub1 = ... - ...;

sub2 = ... - ...;

A[i+0] = sub1 + B[i+0];

A[i+1] = B[i+1] + sub2;
```









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 - 2 Load address mismatch further up the graph

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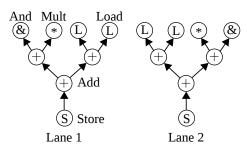


- SLP reordering not effective for:
 - 1 Opcode mismatch further up the graph
 - 2 Load address mismatch further up the graph
 - Reodering across chains of commutative operations*
- Look-Ahead SLP (LSLP) provides a solution to all three.







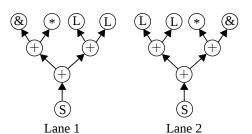


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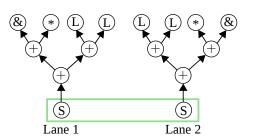












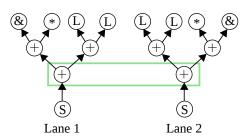












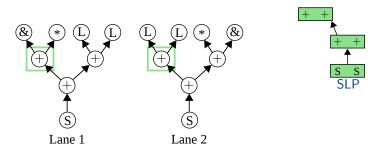


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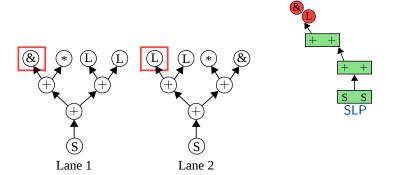










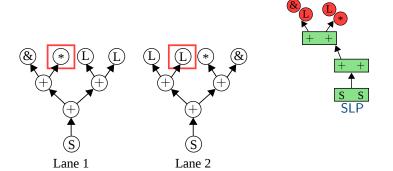










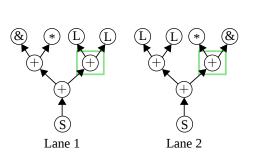


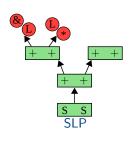








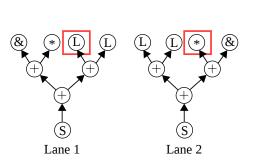


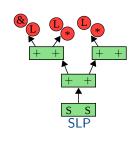












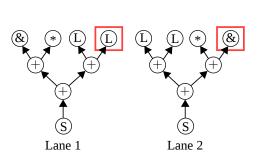


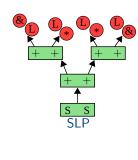
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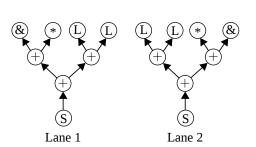


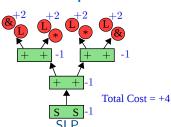








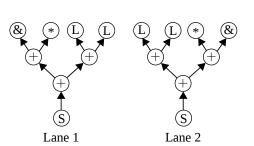


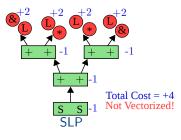








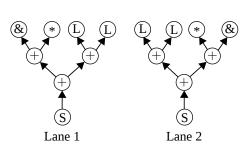


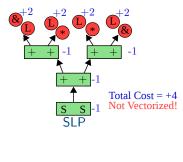


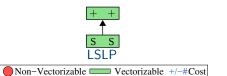








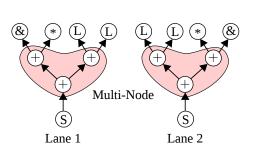


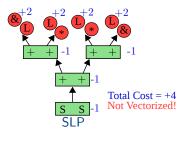


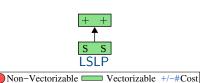








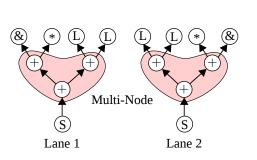


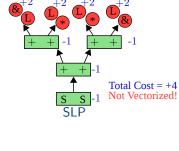


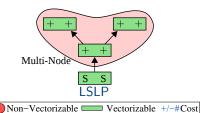








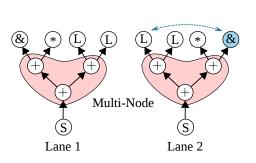


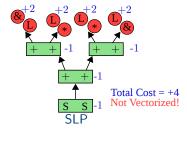


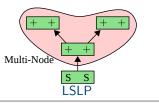








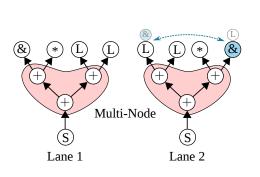


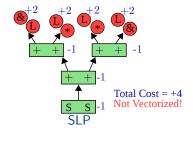


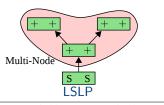








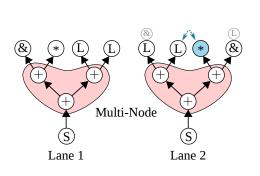


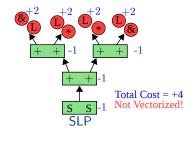


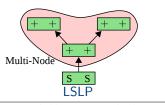








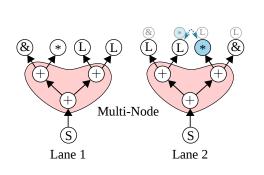


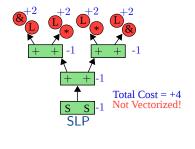


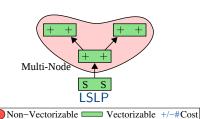








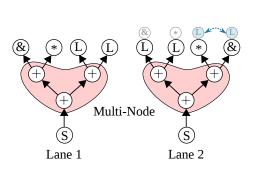


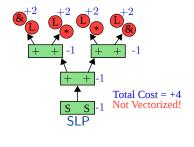


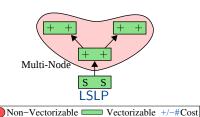








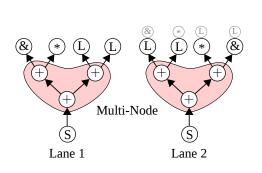


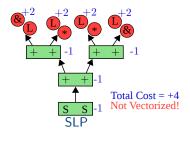


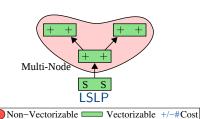








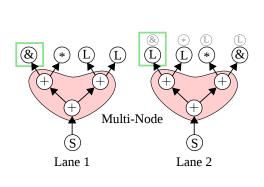


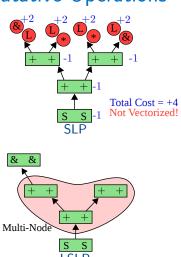










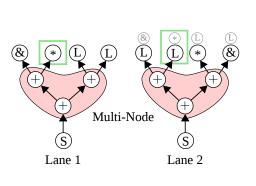


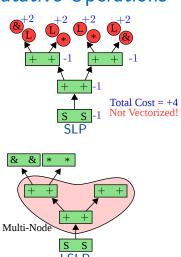
Non-Vectorizable Vectorizable +/-#Cost







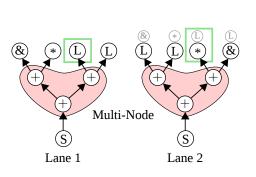


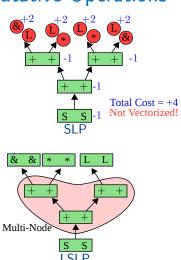








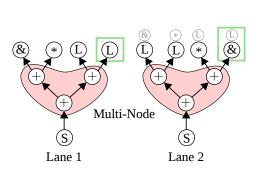


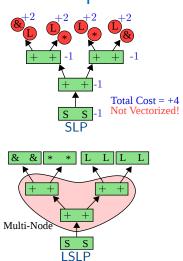










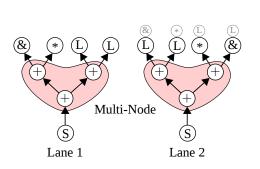


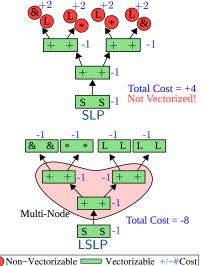
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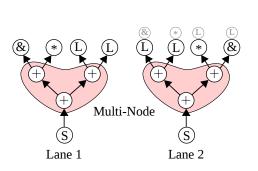


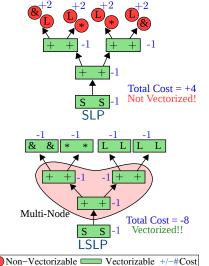


















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- Kernels from SPEC CPU2006
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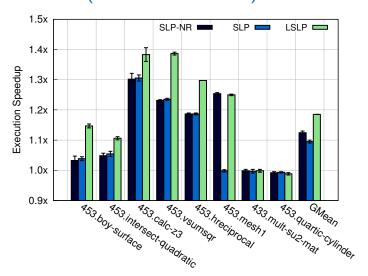
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 - 4 O3 + LSLP enabled (LSLP)







Performance (normalized to O3)









Conclusion

- LSLP introduces an effective scheme for dealing with commutative operations.
- Look-Ahead SLP: Auto-vectorization in the presence of commutative operations (CGO 2018)