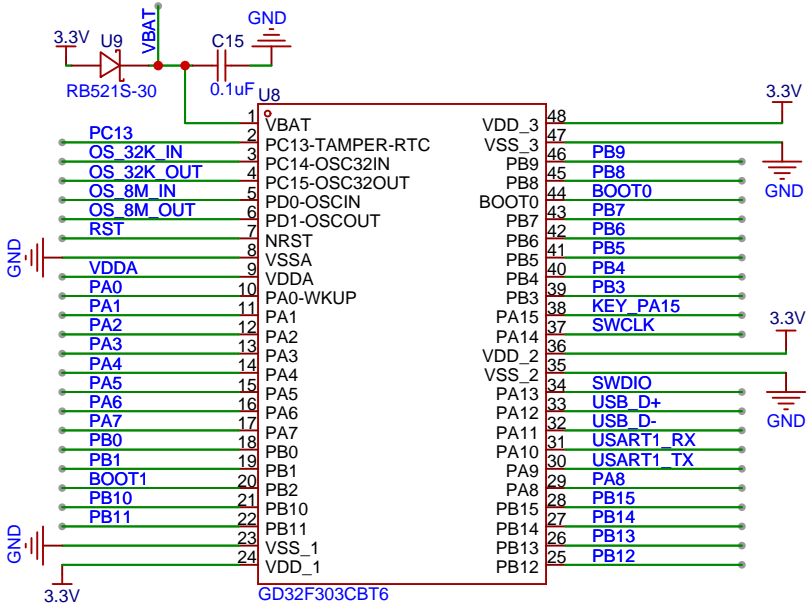
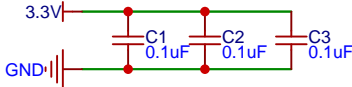


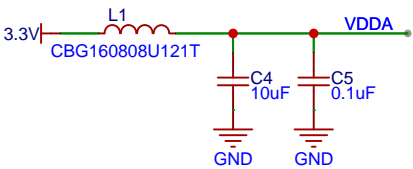
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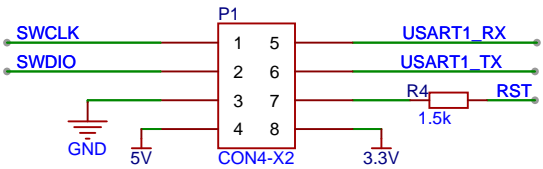
滤波电容



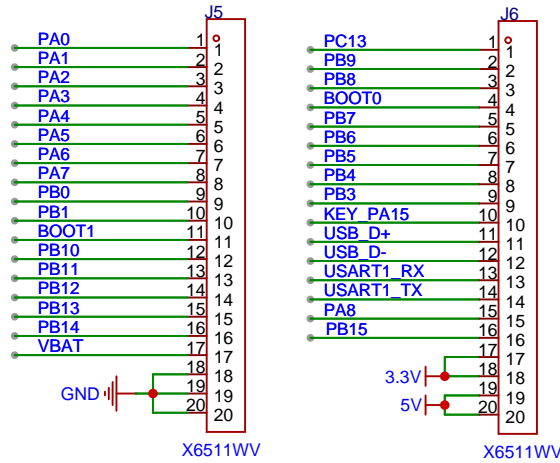
模拟电压



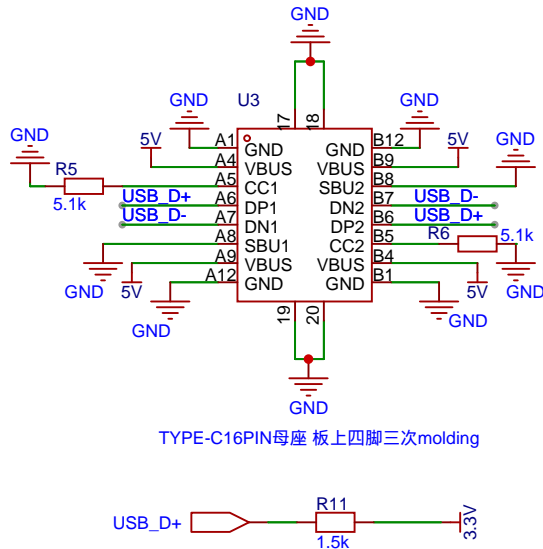
SWD和USART1接口



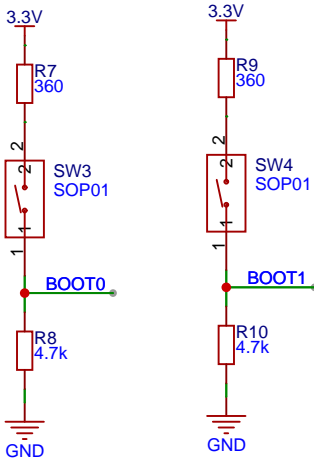
IO口引出



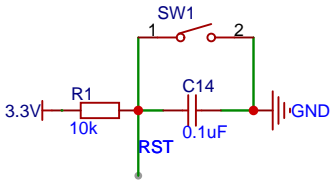
USB接口电路



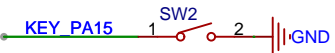
BOOT电路



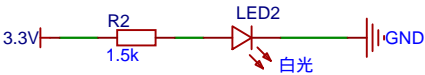
复位电路



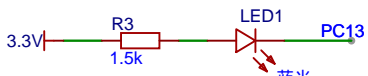
用户按键



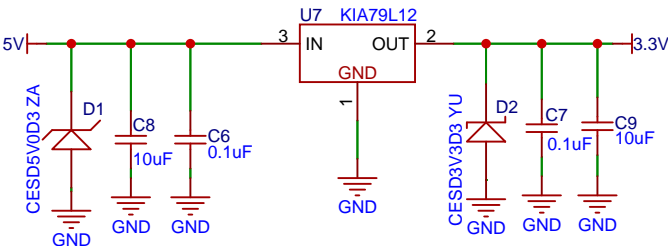
电源指示LED



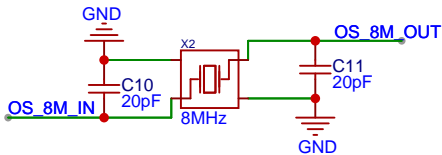
用户LED



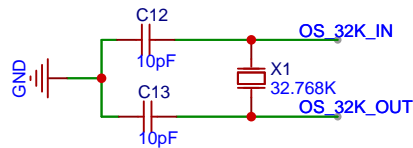
3.3V电源电路



主时钟晶振



RTC时钟晶振



原理图		更新日期	
		创建日期	
图页		物料编码	
绘制			
审阅			
		版本	尺寸
		页	共
嘉立创EDA			