



ARCHITECTURE

SYSTEM ARCHITECTURES

AMBA Specifications

The AMBA specifications define the on-chip interfaces and protocols for use in applications across multiple market areas.

AMBA 5 is the latest generation of specifications and includes two key AMBA protocols: CHI and AXI.

AMBA 5

- [AMBA 5 CHI Architecture Specification](#)
- [AMBA AXI and ACE Protocol Specification](#)
- [AMBA AHB Protocol Specification](#)
- [AMBA AXI-Stream Protocol Specification](#)
- [AMBA APB Protocol Specification](#)
- [AMBA ATB Protocol Specification](#)
- [AMBA CXS Protocol Specification](#)

- [AMBA Adaptive Traffic Profiles Specification](#)
- [AMBA Generic Flash Bus Protocol Specification](#)
- [AMBA DTI Protocol Specification](#)
- [AMBA LTI Protocol Specification](#)
- [AMBA Low Power Interface Specification](#)

AMBA 4

- [AMBA AXI and ACE Protocol Specification](#)
- [AMBA 4 AXI4-Stream Protocol Specification](#)
- [AMBA 4 ATB Protocol Specification](#)
- [AMBA APB Protocol Specification](#)
- [AMBA Low Power Interface \(Arm Q-Channel and P-Channel Interfaces\)](#)

AMBA 3

- [AMBA AXI Protocol Specification](#)
- [AMBA 3 AHB-Lite Protocol Specification](#)
- [AMBA APB Protocol Specification](#)
- [AMBA 3 ATB Protocol Specification](#)
- [Multi-layer AHB Overview Specification](#)