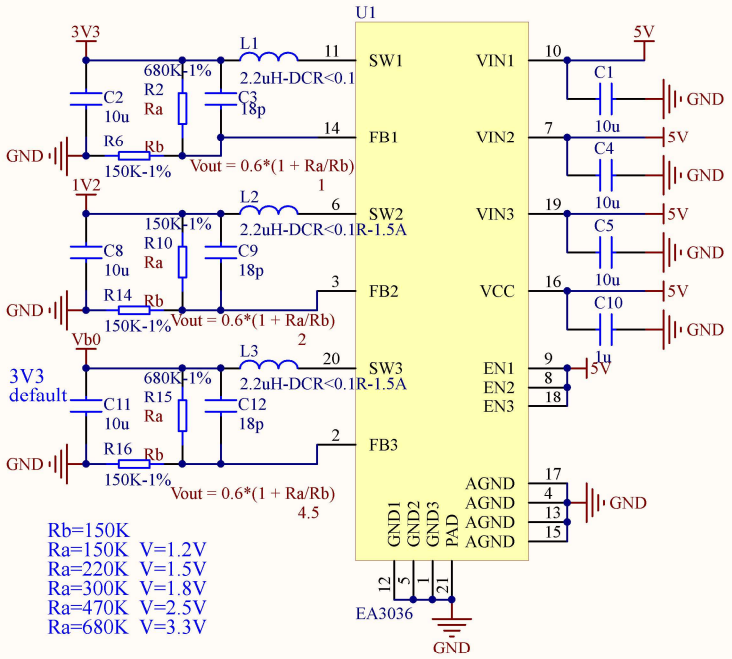
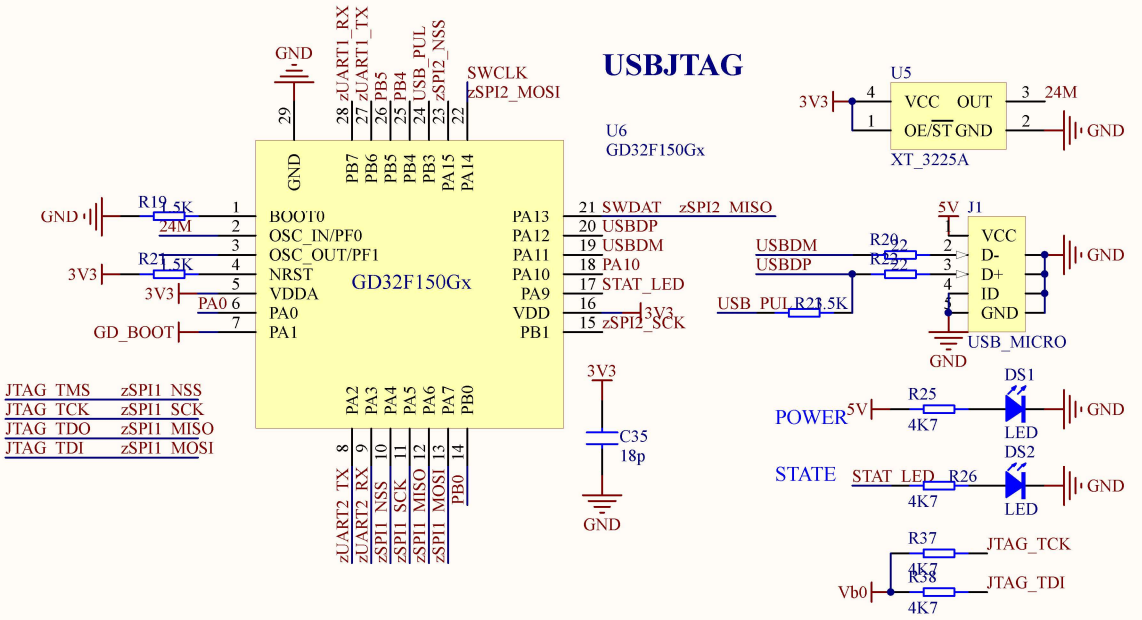
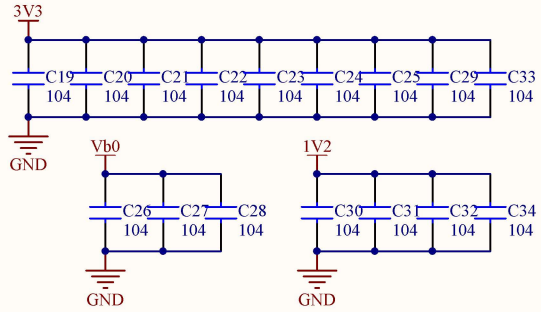


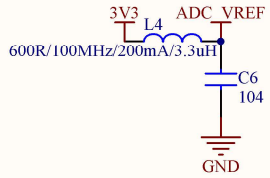


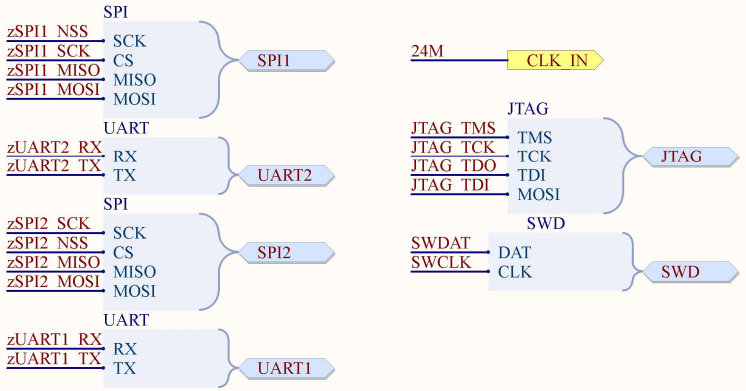
Power

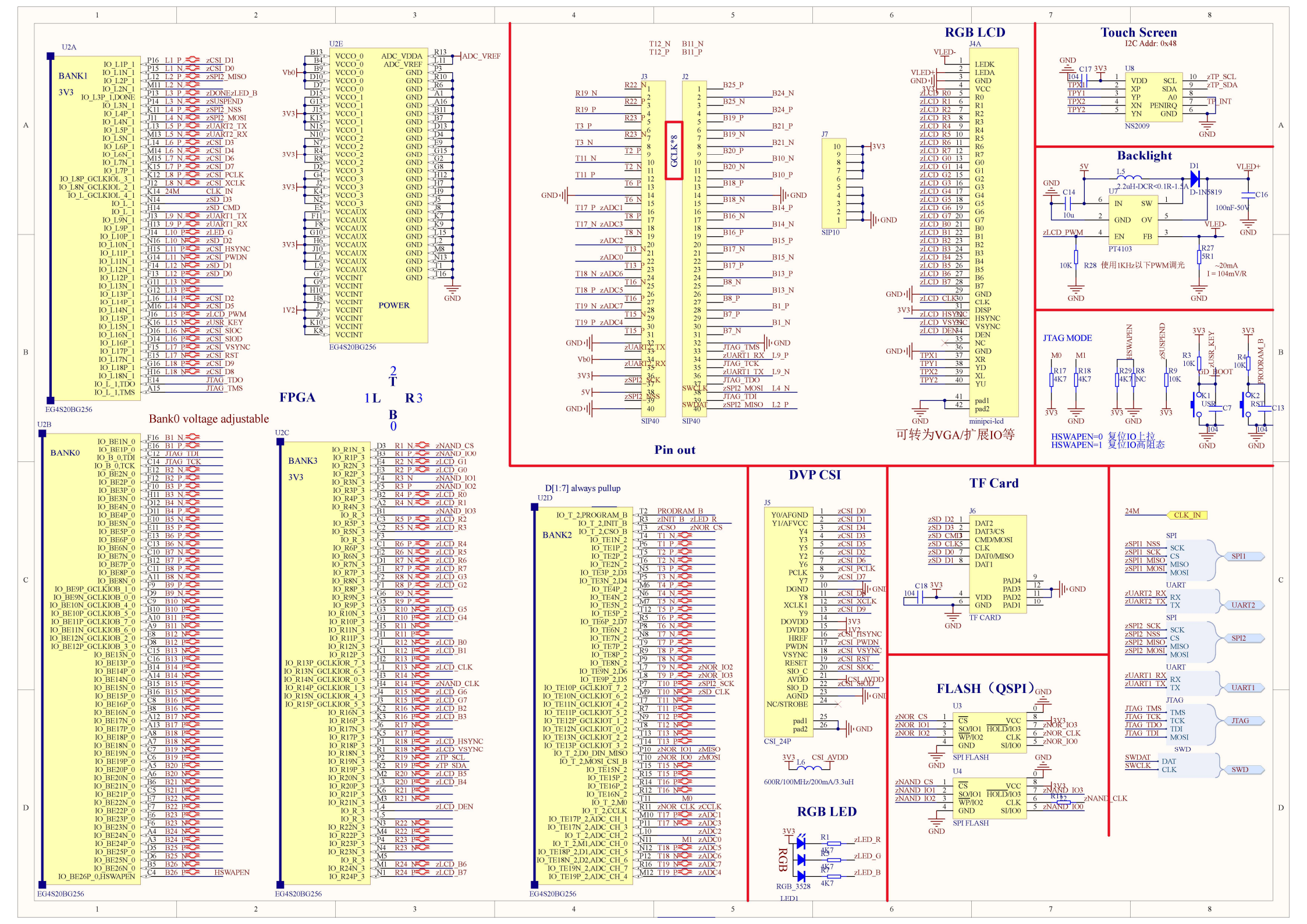
3-CH DCDC



Fliter






U2A
BANK1
3V3
EG4S20BG256
U2E
POWER
EG4S20BG256
FPGA
2
T
1L
R3
B
0
Bank0 voltage adjustable
U2B
BANK0
EG4S20BG256
U2C
BANK3
3V3
EG4S20BG256
Pin out
J3
J2
GCLK*8
SIP40
SIP40
J7
SIP10
RGB LCD
J4A
minipci-led
可转为VGA/扩展IO等
Touch Screen
I2C Addr: 0x48
U8
NS2009
Backlight
U7
PT4103
D-1N5819
R28 使用1KHz以下PWM调光
~20mA
I = 104mV/R
JTAG MODE
HSWAPEN=0 复位IO上拉
HSWAPEN=1 复位IO高阻态
D[1:7] always pullup
U2D
BANK2
EG4S20BG256
DVP CSI
J5
CSI_24P
600R/100MHz/200mA/3.3uH
RGB LED
RGB_3528
LED1
TF Card
J6
TF CARD
FLASH（QSPI）
U3
U4
SPI FLASH
SPI FLASH
24M
CLK_IN
SPI
UART
UART
JTAG
SWD