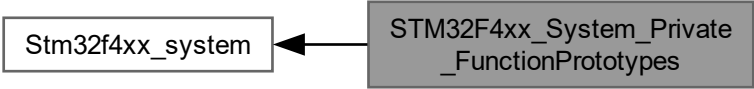


Stm32f4xx\_system

STM32F4xx\_System\_Private  
\_FunctionPrototypes



```
graph LR; A[STM32F4xx_System_Private  
_FunctionPrototypes] --> B[Stm32f4xx_system]
```