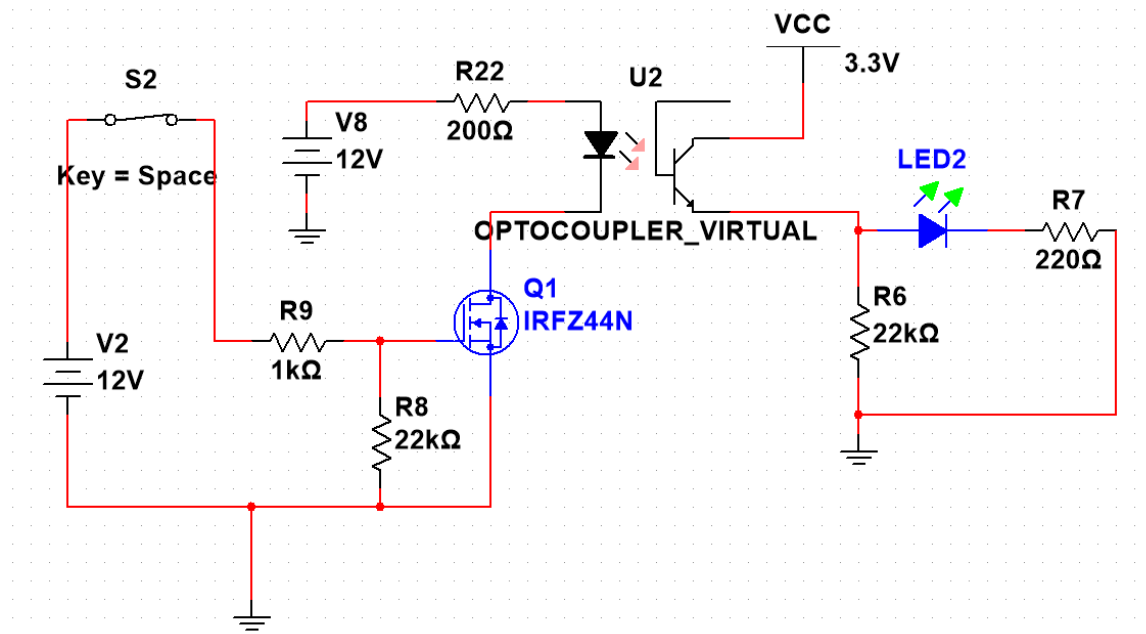


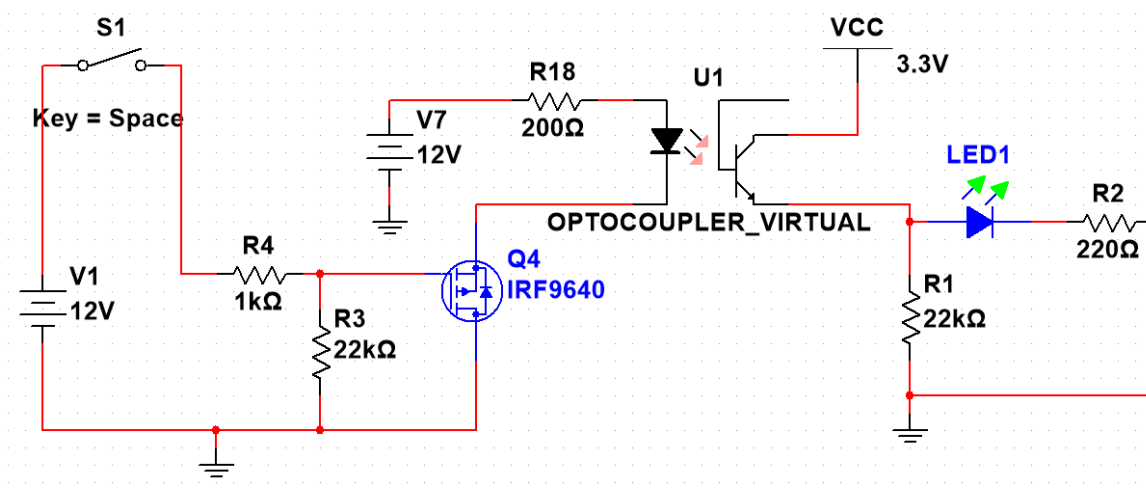
Investigación de los esquemas electrónicos de las funcionalidades que necesitamos

Entradas

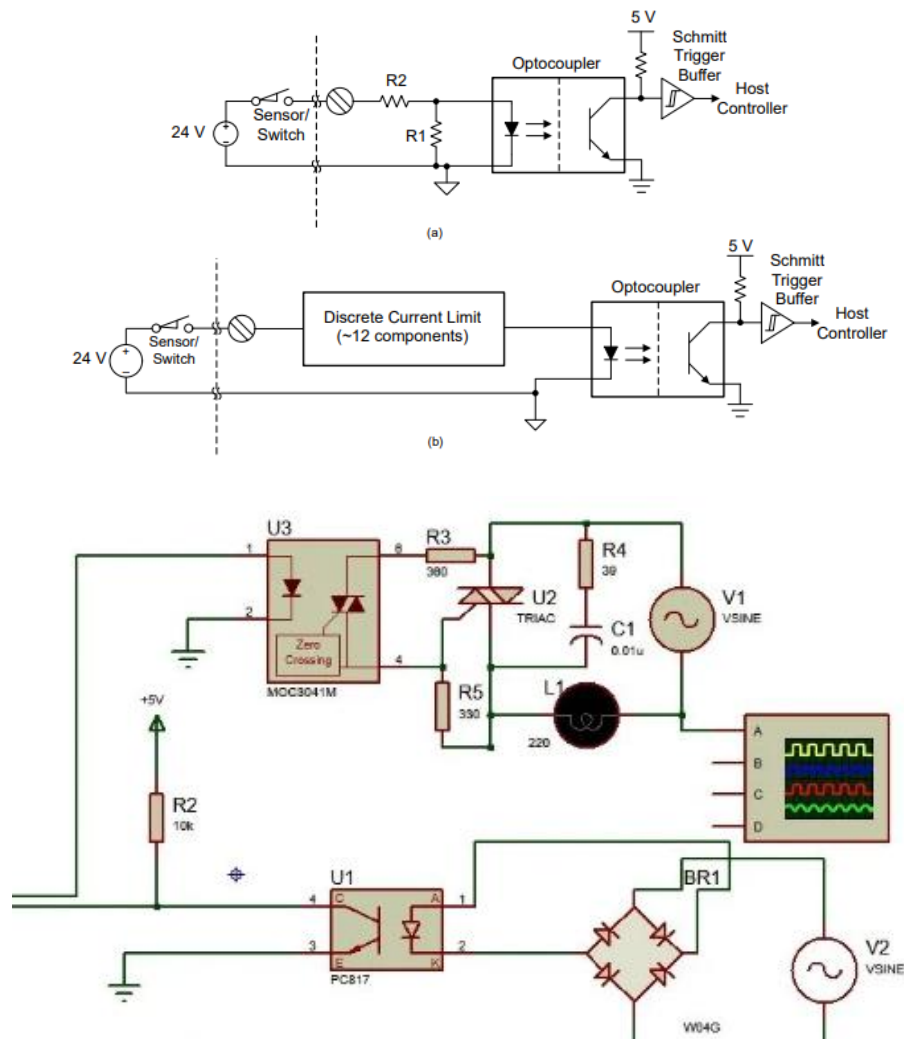
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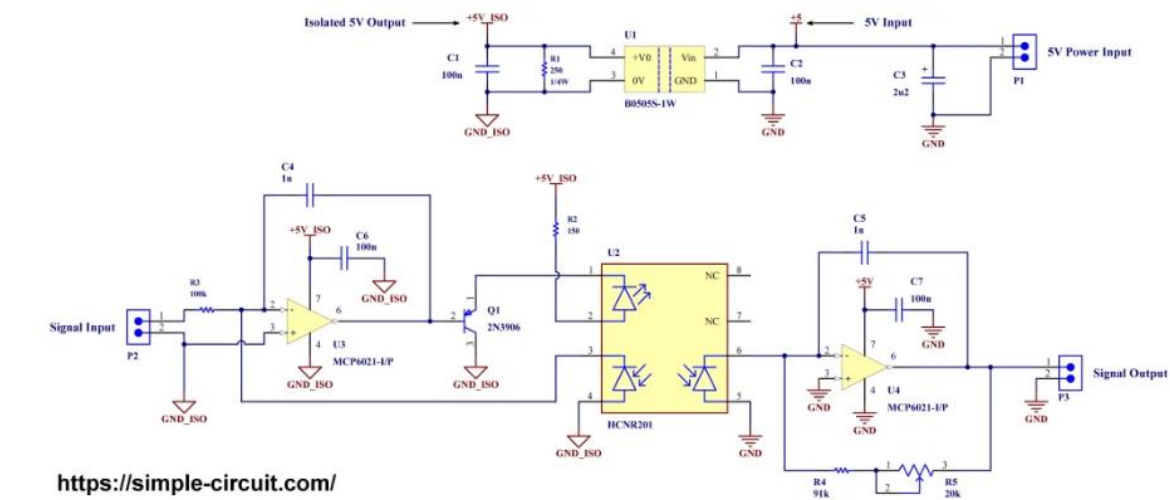
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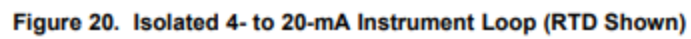
0-32V

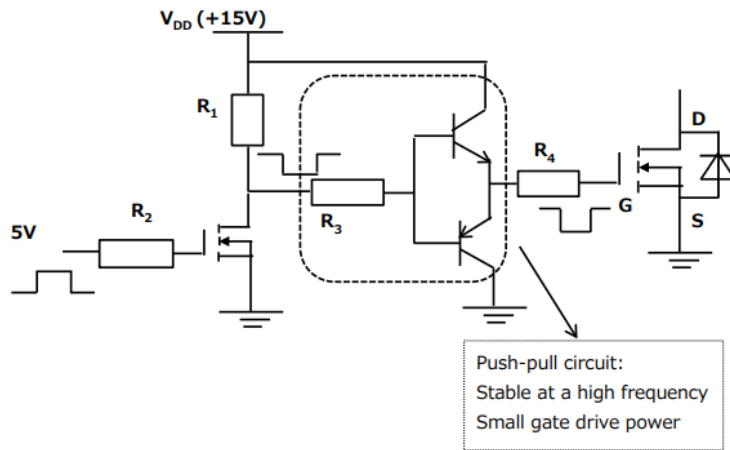


0-10V



Salidas





PNP

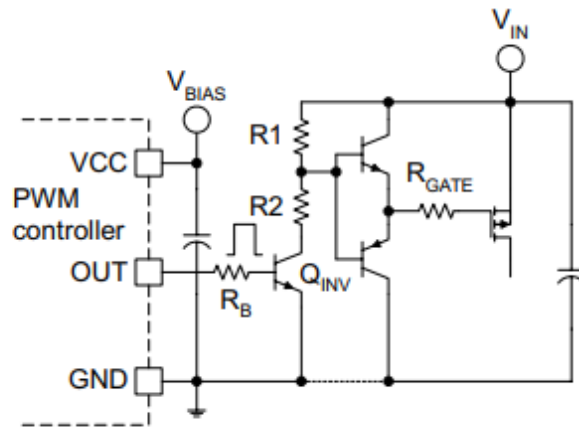


Figure 20. Level-Shifted P-Channel MOSFET Driver

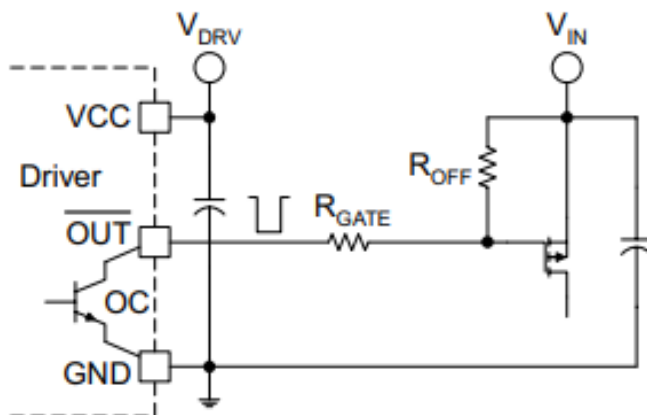
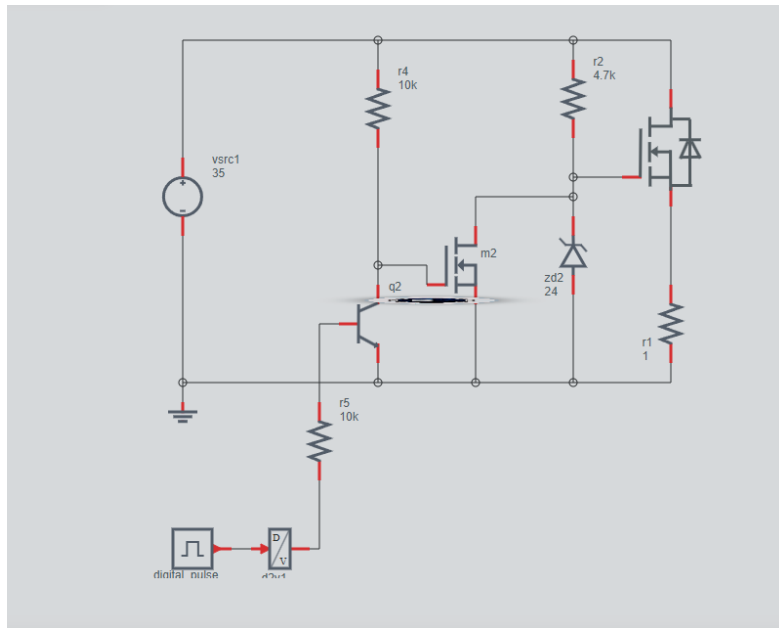
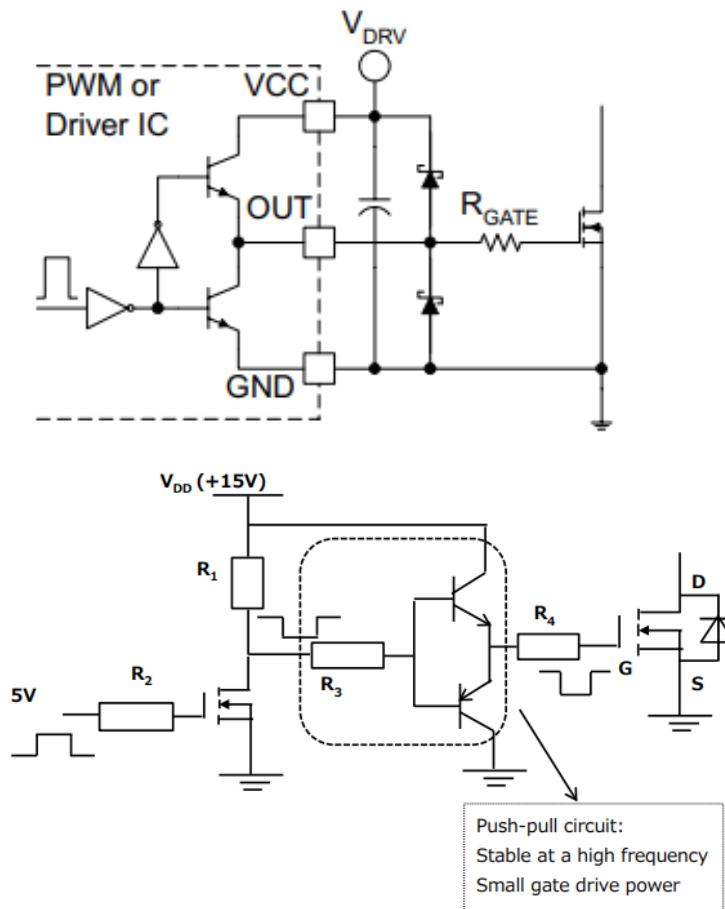


Figure 19. Open Collector Drive for PMOS Device

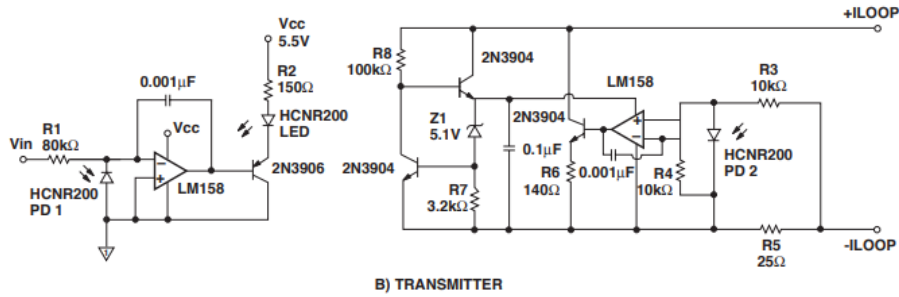
0-32



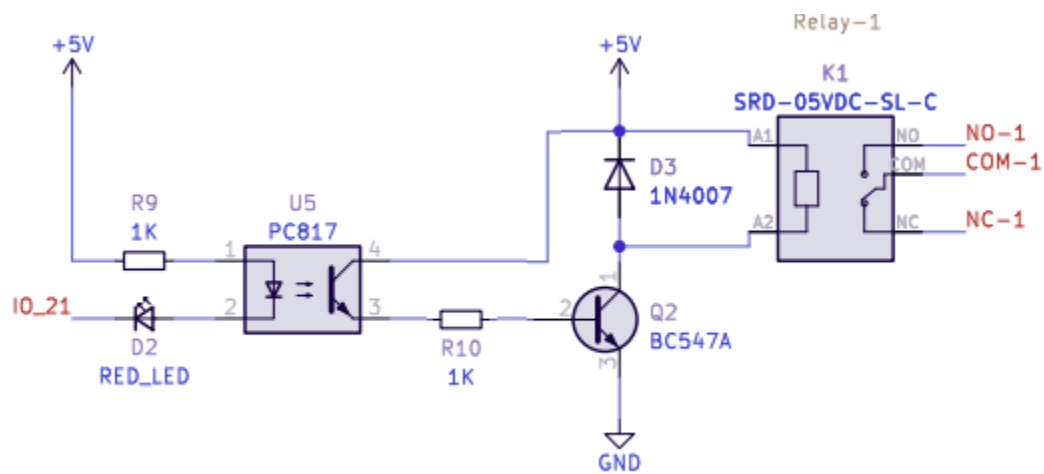
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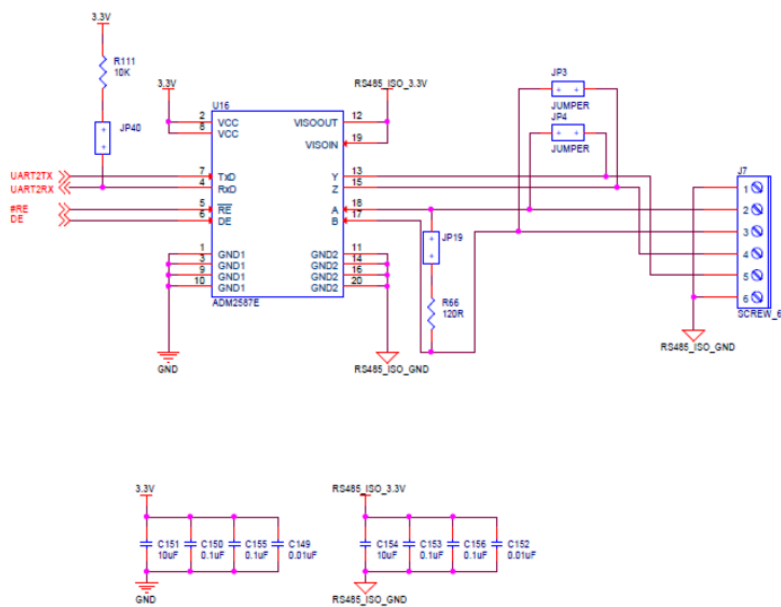
4-20mA



Relé



RS-485



Fuentes

Salidas NPN y PNP

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Relé

<https://github.com/leomanangka/esp32-4-channel-relays/tree/main>