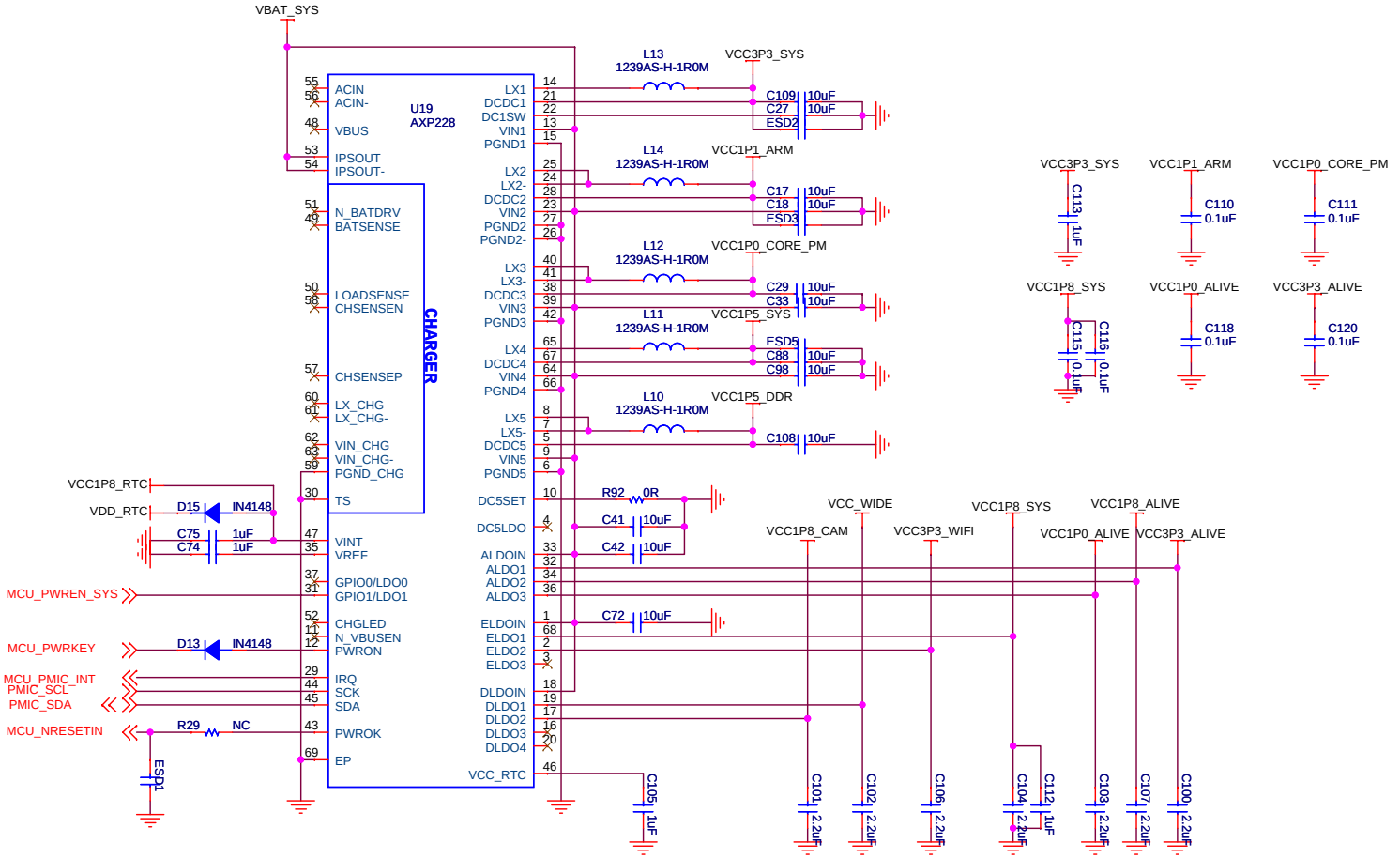

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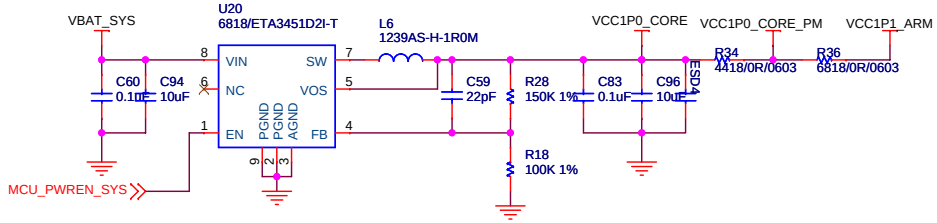

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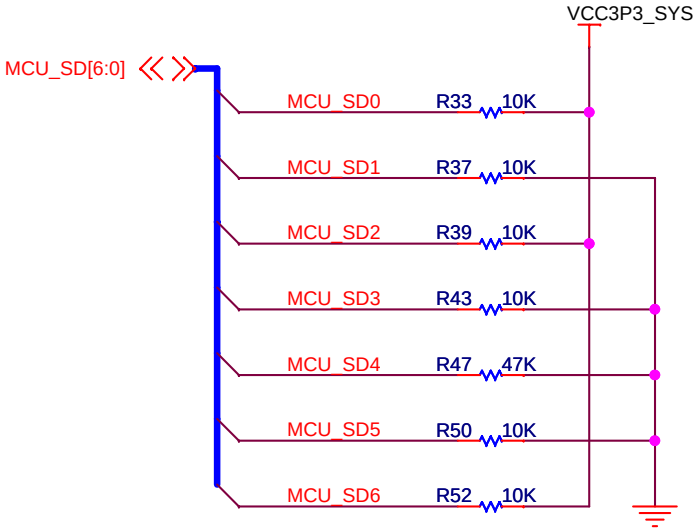

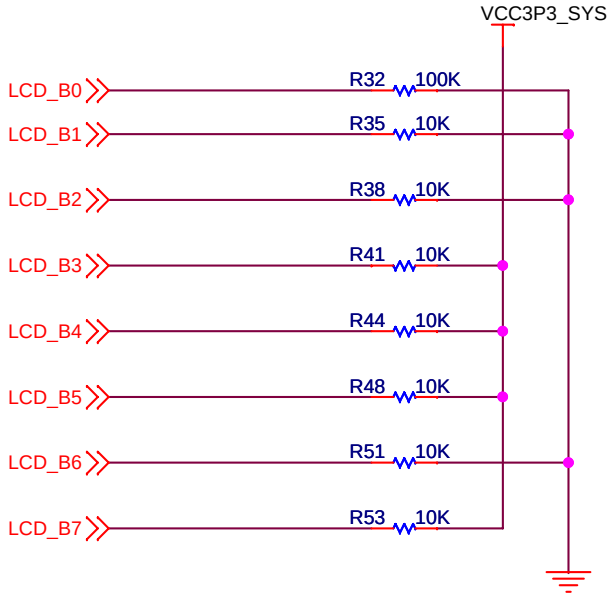

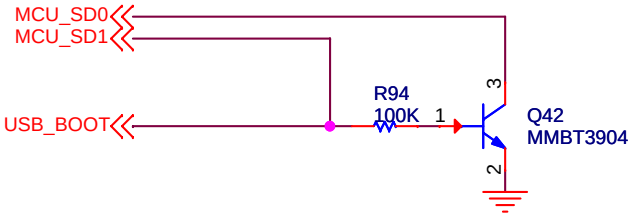

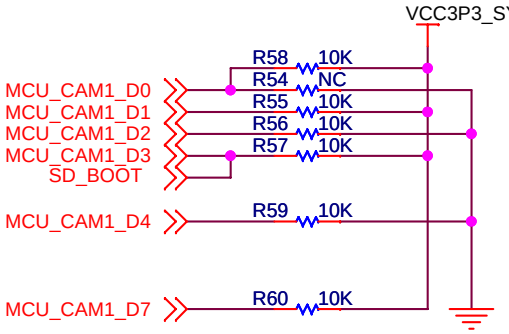

BOOT MODE OPTION

	eMMC	SPI	USB	NAND
MCU_SD0	HIGH	LOW	LOW	HIGH
MCU_SD1	LOW	LOW	HIGH	HIGH
MCU_SD2	HIGH	HIGH	HIGH	HIGH
MCU_SD4	LOW	HIGH		
MCU_SD5	LOW	LOW		

Boot media port select (SPI, eMMC)

	CH0	CH1	CH2
MCU_SD3	LOW	HIGH	LOW
MCU_CAM1_D3	LOW	LOW	HIGH

BOOT MODE CONTROL

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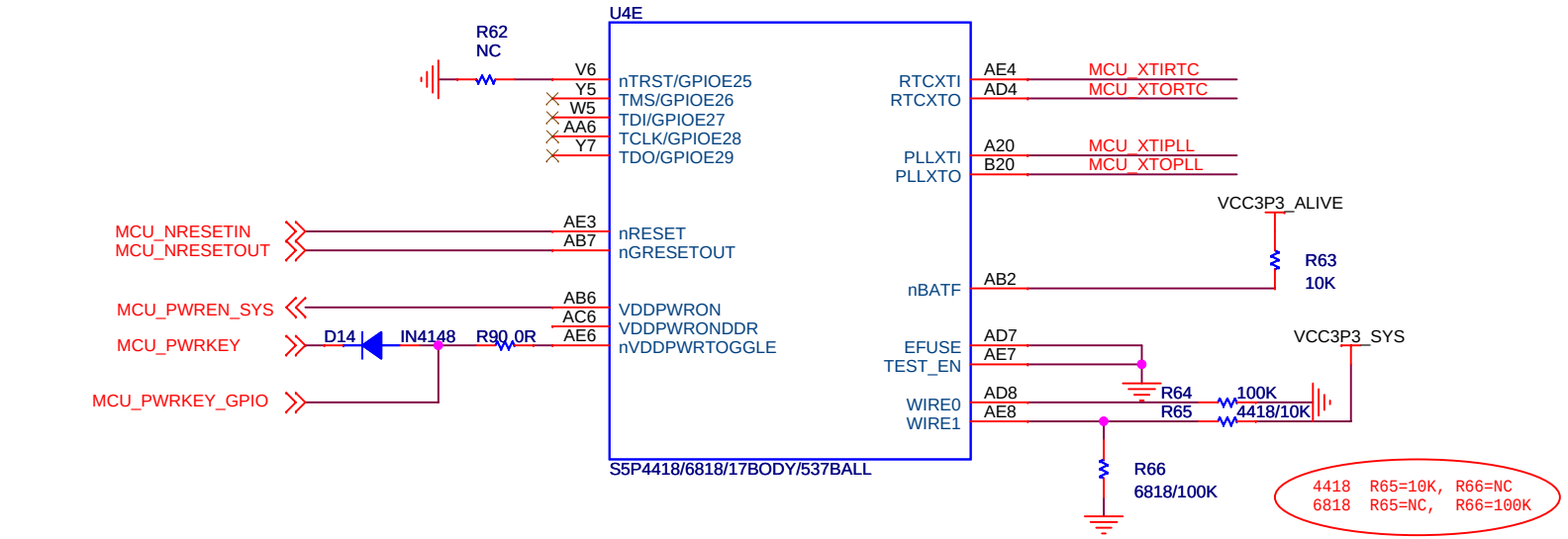
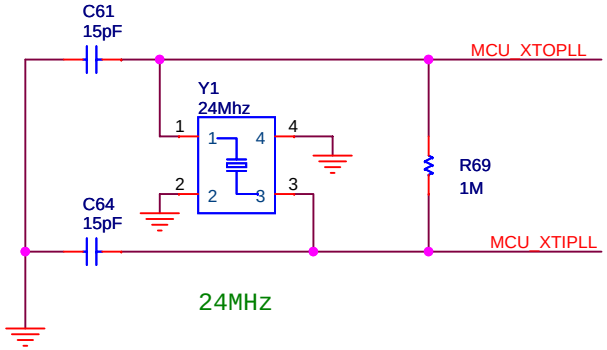
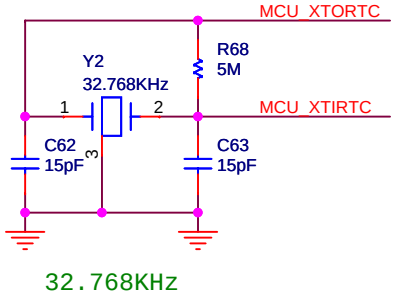
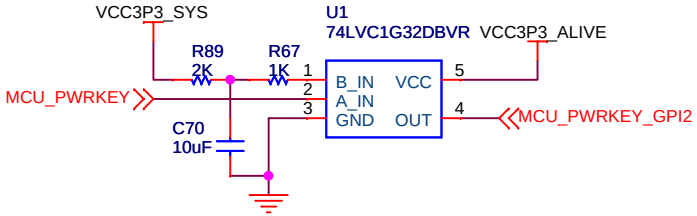
S5P4418 - SYSTEM CONTROL









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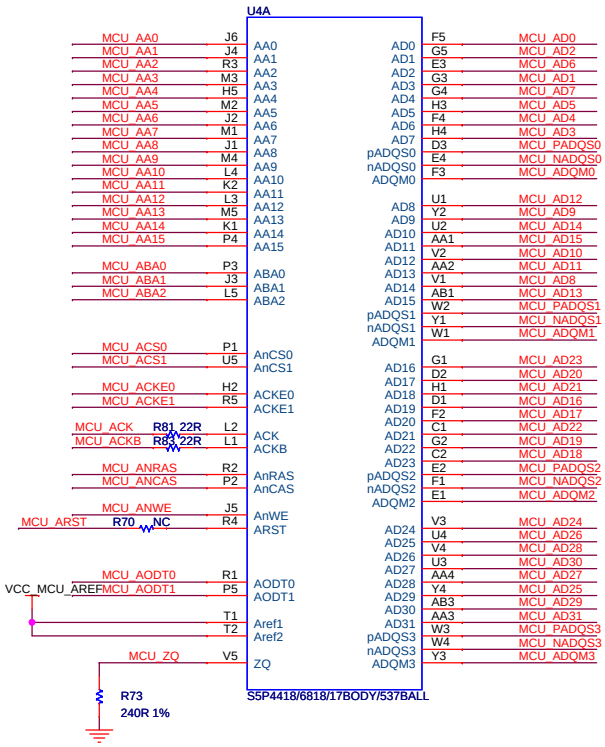
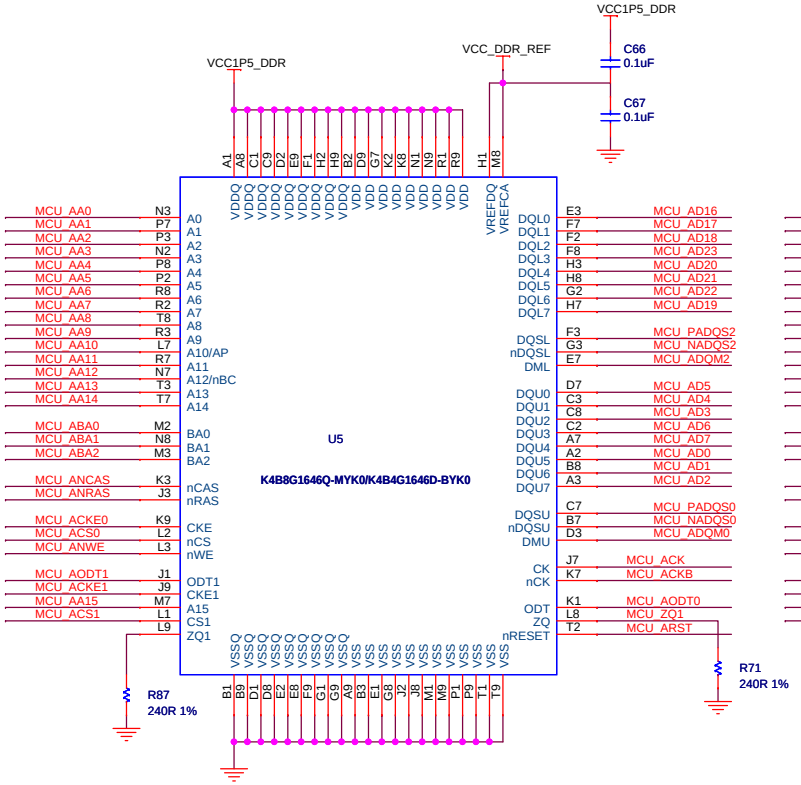
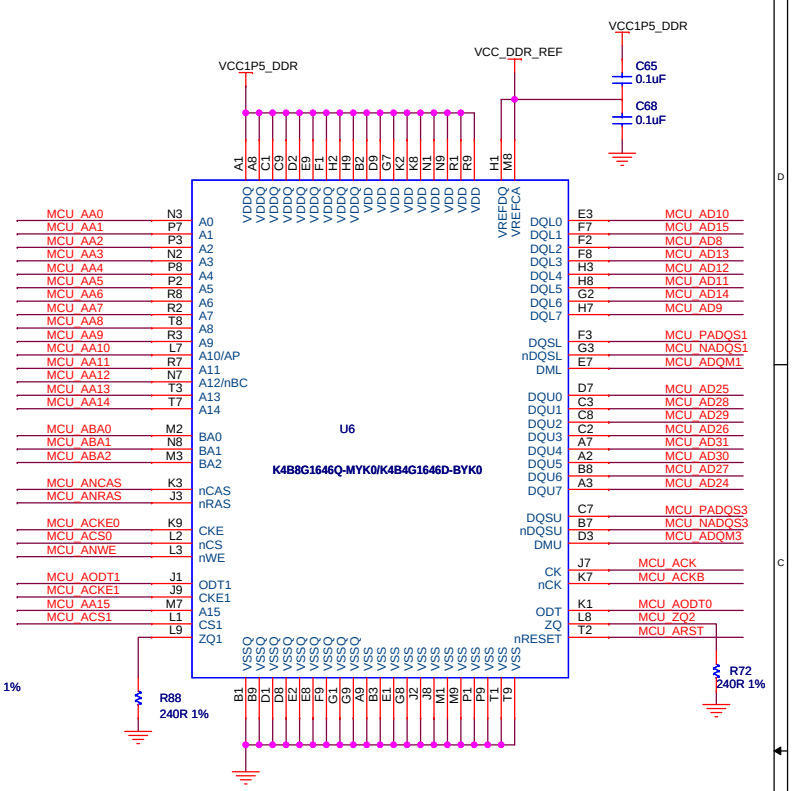
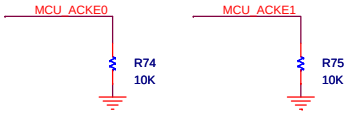
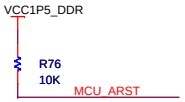
S5P4418 - DDR3

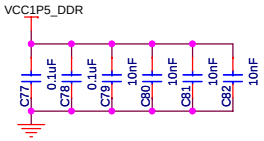


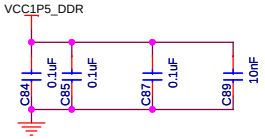


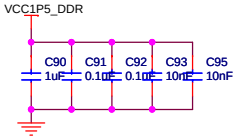


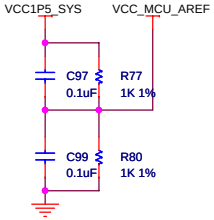


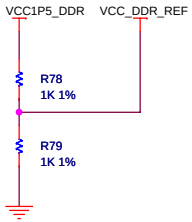












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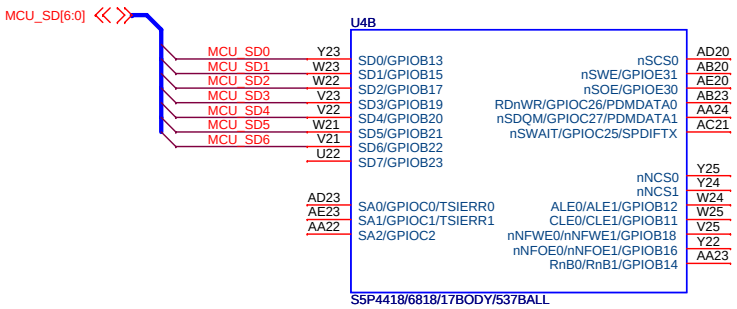
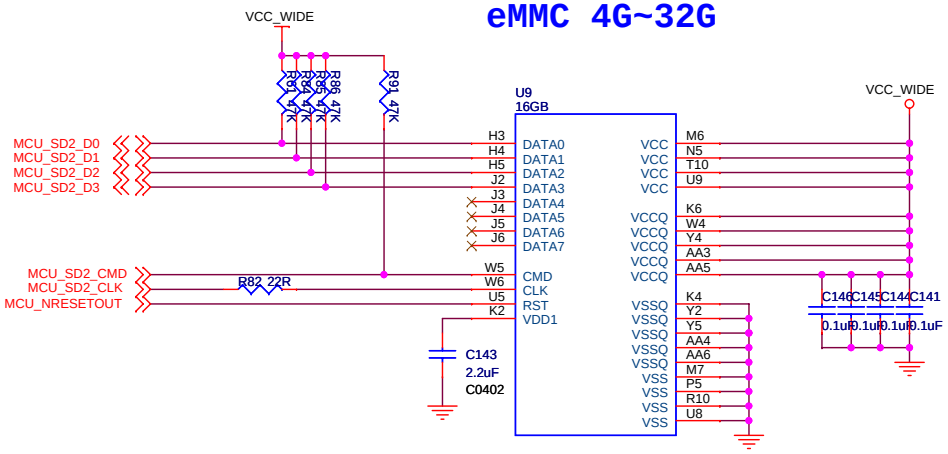


eMMC 4G~32G



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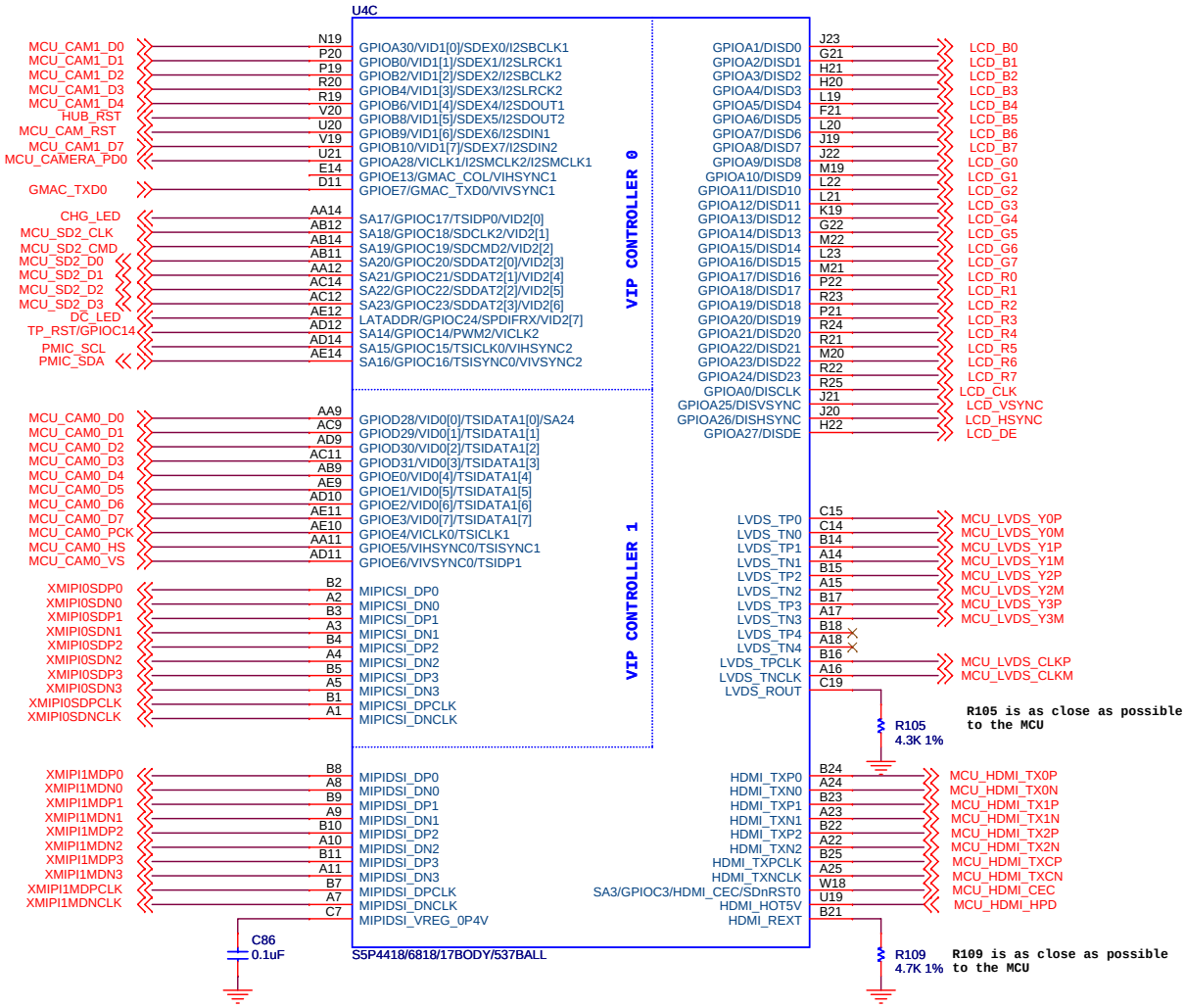
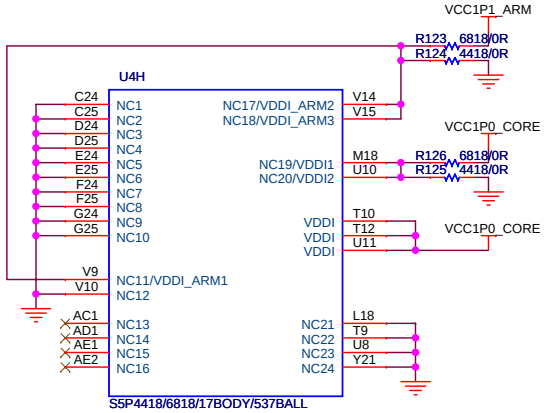
S5P4418 - DISPLAY & CAMERA



Reserved Function

4418 R124=0R, R125=0R, R123,R126,NC
6818 R123=0R, R126=0R, R124,R125,NC



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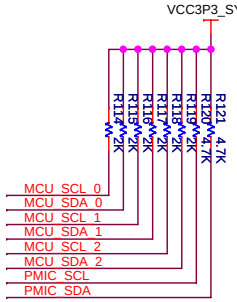
S5P4418 - PERIPHERALS

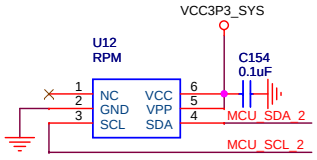


I2C CH0 : Audio, HDMI, Camera

I2C CH1 : Touch

I2C CH2 : G-Sensor RPM ADC

PMIC_I2C : NXE2000 GPIOC15,C16



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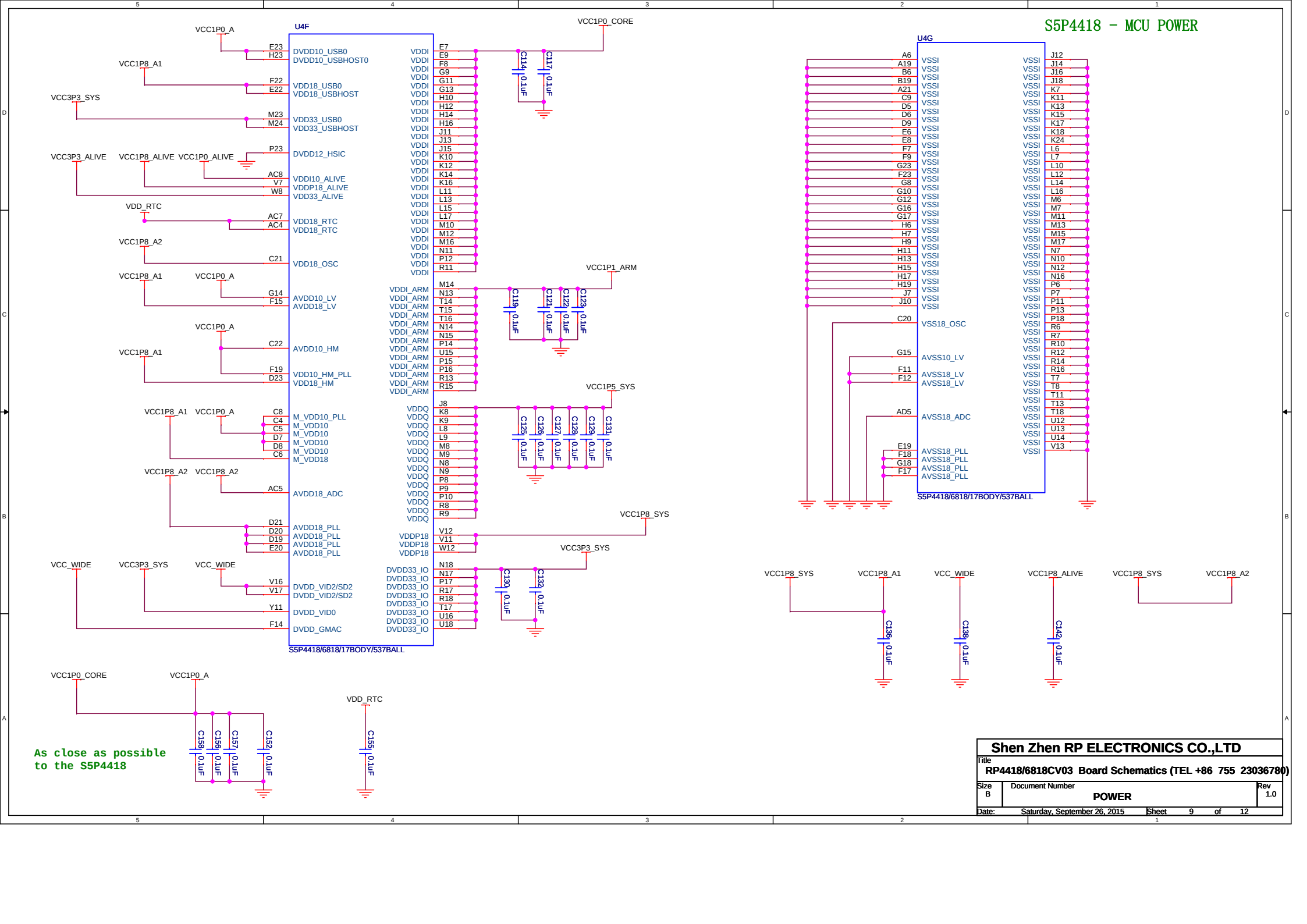

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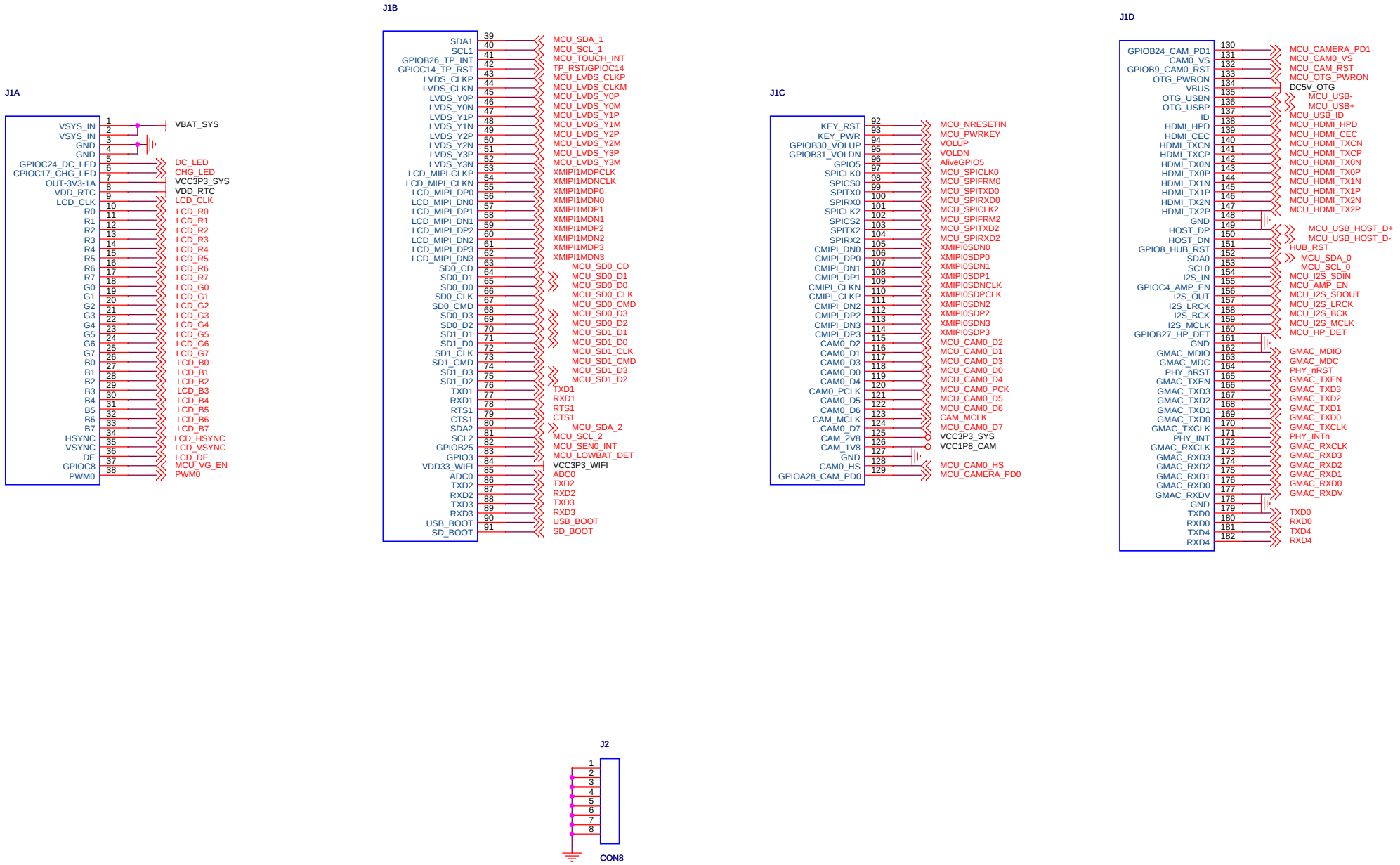

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Size: A3	Document Number: CON	Rev: 1.0
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