

Thanks for checking out this utility.

Feel free to use this as you wish. All I ask is that if you are able to produce Bitstreams, or have your own private Bitstreams, you consider sharing them with me, by way of appreciation for my work.

If you have any issues with this utility, then please get in touch via Discord (@Chris P#5854).

Happy Mining
Chris :)

The screenshot shows the 'Bitstream Loader/Launcher - v1.0' application window. It has a menu bar with 'Load Bitstream & Pool', 'Add Configuration', 'Add Pool', 'View Output', and 'About'. The main interface is divided into several sections: 'Choose a Configuration' and 'Choose a Pool Configuration (optional)' at the top, each with a dropdown menu. Below these are input fields for 'Bitstream (optional)', 'Miner', 'Next/Tag (optional)', 'Miner Params (optional)', 'No. FPGAs' (a spinner set to 1), 'Miner Launch Delay' (a spinner set to 1), and 'Window Title'. There are also checkboxes for 'Set Voltage?' (set to 0.75), 'Load Bitstream?', and 'Location of Params' (radio buttons for 'After Miner' and 'At the end'). On the right side, there are fields for 'Pool URL', 'Port', 'Rig Name Format (optional)', and 'Wallet'. At the bottom left, there is a 'Loop Settings' section with radio buttons for 'Use All FPGAs' (selected) and 'Use 1 FPGA'. A 'Launch Miner' button is located at the bottom right. A footer note reads: 'Written by Chris, for the community. Please report any issues via Discord (@Chris P#5854)'.

This is a close-up of the settings section from the application window. It shows the 'No. FPGAs' spinner set to 1, the 'Miner Launch Delay' spinner set to 1, and an empty 'Window Title' text field. Below these is the 'Loop Settings' section with the 'Use All FPGAs' radio button selected and the 'Use 1 FPGA' radio button unselected.

No FPGAs: This value is used in a few ways...

- 1) Controls the number of instances to launch, when Use All FPGAs is set.
- 2) Used for the window title of each miner instance launched.
- 3) Used as a unique identifier for the Rig Name Format, in the pools section.
- 4) When Use 1 FPGA is set, It's used as the single instance window title and rig name format, if used.

Check-out the examples that I've added. You can see that there's a few options for the more complicated command line instructions, like RB2.