**MCU Bus Interface**

* ARM CM4 Processor has 3 busses :
  + I- Bus : used to fetch instructions from flash
  + D-Bus : used to fetch const data from flash
  + S-Bus
* If the instructions are present in between the memory locations 0x0000 0000 to 0x1FFF FFFC then the Cortex Processor will fetch the instructions using I-CODE interface
* If the instructions are present outside of 0x0000 0000 to 0x0x1FFF FFFC then processor fetches the instructions over the system bus.
* If the data is present in between the memory locations 0x0000 0000 to 0x0x1FFF FFFC, then processor fetches the data over D-CODE bis interface.
* If the data is present outside 0x0000 0000 to 0x0x1FFF FFFF memory locations then, the data will be fetched over the system bus.