**Memory Map of the MCU**

* Arm Cortex M4
* The width of the System Bus is 32bits
* The processor can produce 232 of different addresses. (4G)
* 0x0000\_0000 to 0xffff\_ffff
* When the processor produces 0x4002 0000 on the system bus, it refers to the GPIOA registers.

**Questions**

1. What’s the base address of AHB1 BUS peripheral registers?
   1. 0x4002 0000 – 0x4007 FFFF
2. What’s the base address of GPIOA registers?
   1. 0x4002 0000
3. What’s the base address of RCC engine registers of the MCU?
   1. 0x4002 3800
4. What’s the base address of APB1 peripheral registers?
   1. 0x4002 0000
5. What’s the base address Flash memory?
   1. 0x0800 0000
6. What’s the base address of SRAM2?
   1. SRAM1 starts from 0x2000 0000
   2. Size of SRAM1 = X bytes
   3. Base address of SRAM2 = 0x2000 0000 + X
   4. BASE\_ADDRESS\_OF\_SRAM2 = BASE\_ADDRESS\_OF\_SRAM1 + SIZE\_OF\_SRAM1
7. What’s the base address of ADC1 registers?
   1. 0x4001 2000