

 $D_0=x'y'\bar{z}'$ 

 $D_1 = x'y'z$ 

 $D_2 = x'yz'$ 

 $D_3 = x'yz$ 

 $D_4 = xy'z'$ 

 $D_S = xy'z$ 

 $D_6 = xyz'$ 

1 0 1 F

F = x

Buffer

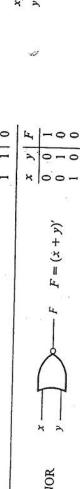
F=x'

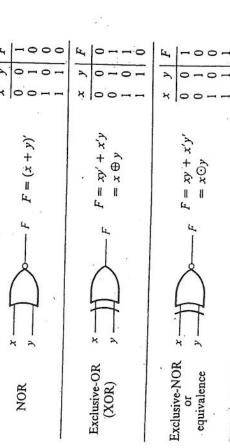
x 00

F = (xy)'

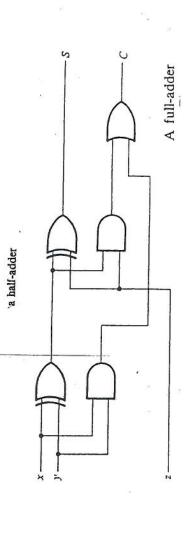
NAND

A 3-to-8 line decoder





(e)  $S = x \oplus y$  C = xy



Digital logic gates