

Lab report format:

Experiment Title

Aim: Study of Digital ICs and basic components like power supply, DSO etc.

Summary of the experiment: Study of basic digital ICs and verifying their functionality, learning the usage of function generator (FG) and digital storage oscilloscope (DSO), and rigging up of circuits.

Components used: IC 7400, 7408, 7432, 7486, 1Kohm resistor array, DIP switches, LED displays, breadboard, power supply.

Design Procedure: Truth table/Excitation table of all logic gates.

Circuit diagram: Logic circuits of gates.

HDL code (for CPLD experiments):

Circuit and Simulation (for CPLD experiments) Snapshots :

Results and Discussions:

Conclusion:
