

Design Procedure

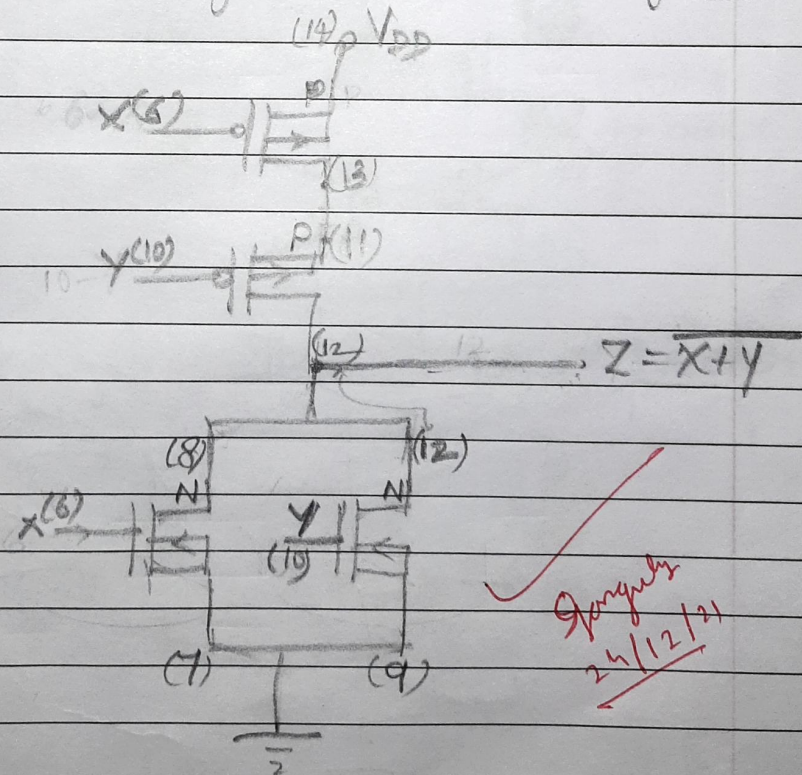
II. (A)

NOR: Truth Table

X	Y	$\overline{X+Y} = (X \text{ NOR } Y)$
0	0	1
0	1	0
1	0	0
1	1	0

In CMOS Logic,

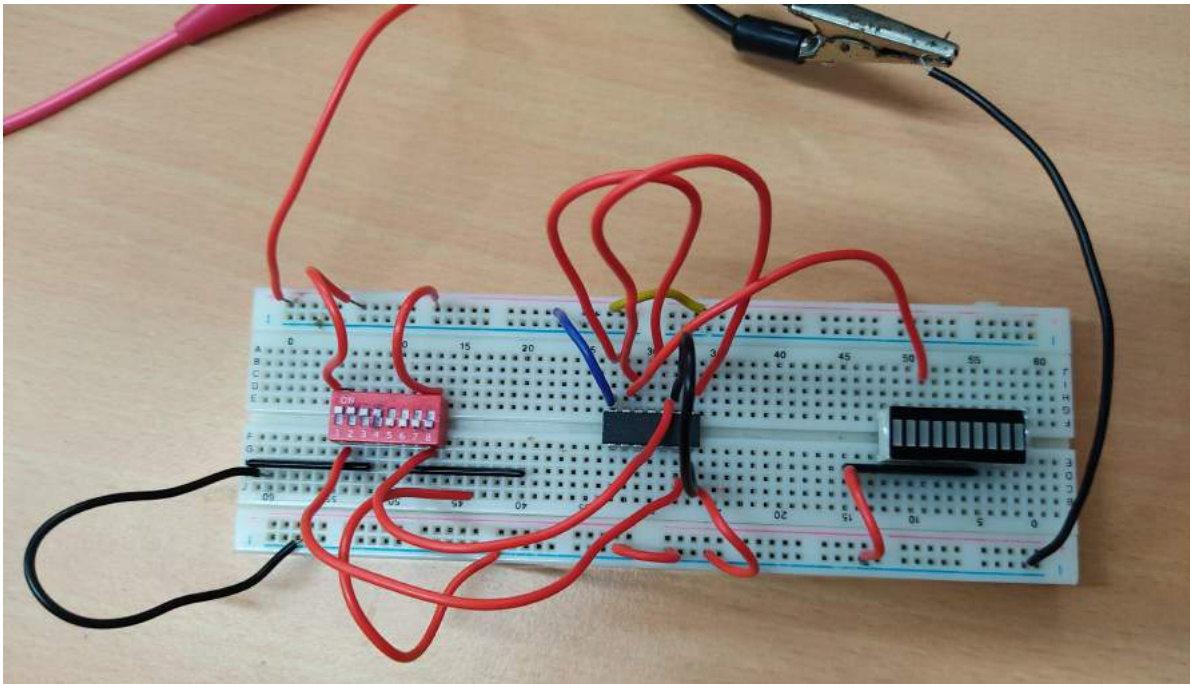
(using ^{IC} HCF4007)



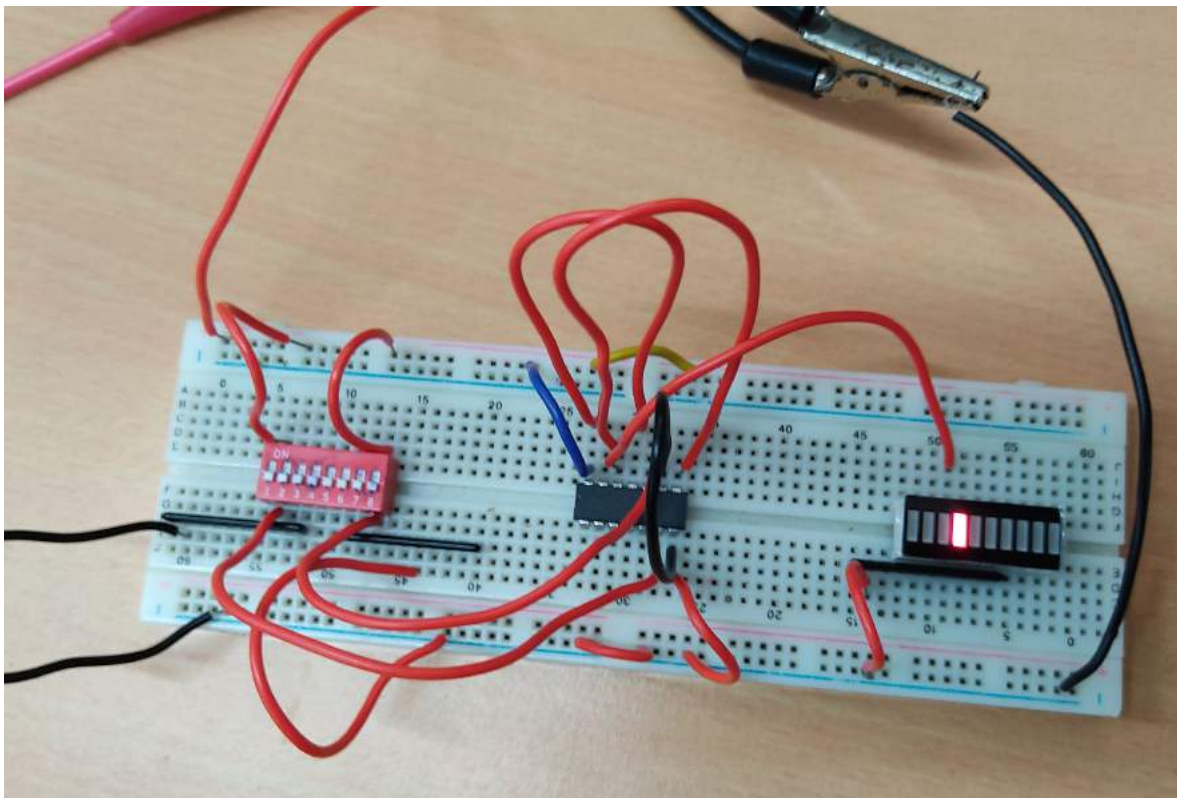
X → pin 6

Y → pin 10

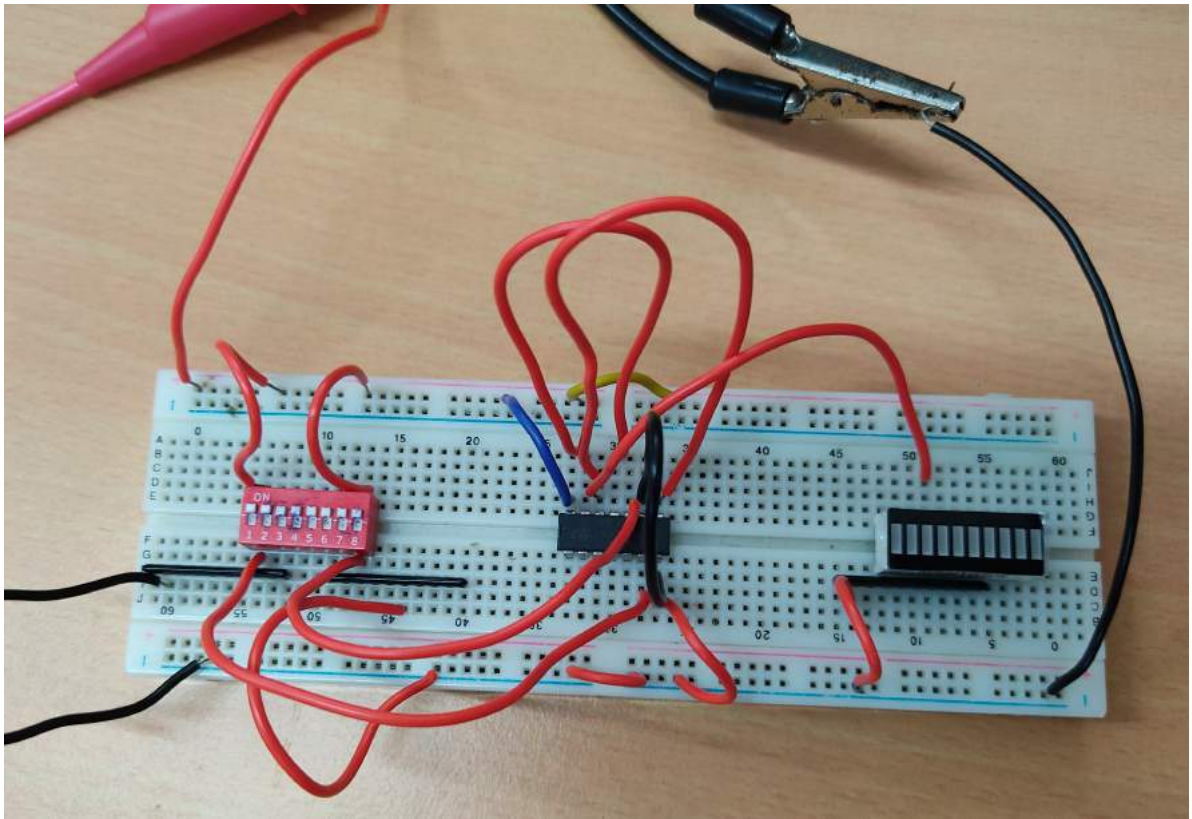
NOR:



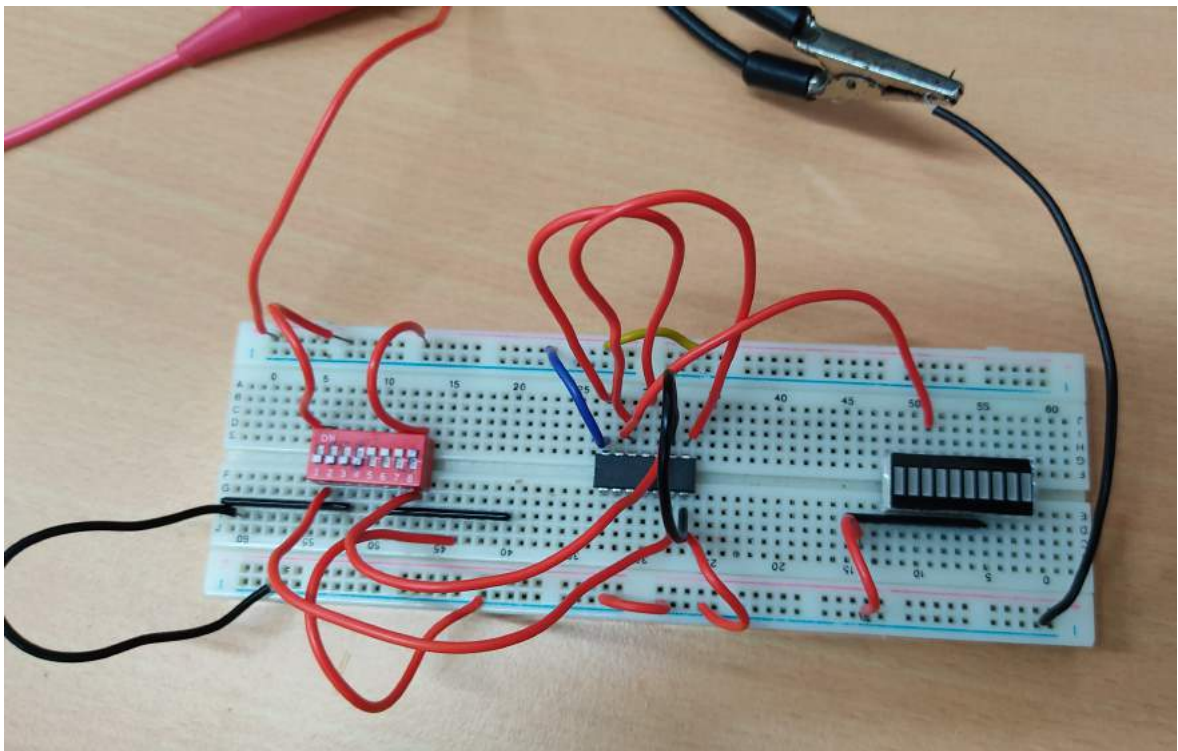
$1_0 \rightarrow 0$



$0_0 \rightarrow 1$



1_1 \rightarrow 0



0_1 \rightarrow 0