

## **LAB 2**

**Aim :** Designing logic gates using CMOS  
(Complementary Metal Oxide Semiconductor) logic.

**Summary of the experiment :** Study of logic gates using CMOS logic (Digital ICs) and verifying their functionality and rigging up of circuits.

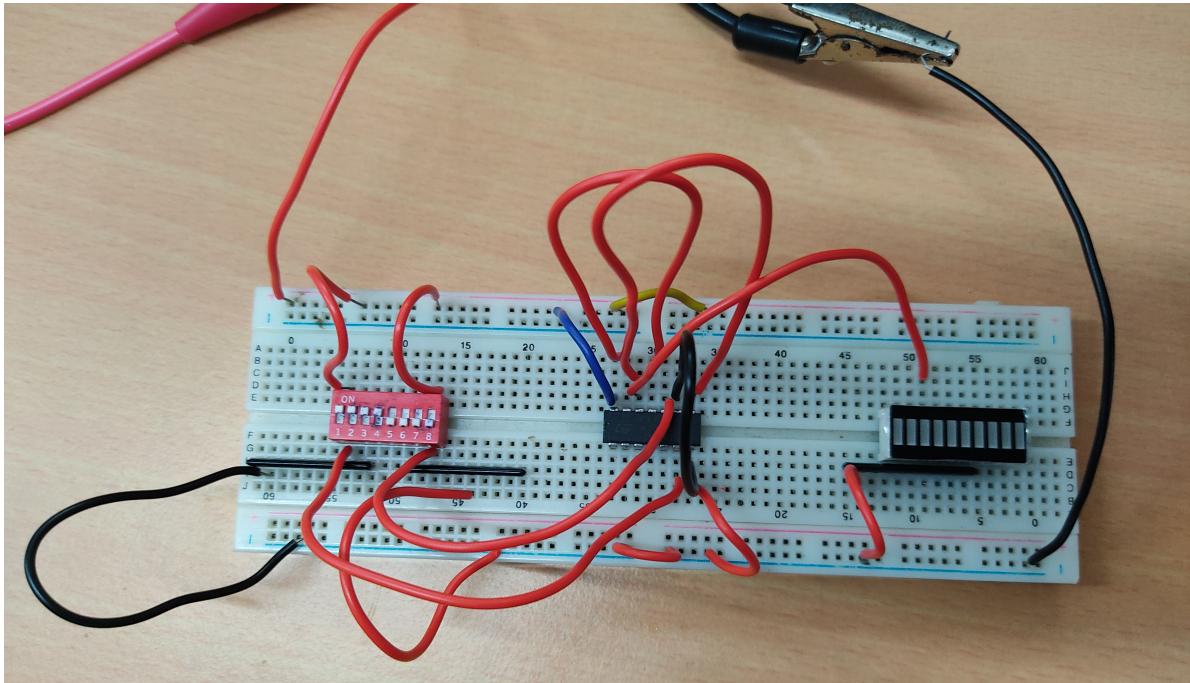
**Components used :** HCC4007UB, 1K ohm resistor array, DIP switches, LED displays, breadboard, power supply.

**Design Procedure and Circuit Diagram:**

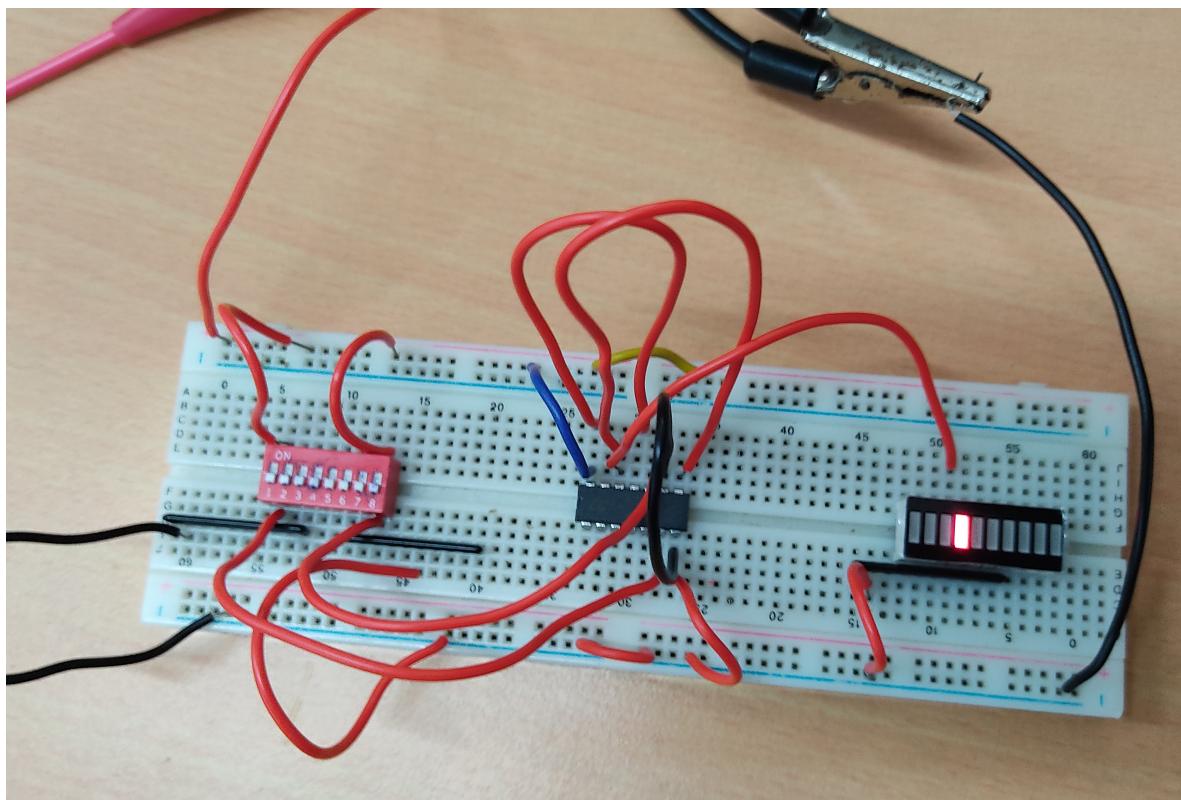
**Results and Discussions:**

**Conclusion:**

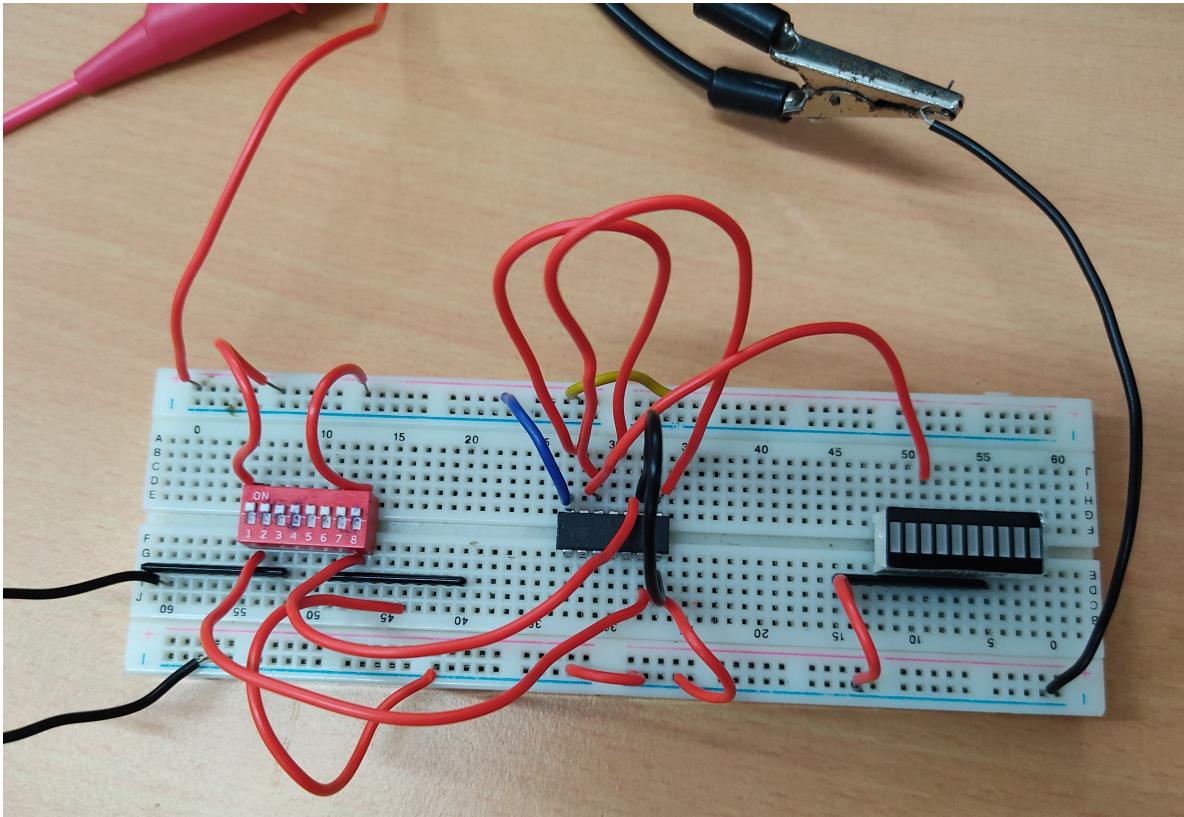
**NOR:**



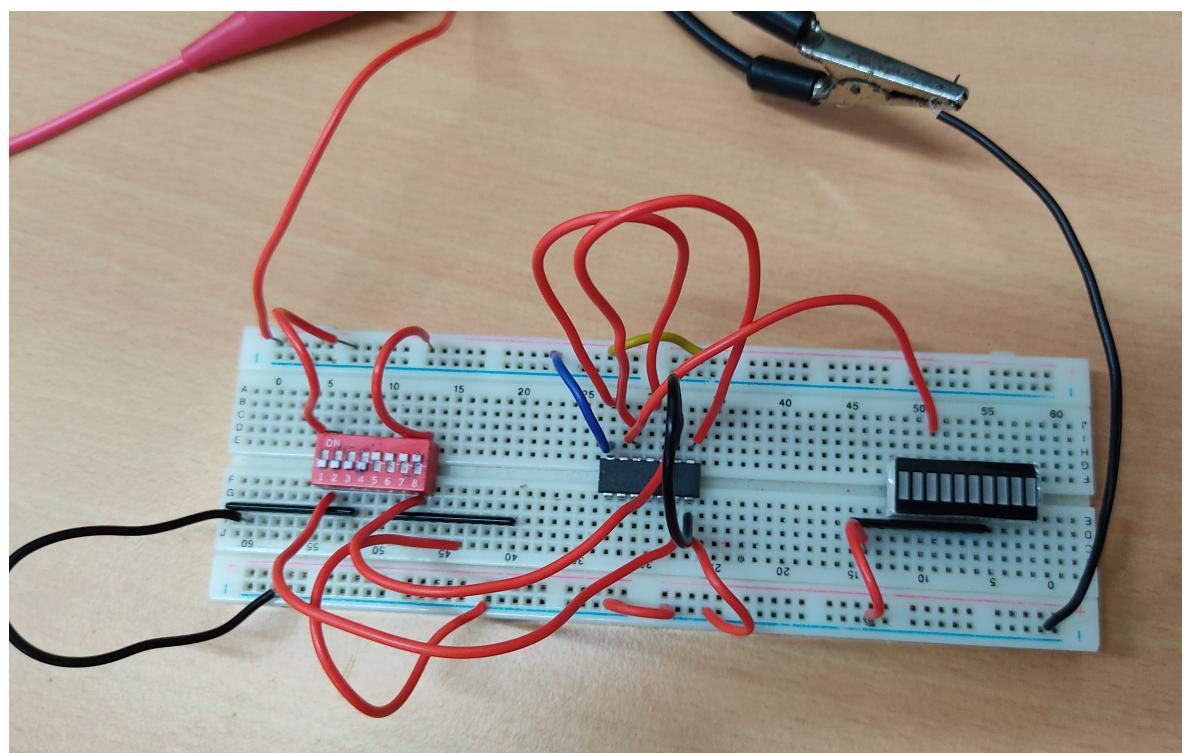
$$1_0 \rightarrow 0$$



$$0_0 \rightarrow 1$$



$1_1 \rightarrow 0$



$0_1 \rightarrow 0$