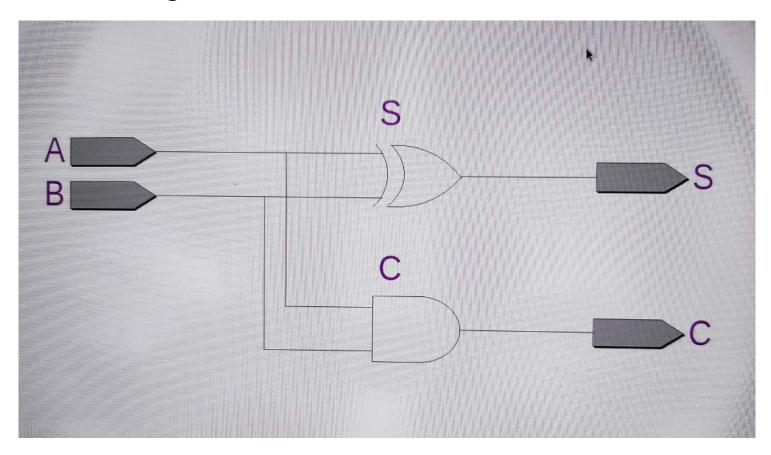
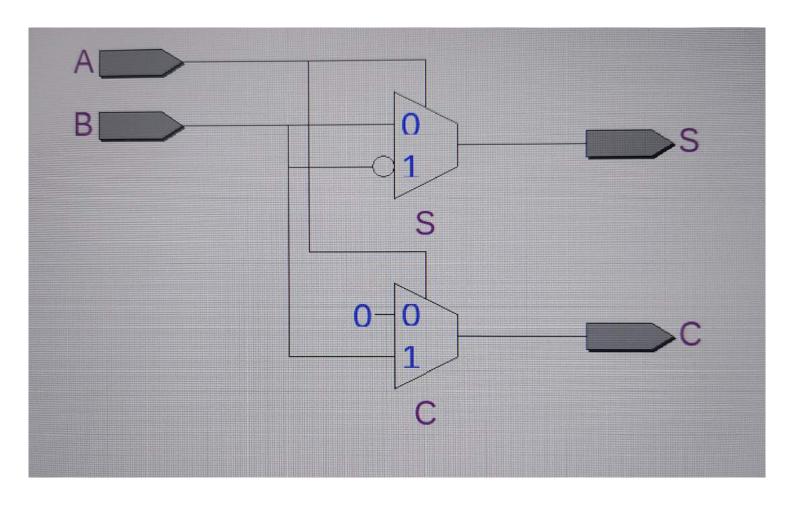
RTL Images:

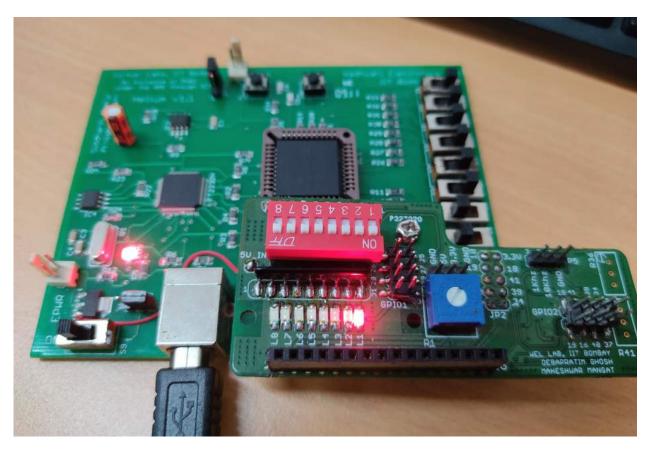




Circuit and Simulation Snapshots:



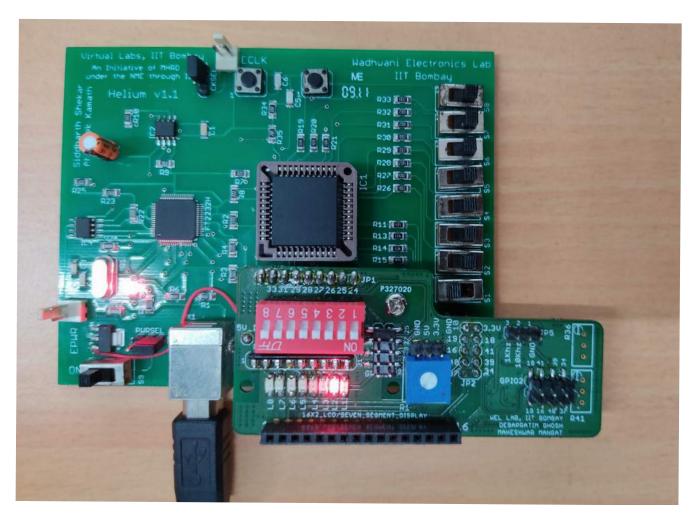
$$0 + 1 -> S = 1, C = 0$$



1 + 1 -> S = 0, C = 1



0 + 0 -> S = 0, C = 0



1 + 0 -> S = 1, C = 0