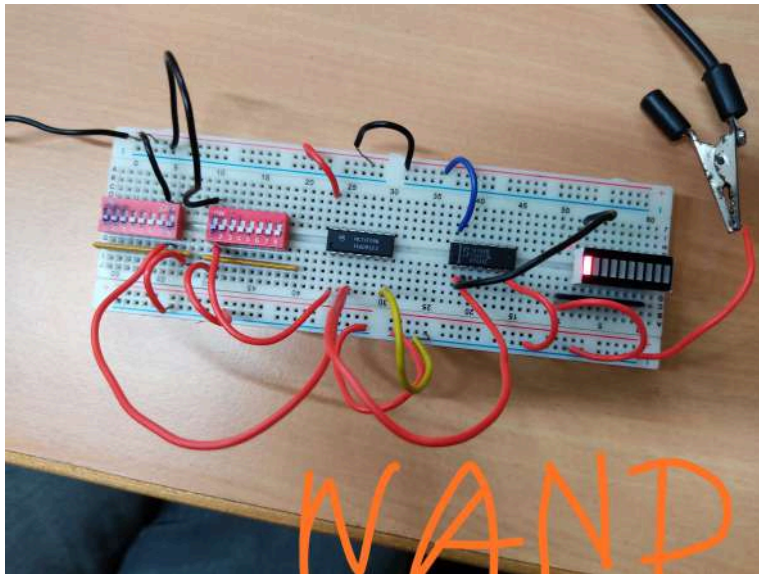
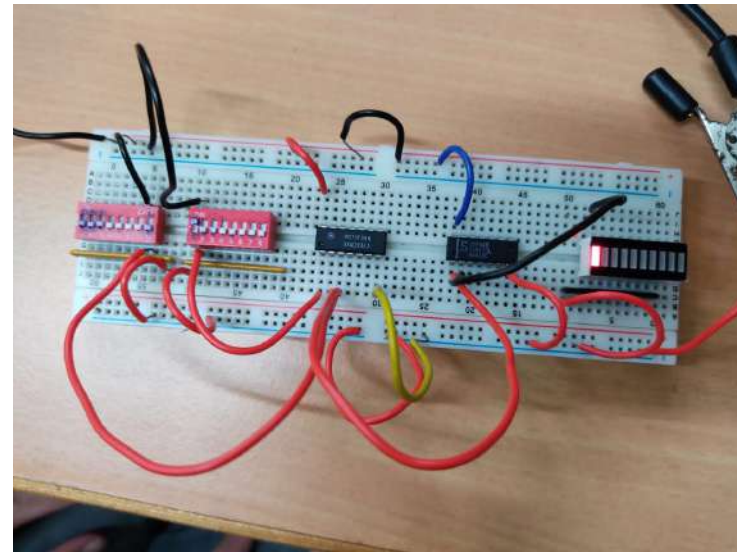


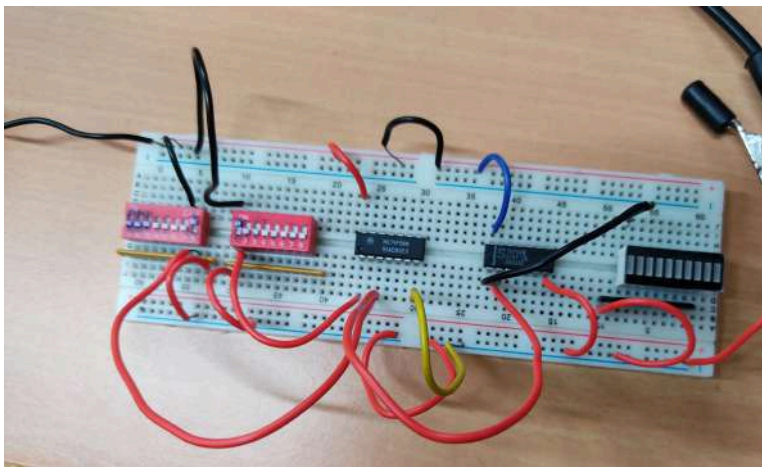
NAND:



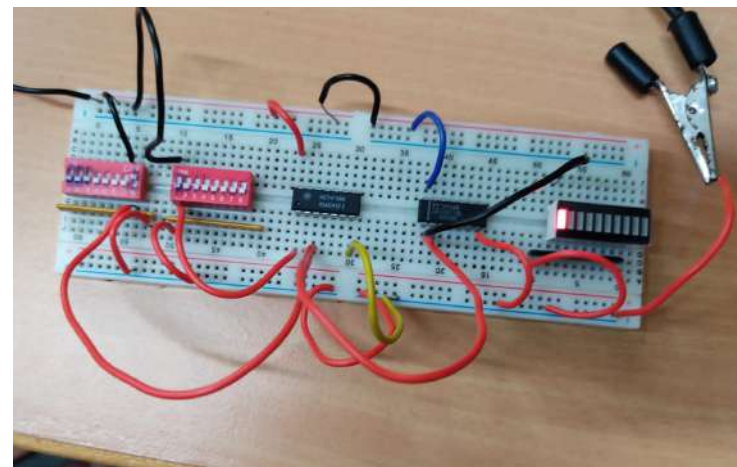
$0_0 \rightarrow 1$



$0_1 \rightarrow 1$

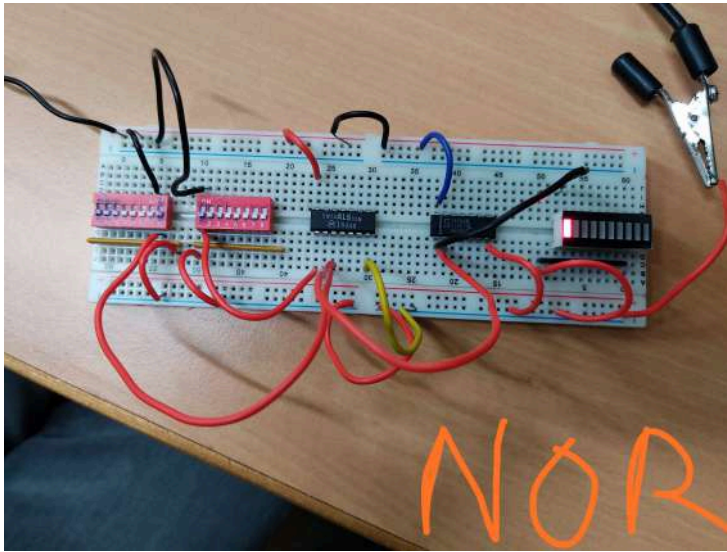


$1_1 \rightarrow 0$

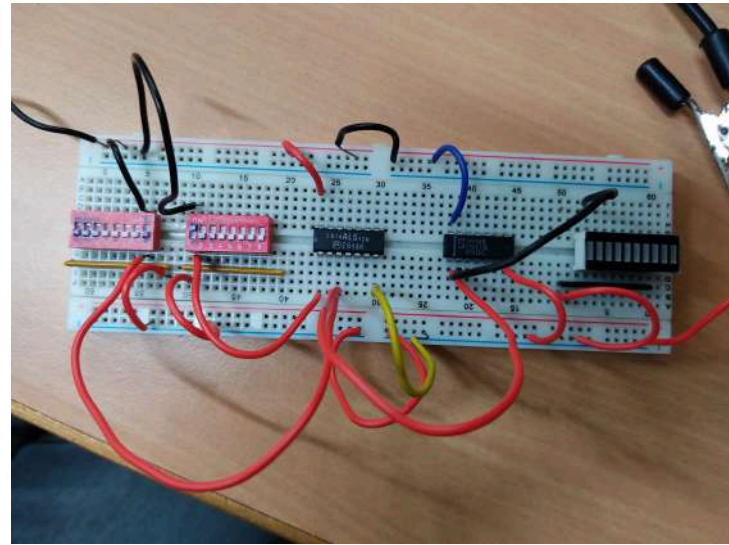


$1_0 \rightarrow 1$

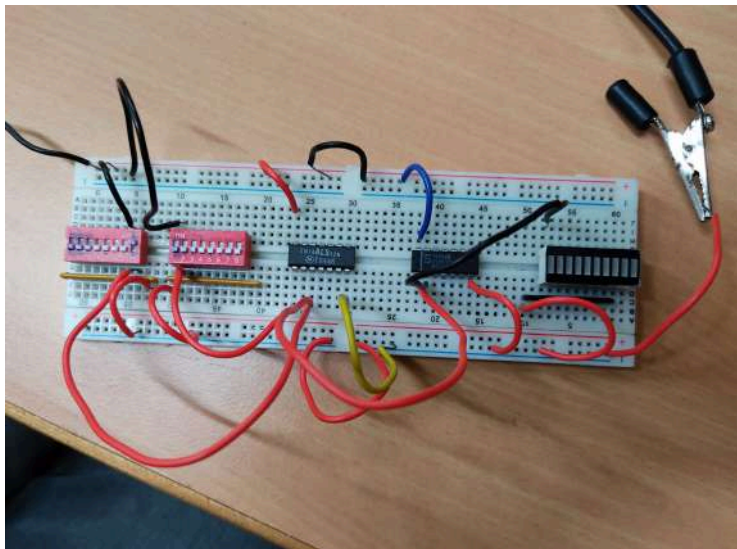
NOR:



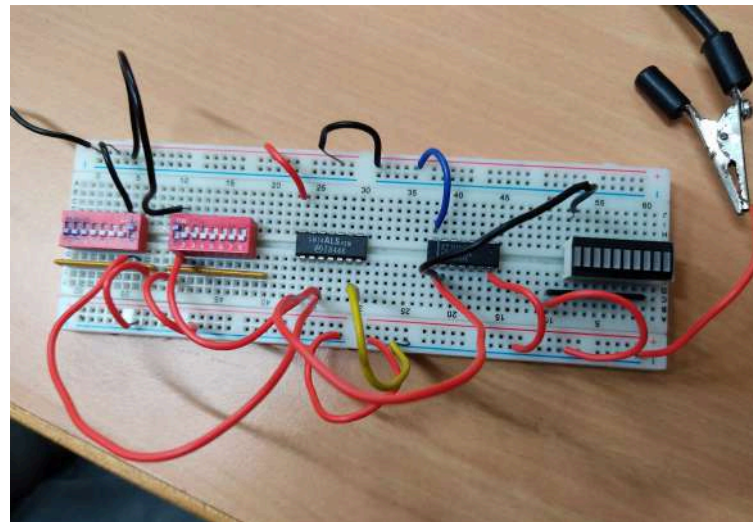
$0_0 \rightarrow 1$



$0_1 \rightarrow 0$

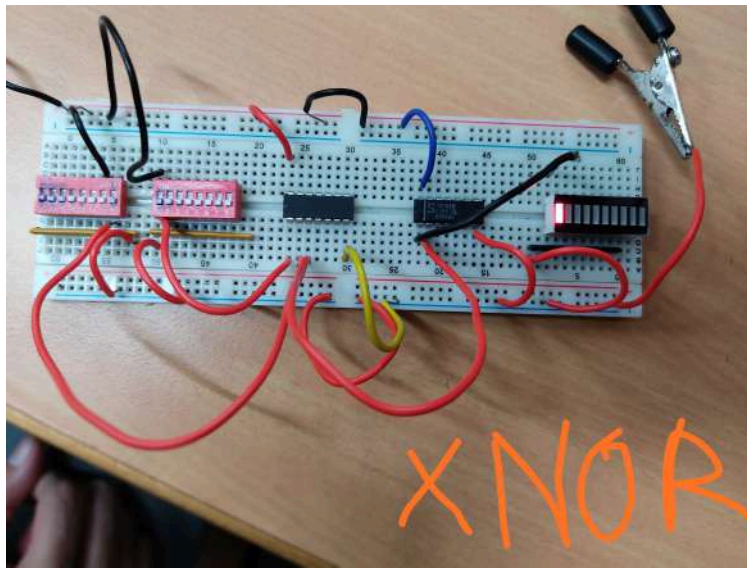


$1_0 \rightarrow 0$

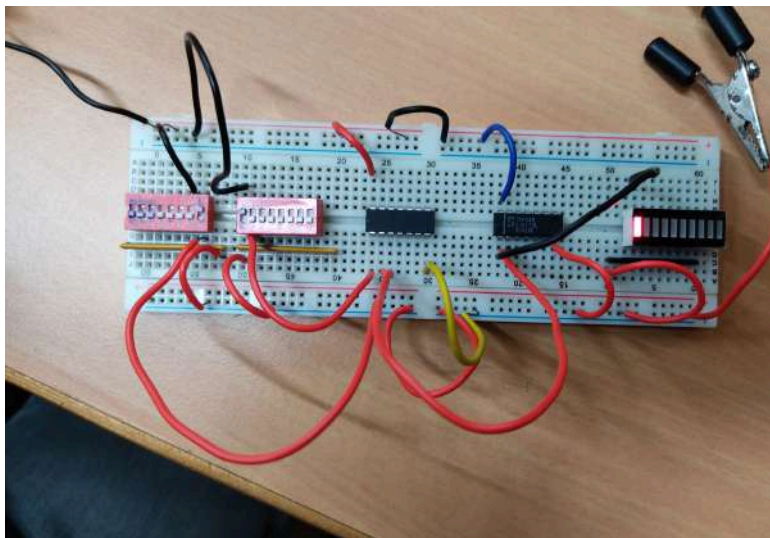


$1_1 \rightarrow 0$

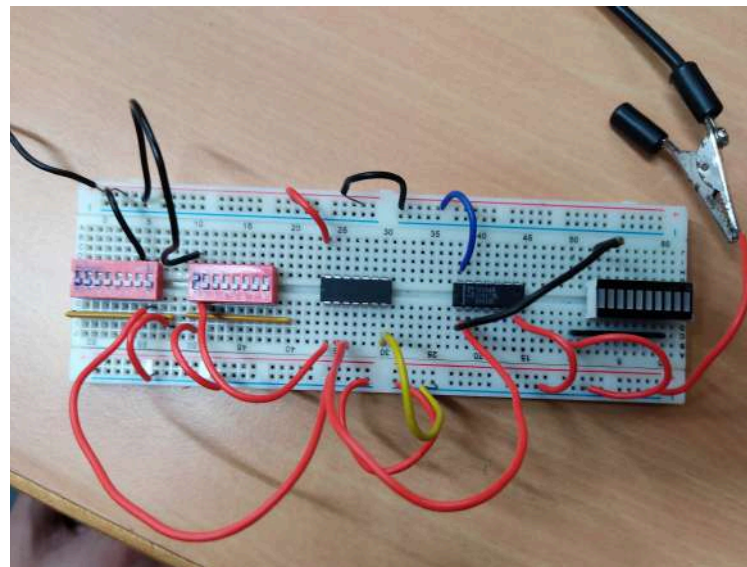
XNOR:



0_0 \rightarrow 1

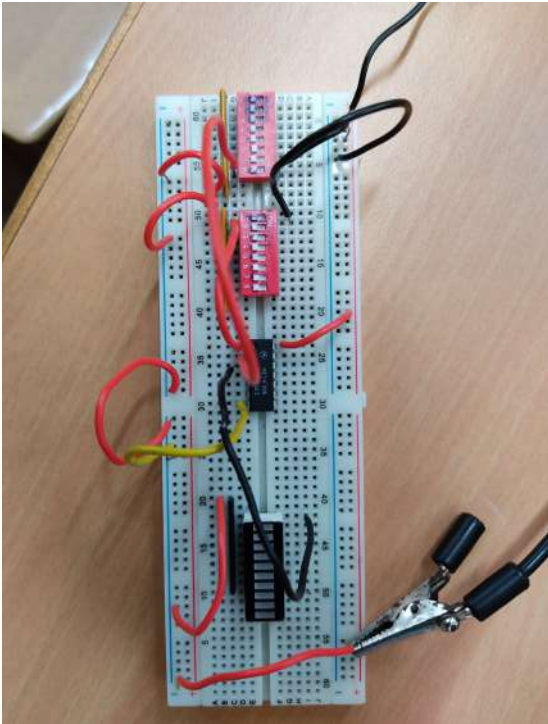


1_1 \rightarrow 1

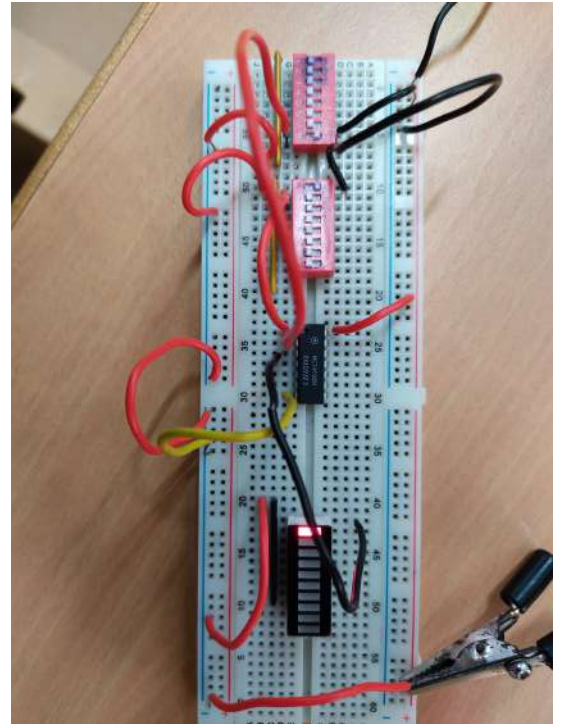


0_1 \rightarrow 0

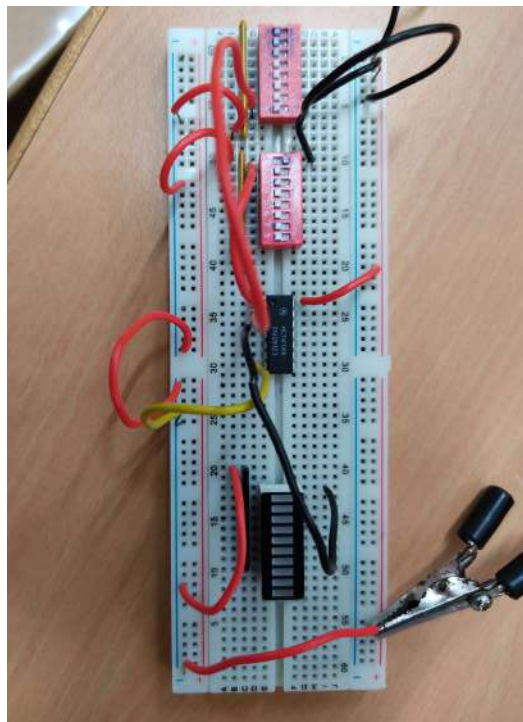
AND:



$0_0 \rightarrow 0$

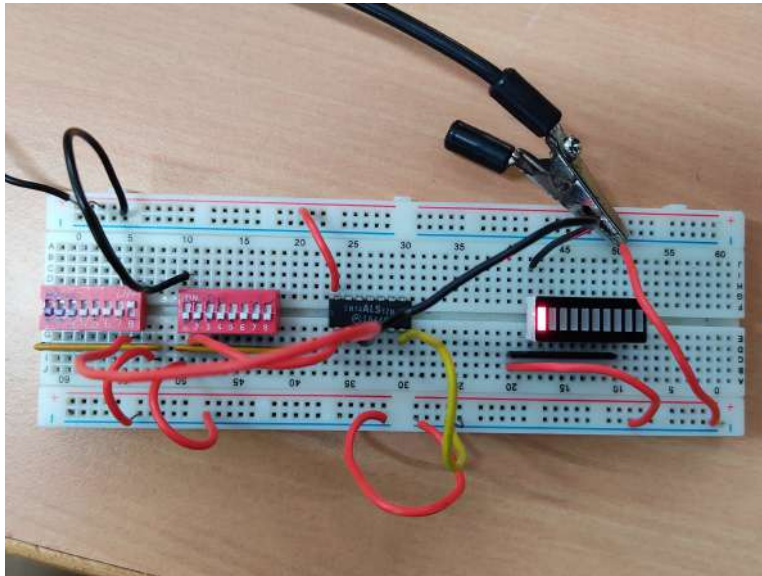


$1_1 \rightarrow 1$

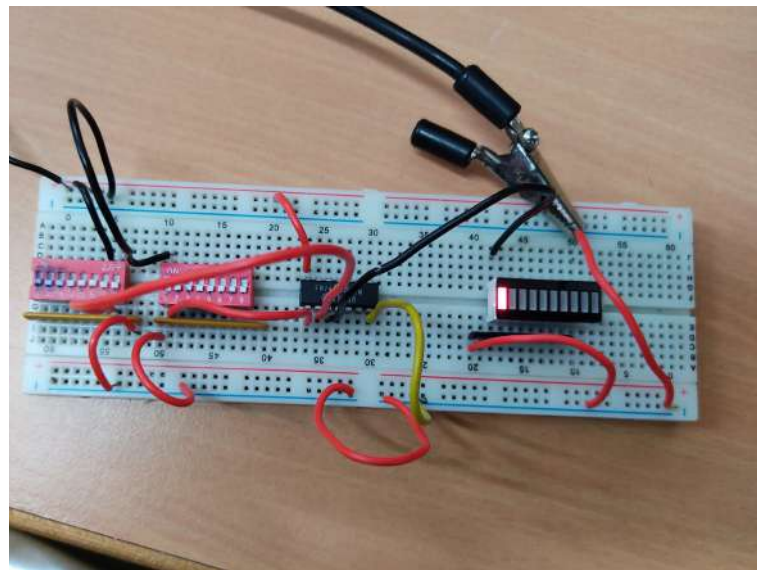


$1_0 \rightarrow 0$

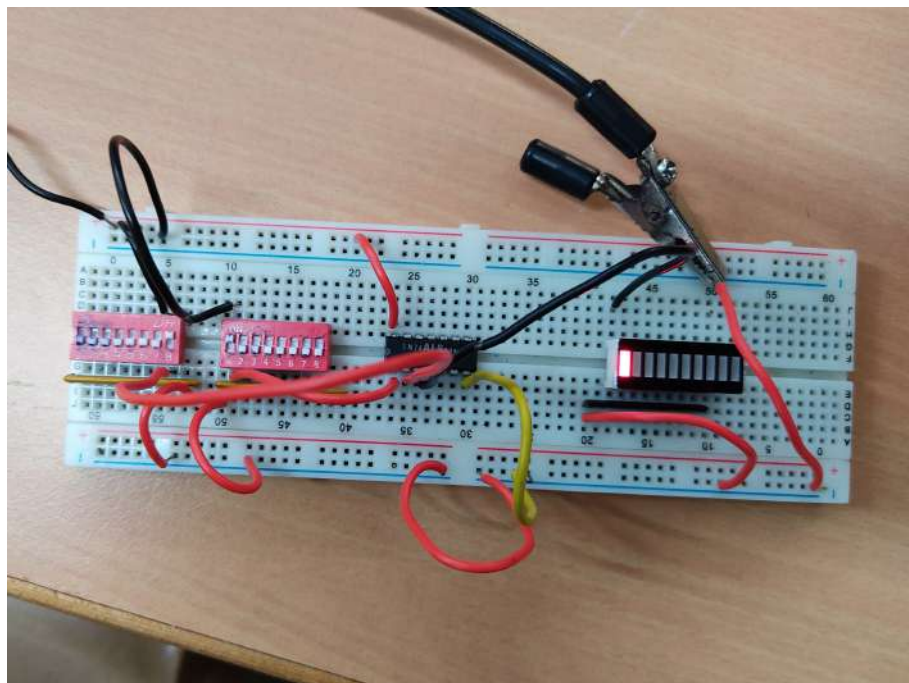
OR:



1_0 \rightarrow 1

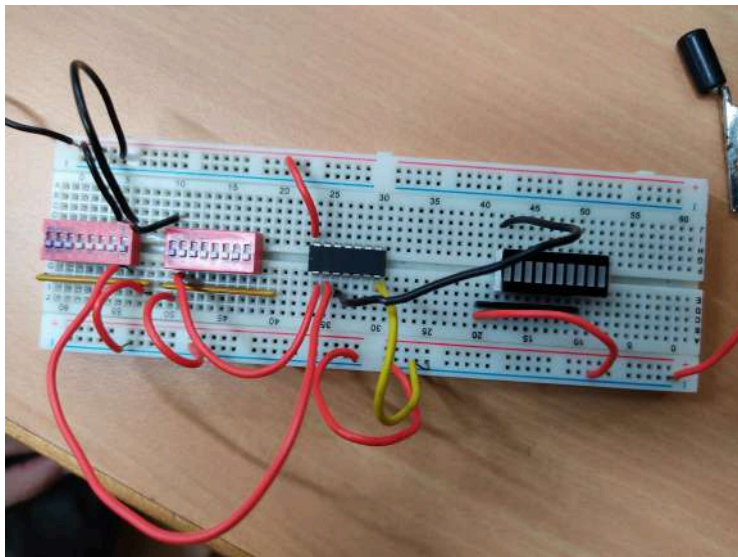


0_1 \rightarrow 1

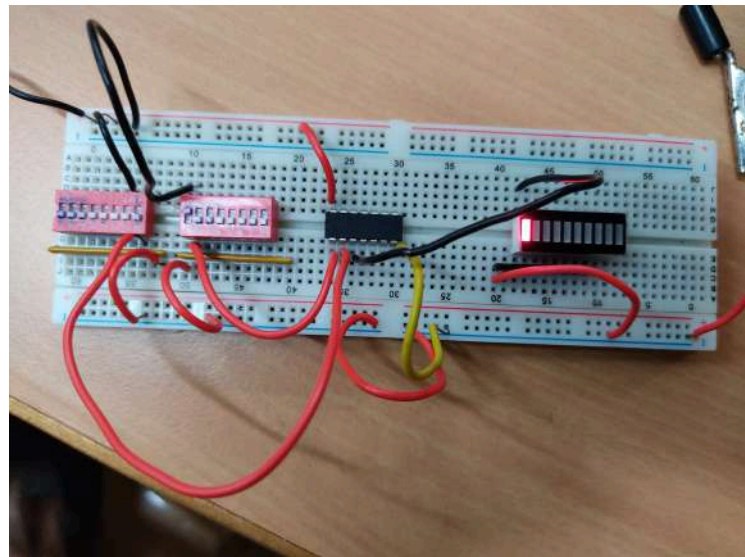


1_1 \rightarrow 1

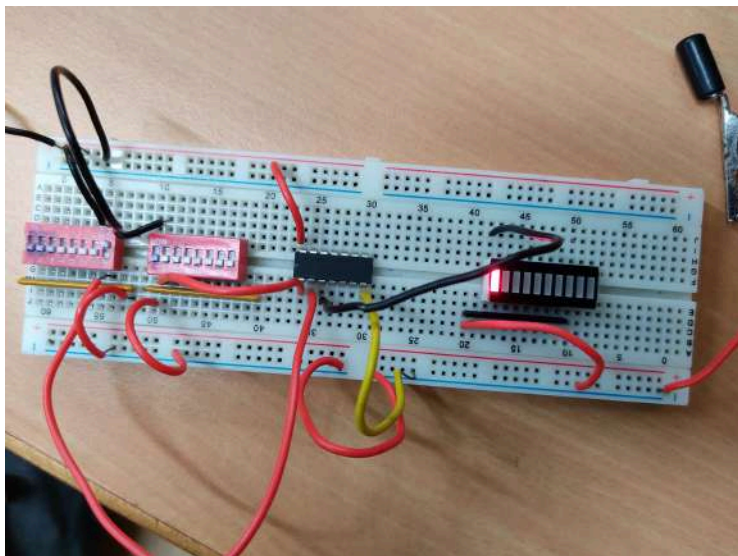
XOR:



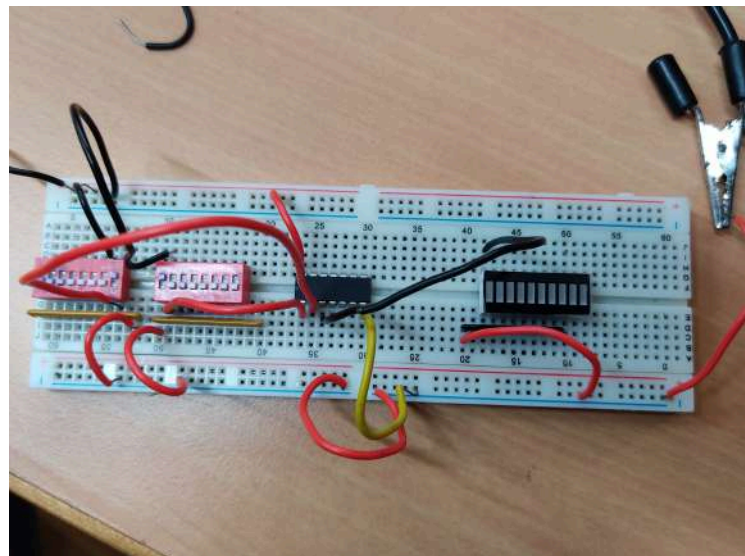
$0_0 \rightarrow 0$



$0_1 \rightarrow 1$

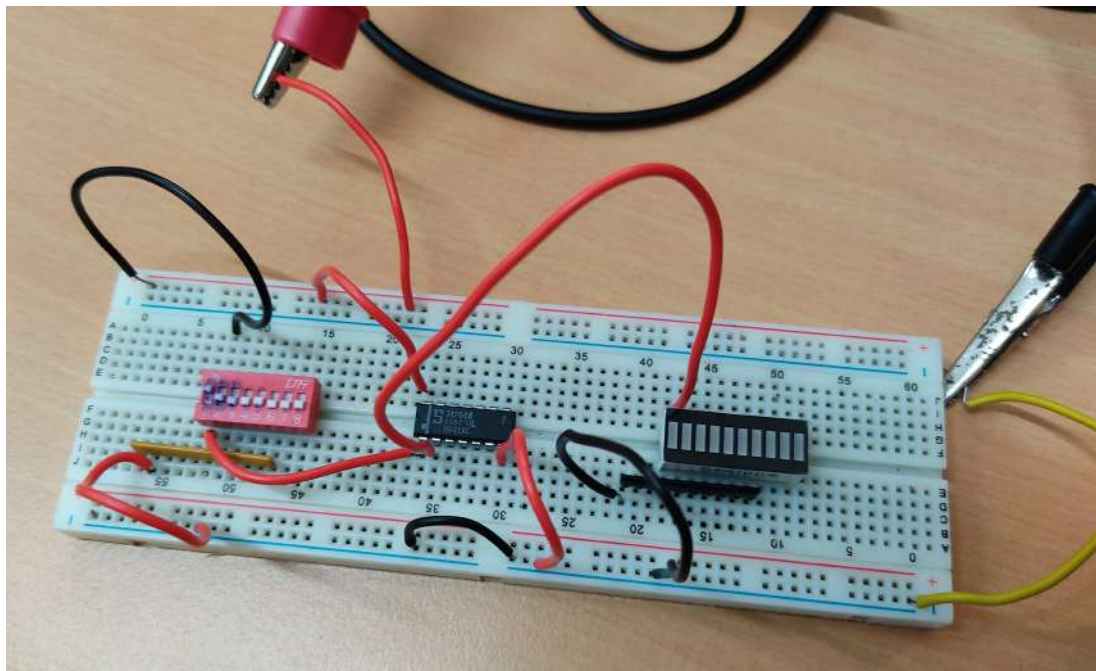


$1_0 \rightarrow 1$

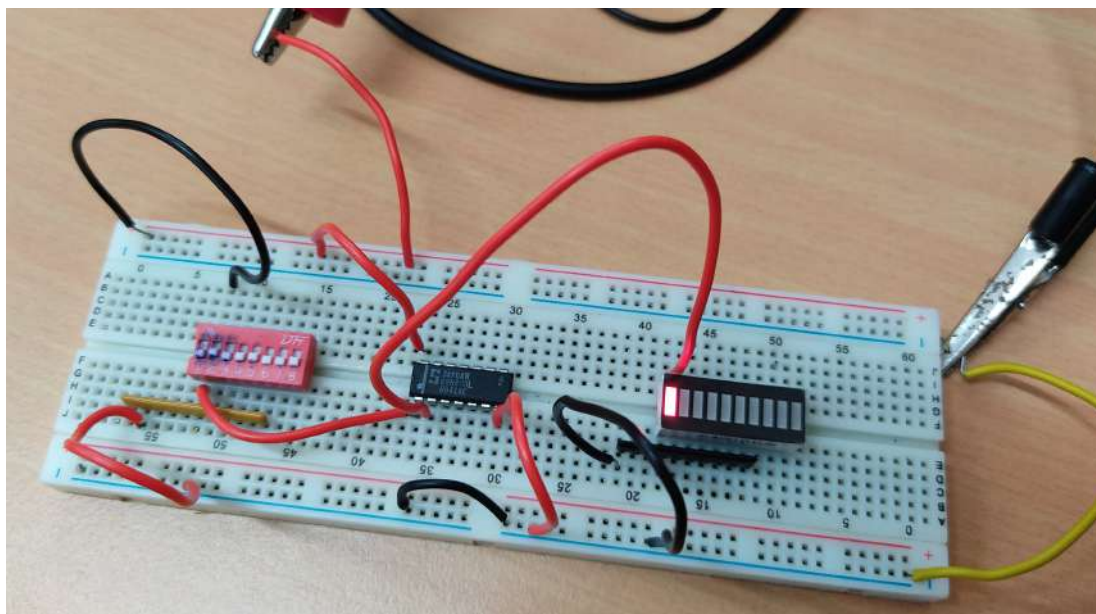


$1_1 \rightarrow 0$

NOT:



1 \rightarrow 0



0 \rightarrow 1