

Design Procedure

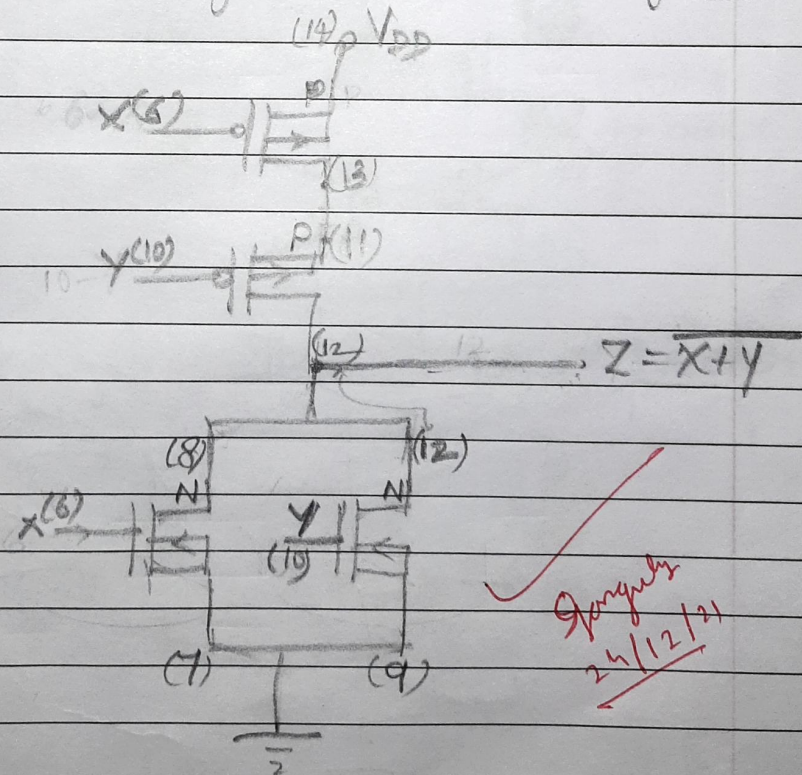
II.(A)

NOR: Truth Table

X	Y	$\overline{X+Y} = (X \text{ NOR } Y)$
0	0	1
0	1	0
1	0	0
1	1	0

In CMOS Logic,

(using ^{IC} HCF4007)



X → pin 6

Y → pin 10

ganga
24/12/21