

# 1 Dynamic Partitioning

## 1.1 Allocating Available Memory: First Fit

*First fit algorithm* starts scanning from the start of the linked list until a link is found which represents free space of sufficient size.

- If requested space is the **exact** same size as the hole, **all the space** is allocated
- Else, the free link is split into two: **The first** entry is set to the size requested and marked used. **The second** entry is set to remaining size and marked free

## 1.2 Allocating Available Memory: Next Fit

The next fit algorithm maintains a record of where it got to:

- It restarts its search from **where it stopped** last time
- It gives an **even** chance to all memory to get allocated (first fit concentrates on the start of the list)

However, simulations have shown that next fit actually gives **worse** performance than first fit!

## 1.3 Allocating Available Memory: Best Fit

**The best fit algorithm** always searches the **entire linked list** to find the smallest hole big enough to satisfy the memory request.

- It is **slower** than first fit
- It also results in **more wasted memory** (memory is more likely to fill up with tiny - useless - holes)

## 1.4 Allocating Available Memory: Worst Fit

**Tiny holes** are created when best fit split an empty partition. The worst fit algorithm finds the **largest** available empty partition and splits it.

- The left over part will still be large (and potentially more useful)
- Simulations have also shown that worst fit is not very good either!

## 1.5 Allocating Available Memory: Quick Fit and Others

Quick fit maintains lists of commonly used sizes

- For example a separate list for each of 4K, 8K, 12K, 16K, etc., holes
- Odd sizes can either go into the nearest size or into a special separate list

It is much **faster** to find the required size hole using *quick fit*. Similar to best fit, it has the problem of **creating many tiny holes**. Finding neighbours for coalescing (combining empty partitions) becomes more **difficult/time consuming**.

## 1.6 Summary

- First fit: allocate first block that is large enough
- Next fit: allocate next block that is large enough, i.e. starting from the current location
- Best fit: choose block that matches required size closest -  $O(N)$  complexity
- Worst fit: choose the largest possible block -  $O(N)$  complexity

## 2 Managing available memory

### 2.1 Coalescing

*Coalescing* (joining together) takes place when two adjacent entries in the linked list become free. Both neighbours are examined when a block is freed.

- If either (or both) are also free
- Then the two (or three) entries are **combined** into one larger block **by adding up** the sizes
- The earlier block in the linked list gives the start point
- The separate links are **deleted** and a **single link** inserted

### 2.2 Compacting

Even with *coalescing* happening automatically, free blocks may still be distributed across memory.

*Compacting* can be used to **join** free and used memory (but is time consuming). Compacting is more **difficult and time consuming** to implement than coalescing (processes have to be moved). Each process is **swapped** out and free space coalesced. Process **swapped back** in at lowest available location

### 2.3 Allocation Schemes

Different contiguous memory allocation schemes have different advantages/disadvantages:

- Mono-programming is **easy** but does result in **low** resource utilisation
- Fixed partitioning facilitates multi-programming but results in **internal fragmentation**
- Dynamic partitioning facilitates multi-programming, reduces internal fragmentation, but results in **external fragmentation** (allocation methods, coalescing, and compacting help)

## 3 Paging

*Paging* uses the principles of fixed partitioning and code re-location to devise a new non-contiguous management scheme:

- Memory is **split into much smaller blocks** and one or multiple blocks are allocated to a process e.g., a 11KB process would take up 3 blocks of 4 KB
- These blocks **do not** have to be contiguous in main memory, but the process still perceives them to be **contiguous**
- Benefits compared to contiguous schemes include: *Internal fragmentation* is reduced to the last block only. There is **no external fragmentation**, since physical blocks are stacked directly onto each other in main memory.

A page is a small block of contiguous memory in the **logical** address space, i.e. as seen by the process. A frame is a small contiguous block in **physical** memory

### 3.1 Relocation

Logical address (page number, offset within page) needs to be **translated into a physical address** (frame number, offset within frame)

. Multiple base registers will be required:

- Each logical page needs a **separate base register** that specifies the start of the associated frame. I.e., a set of base registers has to be maintained for each process
- The base registers are stored in the **page table**

The page table can be seen as a function, that **maps** the page number of the logical address onto the frame number of the physical address  $\text{frameNumber} = f(\text{pageNumber})$ . The **page number** is used as **index** to the page table that lists the number of the associated frame, i.e. it contains the location of the frame in memory.

Every process **has its own page table** containing its own base registers. The operating system maintains a list of free frames.

## Reference section

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