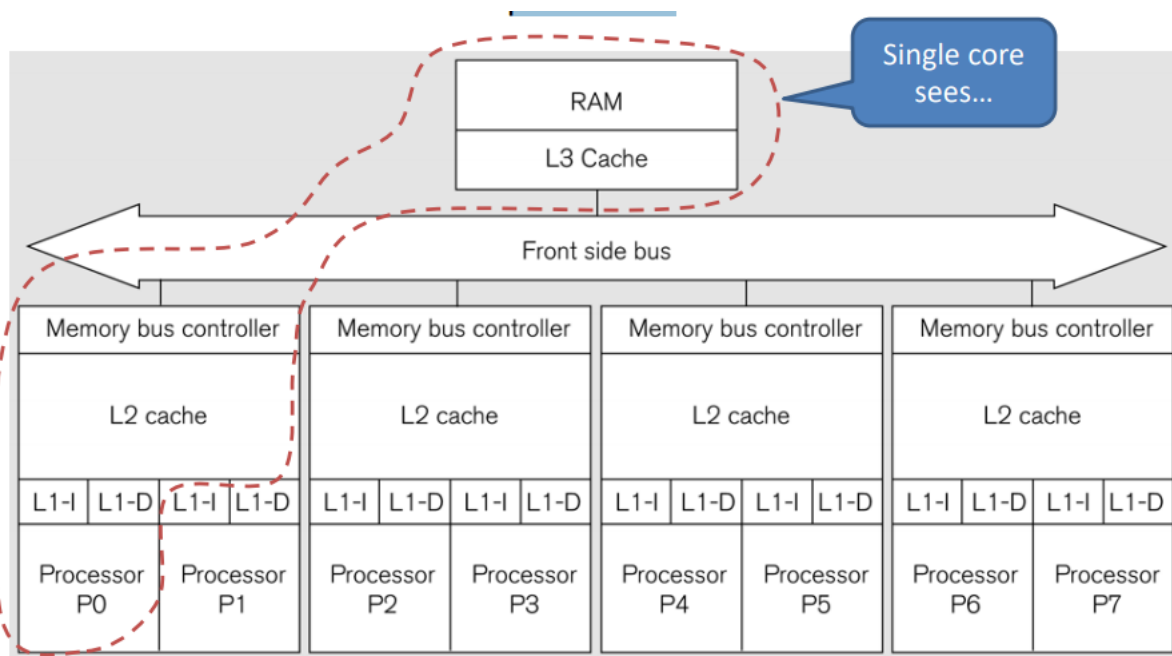


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# 1 Single core

Each processor has a **private L1 cache**; it shares an **L2 cache** with its chip-mate and shares an **L3 cache** with the other processors.



## 1.1 Cache

The purpose of cache memory is to store **program instructions and data** that are used repeatedly in the operation of programs or information that the CPU is likely to need next.

**L1 cache**, or primary cache, is extremely fast but relatively small, and is usually embedded in the processor chip as CPU cache.

## Reference section

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