/ALUwithRegister/SW	Ozzzzz0101		1zzzzz0011		1zzzzz1111		0zzzzz1001		1zzzzz1010	
/ALUwithRegister/KEY	0000	0001	0000	0001	0000	0001	0000	0001	0000	0001
/ALUwithRegister/HEX0	0010010	-	0110000		0001110		0011000	+	0001000	
/ALUwithRegister/HEX1	1000000	-			1		-	+		
/ALUwithRegister/HEX2	1000000							•		
/ALUwithRegister/HEX3	1000000							<u> </u>		
/ALUwithRegister/HEX4		0010010	0110000	0000010	0100100	1111001	0000011	0011000	0001000	0011001
/ALUwithRegister/HEX5	xxxx00x	1000000			1111001		1000000	†	-	1111001
/ALUwithRegister/LEDR	ZZXXXXXXX	zz00000000		zz00000011	•	zz00010010	-	zz00000000	•	zz00001010
/ALUwithRegister/A	5		3	<u> </u>	f		9	+	а	
/ALUwithRegister/B		0		3		2		0		a
/ALUwithRegister/sel	000	-		•		-	-	-	-	
/ALUwithRegister/clock									-	
/ALUwithRegister/reset_b		•		<u> </u>						†
/ALUwithRegister/ALUout	Xx	05	03	06	12	11	0b	09	0a	14
/ALUwithRegister/q		00		03		12		00	•	(0a