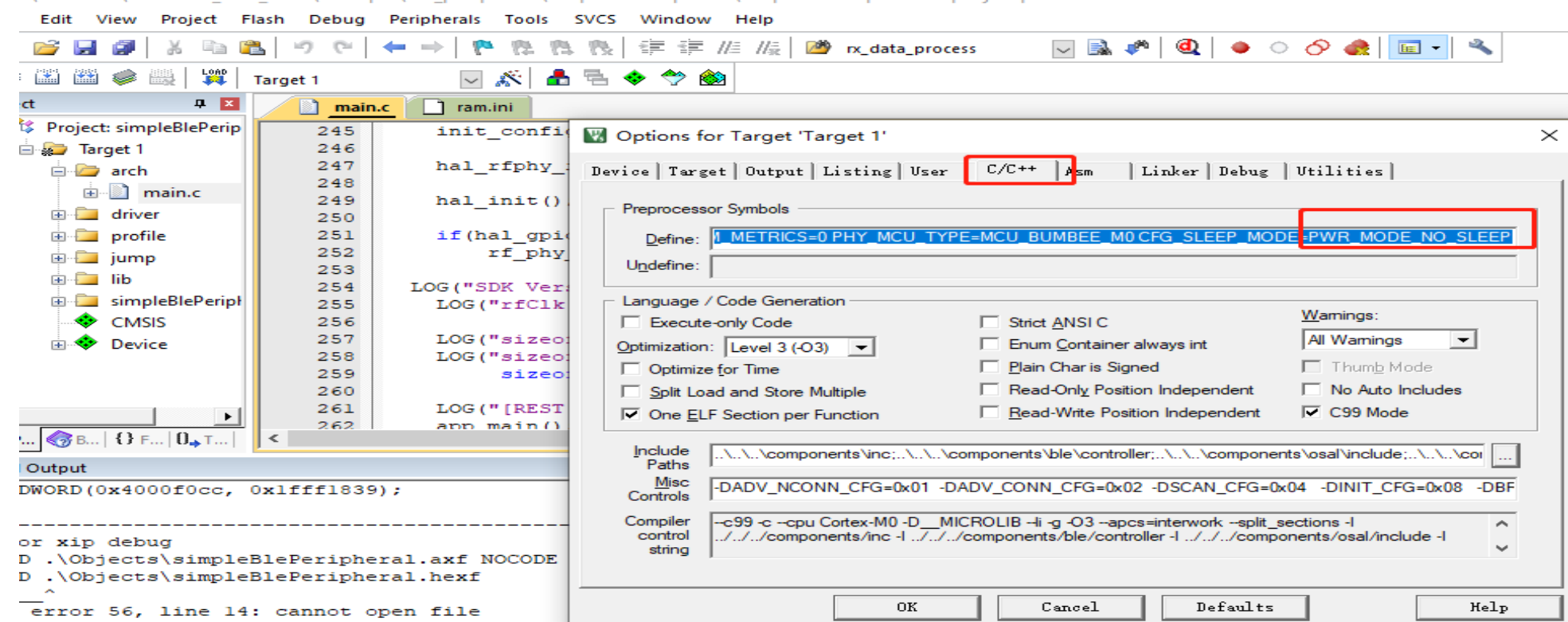


PHY6222/52 SWD Debug  
chendy(20210118)

# (一)帶有XIP工程debug

## 1)把工程配置成NO SLEEP模式

\\PHY6252\\PHY62XX\_SDK\_3.0.7\\example\\ble\_peripheral\\simpleBlePeripheral\\simpleBlePeripheral.uvprojx - μVision



# (一)帶有XIP工程debug

## 2) 擦除芯片里的程序

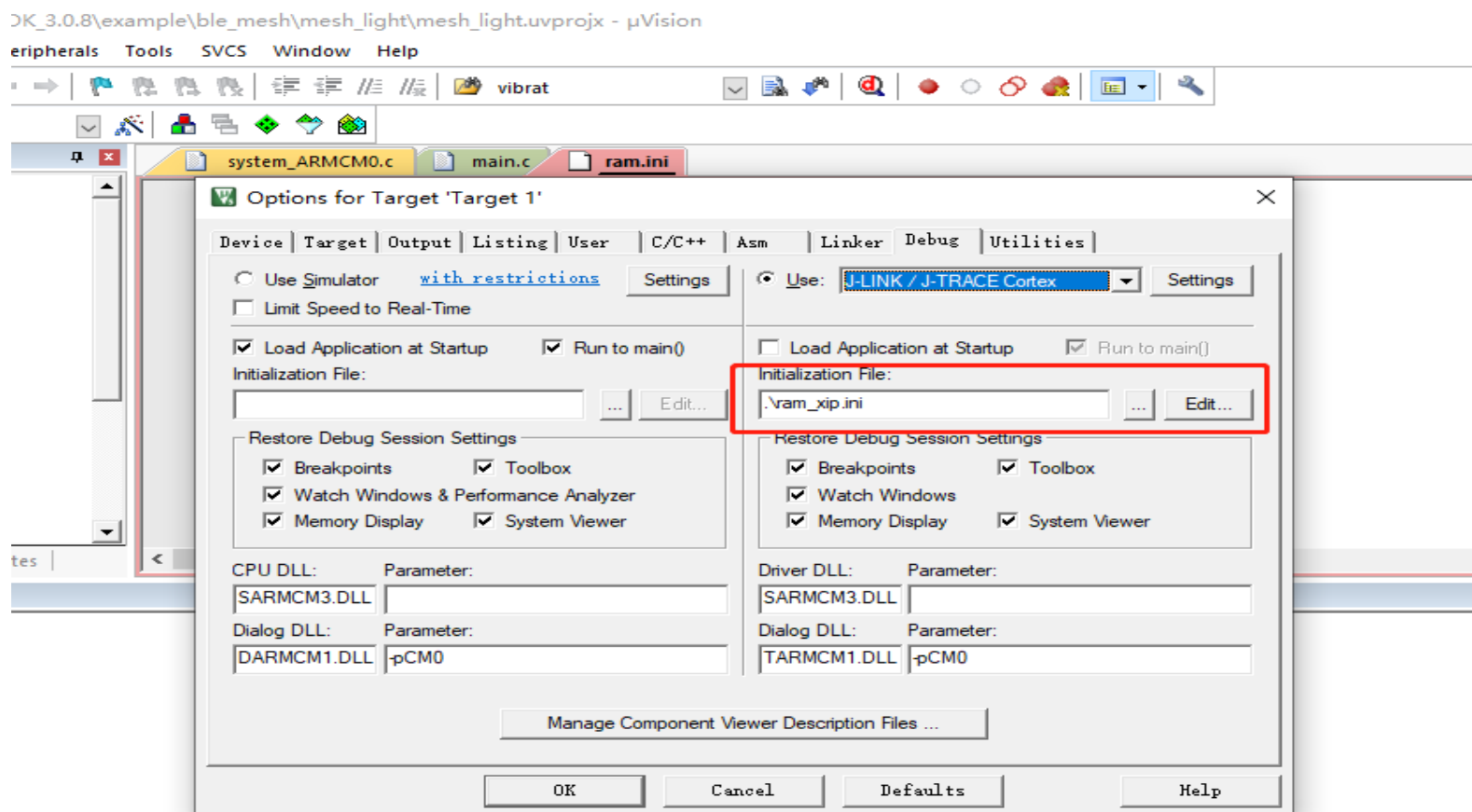
The screenshot displays the Flash\_Writer application interface, which is used for programming and debugging flash memory. The interface is divided into several sections:

- Flash\_Writer** (Main Window):
  - Config**: Includes fields for **Timeout** (4000), **Erase Size** (512k), and **Address**. Buttons for **Save**, **Clear**, **Erase**, **Write**, and **LW** are present.
  - fct\_Mode**: A dropdown menu.
  - IMG / HEX / HEX Merge**: Tabs for selecting the file format.
  - BOOT**: A dropdown menu.
  - APP**: A text field containing the file path `sPhy/smart_rf/bin/YiShiHeatClothesCharge.hex`.
  - No OTA**: A dropdown menu.
  - Hex16** and **HexF**: Buttons for selecting the hex format.
  - SEC** and **Auth**: Checkboxes.
  - Encrypt**: A button.
  - FLA\_ADDR**: A list of five address fields.
  - ChipID/IV**: Fields for **PID[16]**, **LID[10]**, **TID[14]**, **MID[16]**, **SID[08]**, **IV[13]**, **MAC[6]**, **KEY1[32]**, and **KEY2[32]**. Buttons for **CheckID**, **WriteID**, and **WriteMAC** are provided.
  - Single / Batch**: Tabs for selecting the operation mode.
  - Table**: A table with columns **TYPE**, **PATH**, **SIZE**, **ADDRESS**, and **VALUE**. It contains one row with **TYPE** set to **MAC** and **VALUE** set to `89:88:62:52:03:05`.
  - Command**: A text field and a **Send** button.
  - HEX**: A checkbox.
  - ClearBuf**: A button.
- UART Setting** (Right Panel):
  - Port**: **COM14**.
  - Baud Rate**: **115200**.
  - Stop Bits**: **1**.
  - Parity**: **No**.
  - SWU**: A checkbox.
  - Disconnect**, **AutoCheck**, and **Update** buttons.
- Log** (Right Panel):
  - A text area showing the execution log. A red box highlights the following messages:

```
Send erase successfully!  
Receive #OK!  
Erase successfully!
```
  - TimeTic**: A checkbox.
  - Mode**: **ASCII**.
  - Save** and **Clear** buttons.

# (一)帶有XIP工程debug

## 3) 选择对应ramxip.ini,如下图参考



## (一)帶有XIP工程debug

4) 用Phypluskit生成hexf文件, ramxip.ini会调用hexf文件(默认是object, 为了方便, 可以hexf文件目录修改为bin录下)

Edit Settings Help

sh\_Writer RF\_CMD RF\_QuickSet Multi\_FW

Config [ ] Timeout 4000 Save Clear

st\_Mode Erase Size 512k Address [ ] Erase Write ☒ LW

IMG HEX HEX Merge

BOOT [ ] No OTA Hex16 HexF

APP PhyPlusPhy/smart\_rf/bin/GZCX\_RemoteCtrl.hex ☐ SEC ☐ Auth -- Encrypt

-- FLA\_ADDR [ ]

-- FLA\_ADDR [ ]

-- FLA\_ADDR [ ]

-- FLA\_ADDR [ ]

-- FLA\_ADDR [ ]

ChipID/IV

PID[16] [ ] LID[10] [ ] TID[14] [ ] CheckID

MID[16] [ ] SID[08] [ ] IV[13] [ ] WriteID

MAC[6] [ ] KEY[32] [ ] Hex[xx-xx-xx-xx-xx-xx] WriteMAC

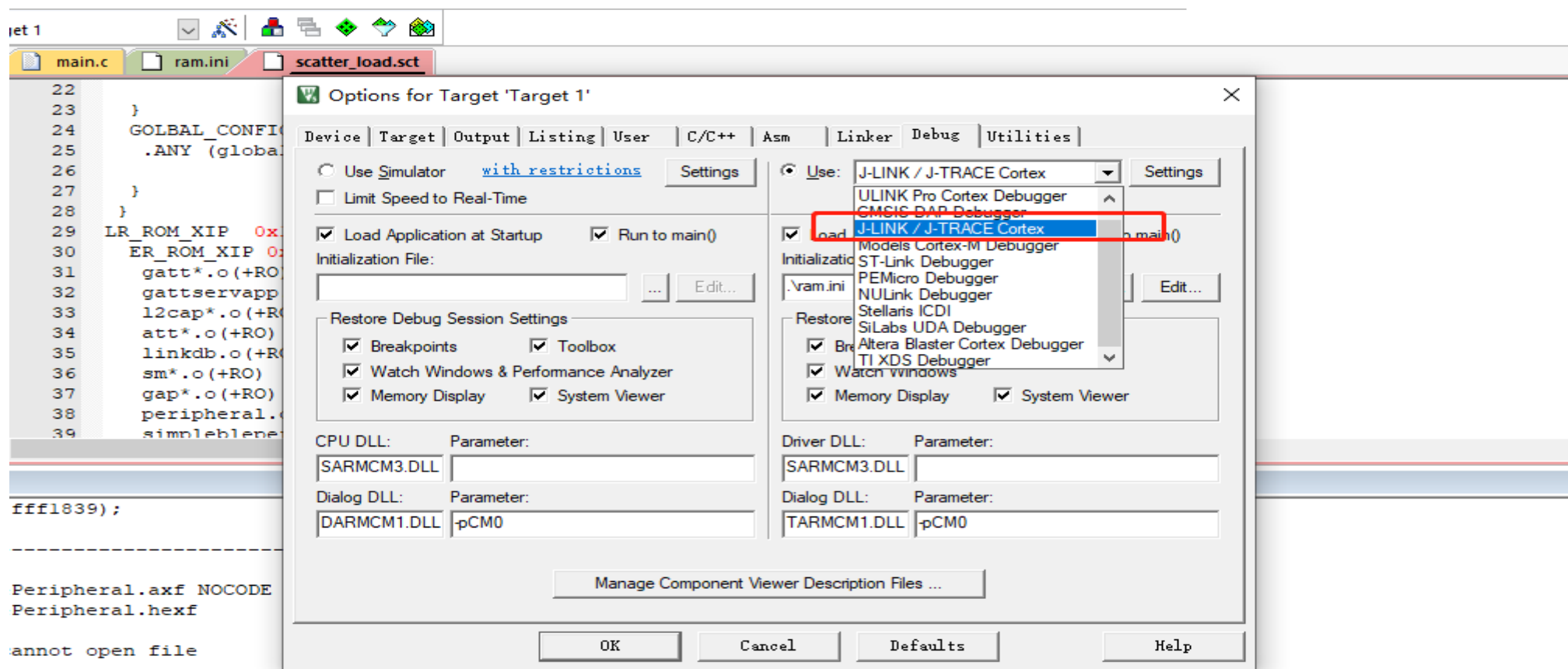
Single Batch

	TYPE	PATH	SIZE	ADDRESS	VALUE
1	MAC				62:52:20:21:01:14
2					
3					
4					
5					

[illegible]

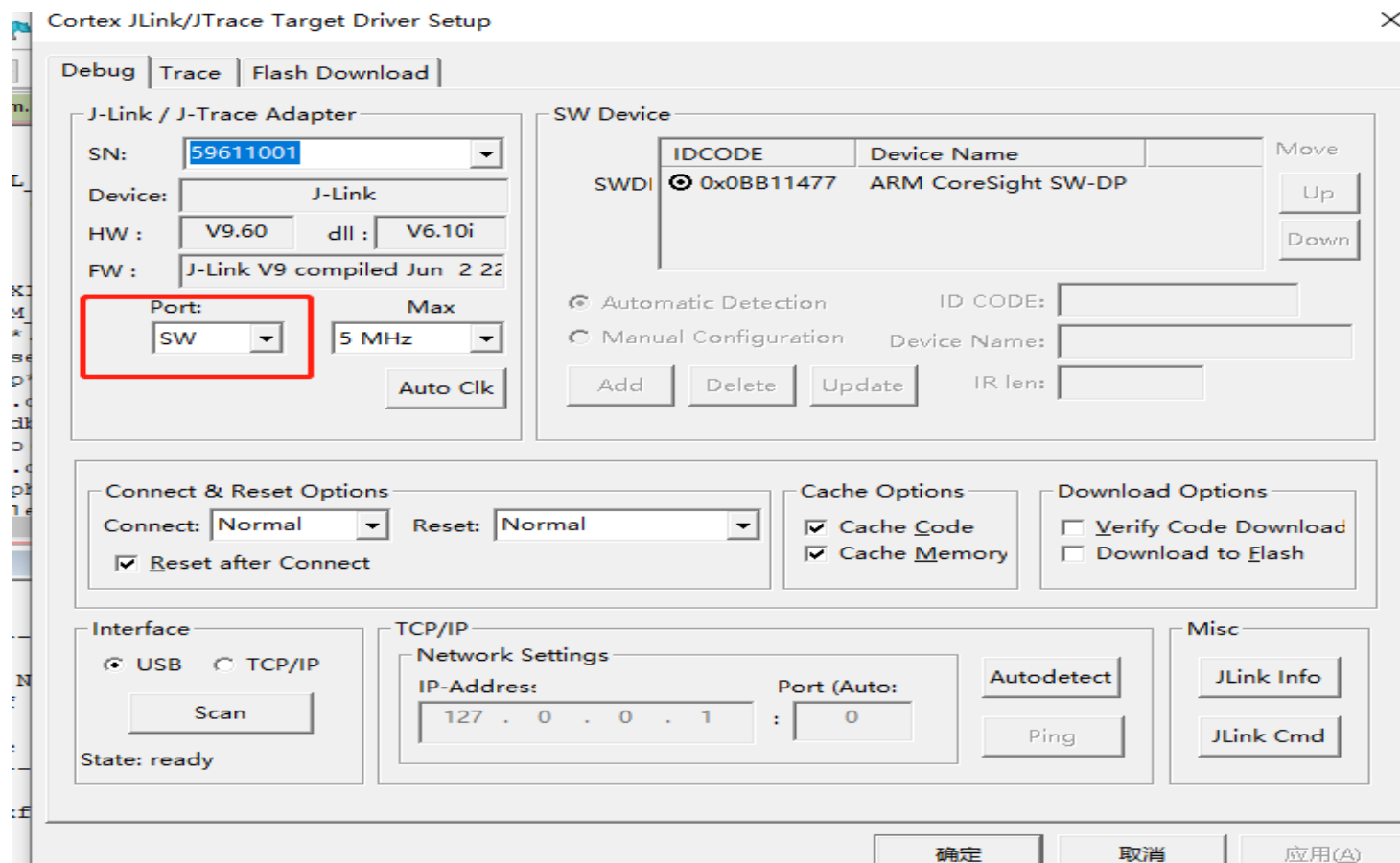
# (一)帶有XIP工程debug

5)选择debug连接设备，如下图



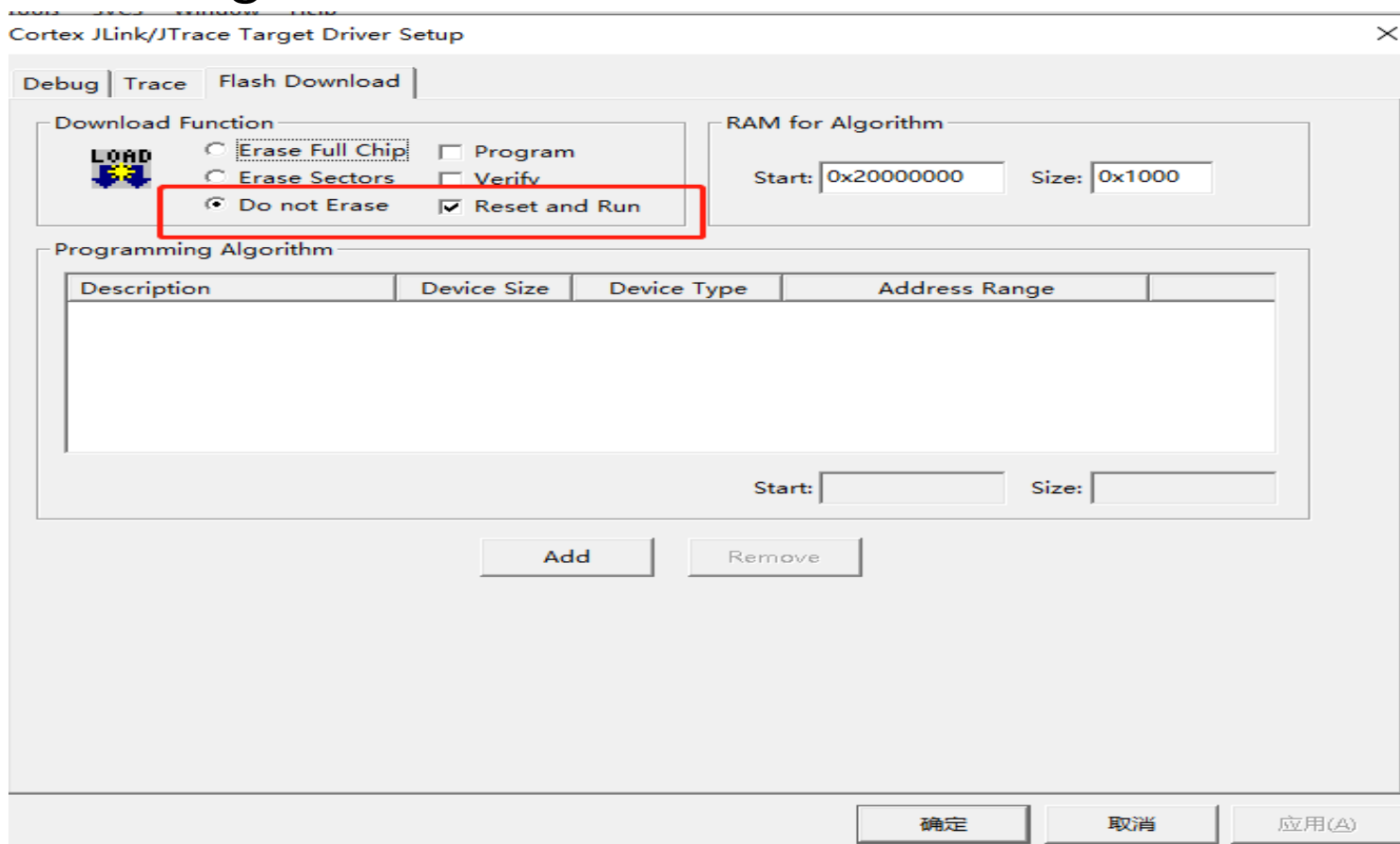
# (一)帶有XIP工程debug

6) setting,debug选项中选择“SW”



# (一)帶有XIP工程debug

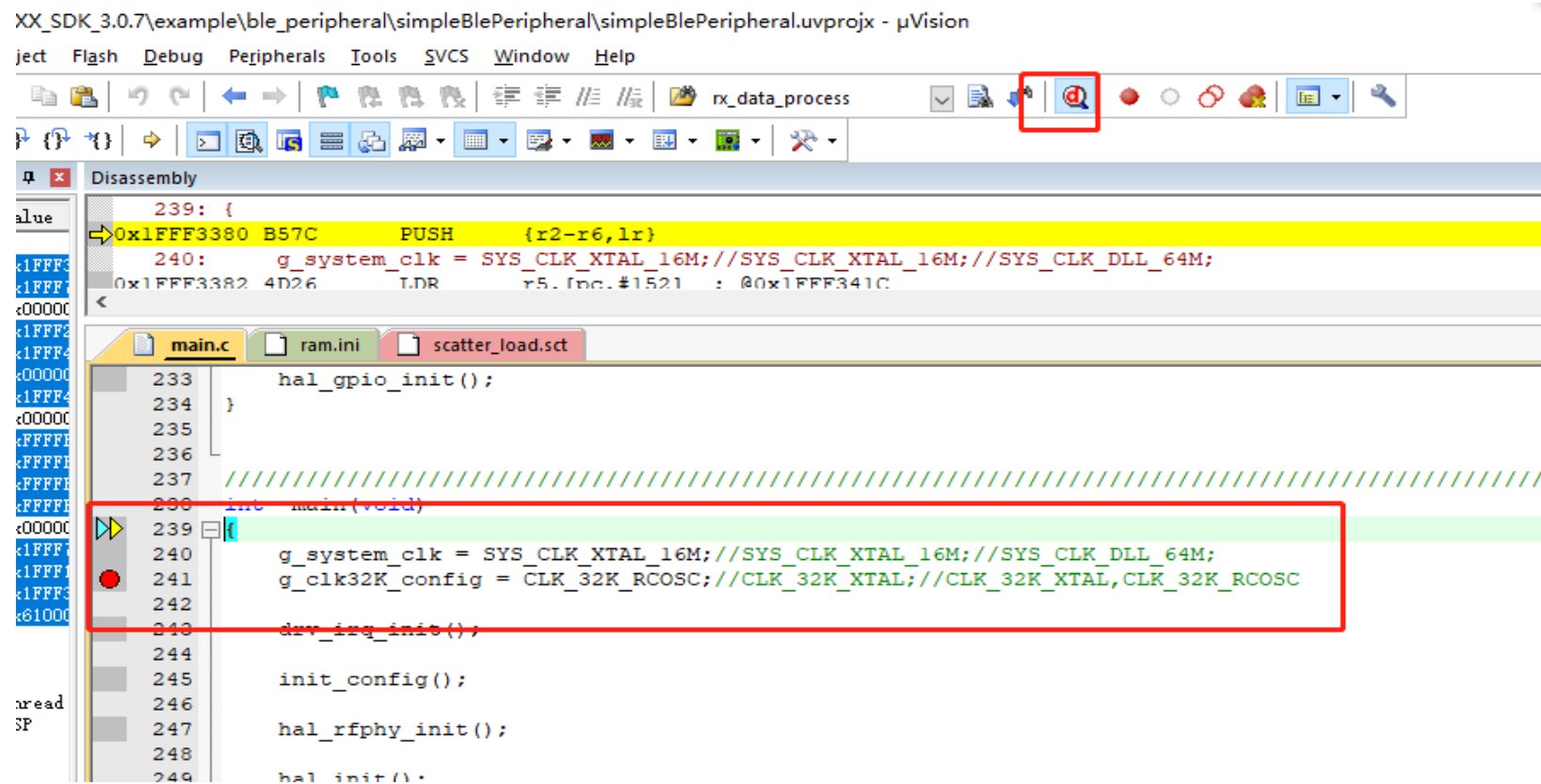
7) setting-》flash download选项如下





# (一)帶有XIP工程debug

## 8) 运行debug,run到对应的断点处



## (二)非XIP工程

非XIP工程是直接在RAM 里run，通过SWD debug的时候，.ini文件选则ram.ini文件，不需要hexf文件。其他参考上面带xip工程的debug步骤。