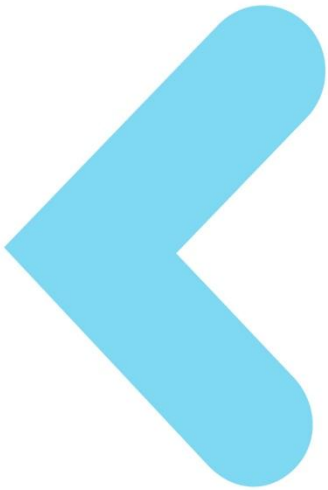
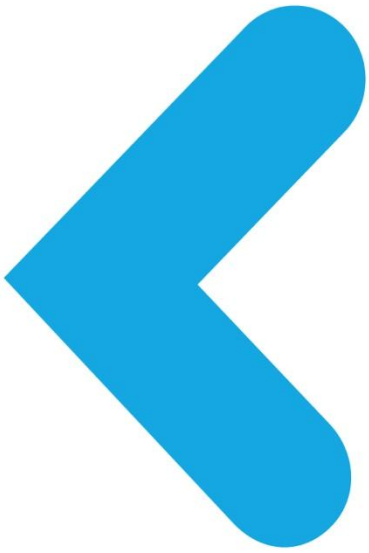
TRUSTONIC



‹t-base Driver Developer's Guide

DEVELOPER'S GUIDE



PREFACE

VERSION HISTORY

Version	Date	Modification
1.0	26 Feb 2013	First version
2.0	November 19th 2013	Updated for <t-base-300

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1 INTRODUCTION

This Developer's Guide is a practical introduction for developing Secure Drivers for <t-base.

This guide provides an overview of the different sets of API available and explains how to use the <t-ddk.

The full API for Secure Drivers can be found in the `<t-base Driver API Documentation`.

2 <t-base PRODUCT OVERVIEW

<t-base is a portable and open Trusted Execution Environment (TEE) aiming at executing Trusted Applications on a device. It includes also built-in features like cryptography or secure objects. It is a versatile environment that can be integrated on different System on Chip (SoC) supporting the ARM TrustZone technology.

<t-base uses ARM TrustZone to separate the platform into two distinct areas, the Normal-World with a conventional rich operation system and rich applications and the Secure-World.

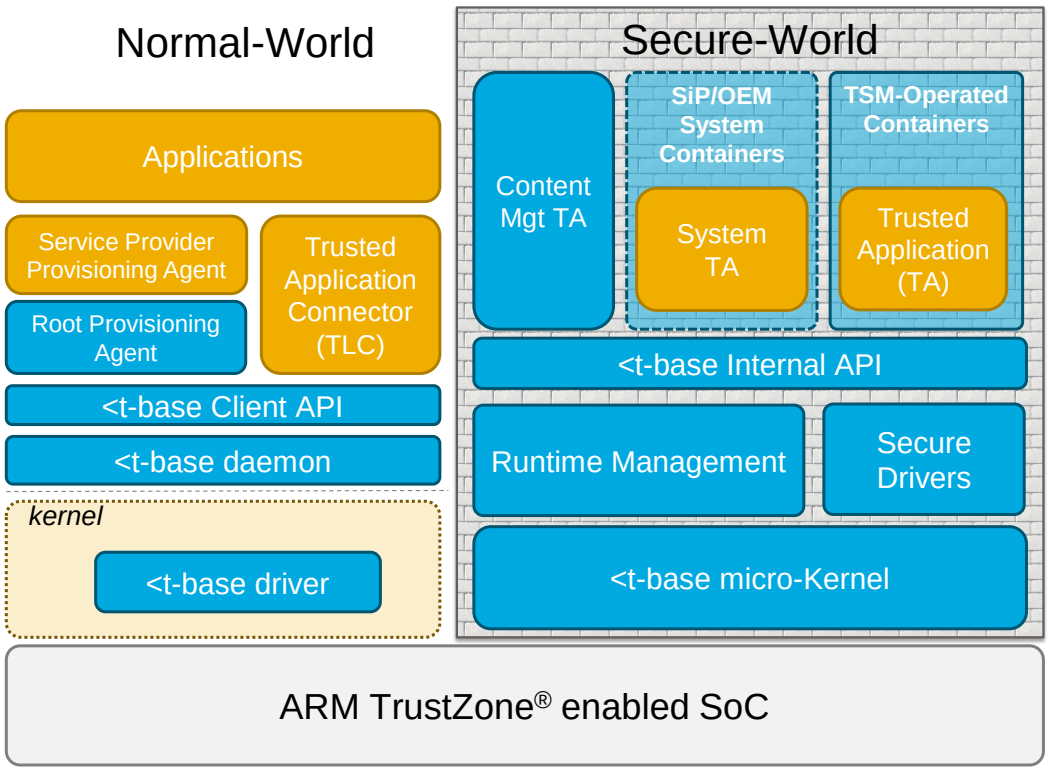

Figure 1: <t-base Architecture Overview.

The Secure-World contains essentially the <t-base core operating system and the Trusted Applications. It provides security functionality to the Normal-World with an on-device client-server architecture. The Normal-World contains mainly software which is not security sensitive (the sensitive code should be migrated to the Secure-World) and it calls the Secure-World to get security functionality via a communication mechanism and several APIs provided by <t-base. The caller in the Normal-World is usually an application, also called a client.

The Trusted Applications in the Secure-World are installed in Containers. A Container is a security domain which can host several Trusted Applications controlled by a third party. There are two kinds of Containers:

- The TSM-Operated Containers, which are created at runtime under the control of Trustonic. Trusted Applications of a TSM-Operated Container can be administrated Over-The-Air via a Trusted Service Manager (TSM).
- The System Container, which is pre-installed at the time of manufacture along with some Trusted Applications. Trusted Applications in the system container cannot be downloaded or updated Over-The-Air via a TSM.

The root Provisioning Agent is a Normal-World component which communicates between the Device and TRUSTONIC's backend system to create TSM-Operated Containers within <t-base at runtime.

Note that in order to enable <t-base and the TSM-Operated Containers on a Device, the OEM must install Trustonic's Key Provisioning Host (KPH) at its manufacturing line. The KPH is a tool which injects a key on the device and which stores a copy in the Trustonic backend system.

<t-base provides APIs in the Normal-World and the Secure-World. In the Normal-World, the <t-base Client API is the API to communicate from the Normal-World to the Secure-World. This API enables to establish a communication channel with the Secure-World and send commands to the Trusted Applications. It enables also to exchange some memory buffers between the Normal-World and the Trusted Applications.

In the Secure-World, <t-base provides the <t-base Internal API which is the interface to be used for the development of Trusted Applications.

Furthermore, the <t-base architecture supports Secure Drivers to interact with any secure peripherals.

2.1 <T-BASE API FOR DRIVER DEVELOPERS

<t-base is an open environment which provides API for developers to develop Trusted Applications and Client Applications.

<t-base supports Secure Drivers to address security peripherals in order to enable built in features such as DRM or Trusted User Interface. Secure Drivers can also be used to provide additional custom features to Trusted Applications.

Secure Drivers are developed using the DrAPI.

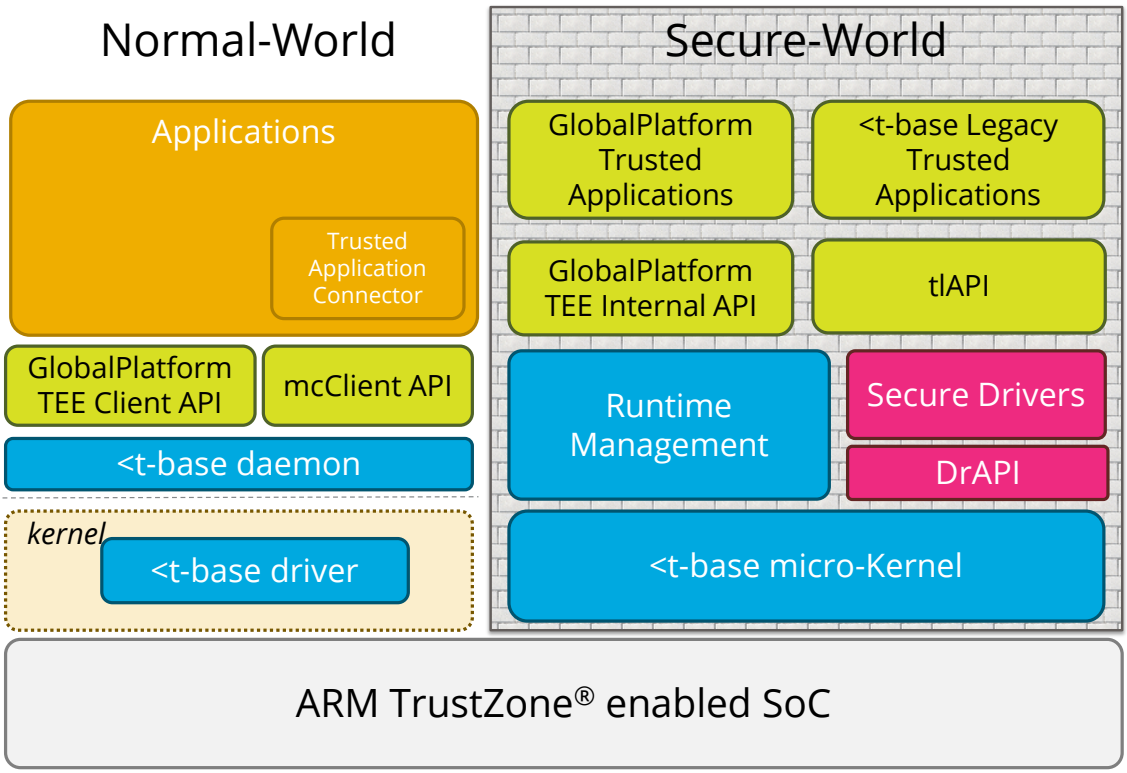

Figure 2: <t-base API Overview.

3 ABOUT SECURE DRIVERS

A Secure Driver enables a Trusted Application to access peripherals in a controlled and coordinated manner. Secure Drivers hide the hardware complexity from the Trustled Applications.

<t-base uses a microkernel architecture and as such Secure Drivers are implemented as separate processes. Basically a Secure Driver is a user mode task similar to a Trusted Application. However a Secure Driver has different interface to <t-base which allows accessing the hardware.

3.1 LOADING A SECURE DRIVER

<t-base supports dynamic loading of Secure Drivers. Secure Drivers are loaded and authenticated into <t-base in the same way as the System Trusted Applications. Alternatively Secure Drivers can be loaded by running <t-base Normal World daemon with the '-r' option.

3.2 SECURE DRIVER INTERFACES

Secure Drivers can use more system calls than Trusted Applications and thereby control hardware. The <t-base system calls are available via the Driver API. Drivers cannot currently talk to each other.

The mechanism for communicating between Secure Drivers and Trusted Applications is based on message-passing IPC with marshalling of shared-memory.

3.3 LIMITATIONS

Note the following limitations concerning the usage of Secure Drivers:

- Trusted Applications cannot use the Driver API, and cannot access hardware directly.
- Secure Drivers cannot use the API for Trusted Applications.
- Secure Drivers cannot communicate between each other.

Most of the use cases will have an architecture that consists of a Client Application, a Trusted Application and a Secure Driver. The Client Application orchestrates everything and exchanges encrypted objects with the Trusted Application. The Trusted Application will decrypt them and pass commands to the Secure Driver. The Secure Driver will take commands from the Trusted Application and transfer data in and out of the device.

3.4 SECURE DRIVER FEATURES AND SECURITY

3.4.1 Processing Environment

A Secure Driver runs in a special virtual address space that separates it from other Secure Drivers, from Trusted Applications, from <t-base kernel and runtime, and from device memory.

Secure Drivers can only use the Driver Api.

3.4.2 Memory Management

To access device registers and peripheral memory, a Secure Driver can map physical memory into its virtual address space using the Driver API.

- A Secure Driver can map physical memory to access device registers
- A Secure Driver can map secure memory regions
- A Secure Driver can translate its own virtual address to physical address by using a dedicated API mentioned below.
- A Secure Driver can do L1 d-cache clean/clean invalidate operations.
- To access data residing in Trusted Application memory, a Secure Driver may map the entire Trusted Application memory to its virtual address space. This is required for parameter passing to and from a Trusted Application. There is a function in Driver API that can be used for this purpose.

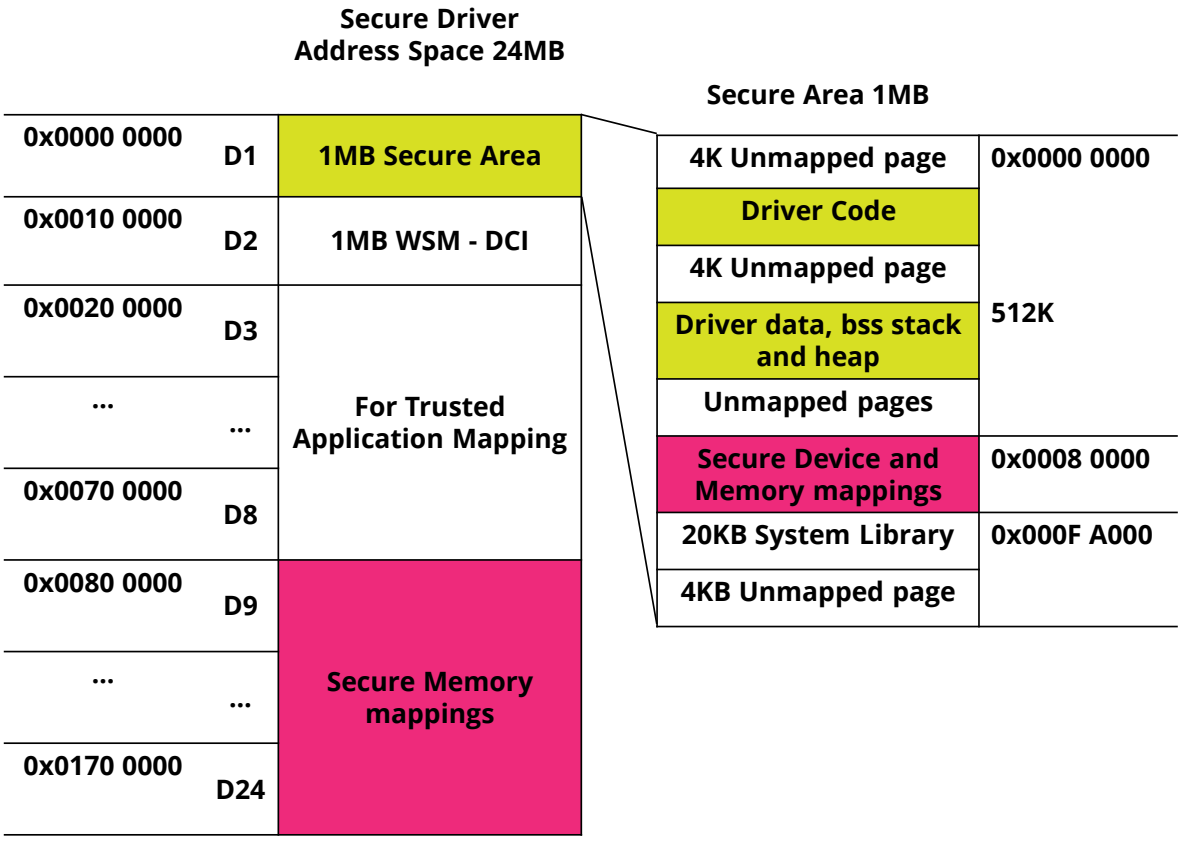

Figure 3: Secure Driver Virtual Address Space.

3.4.3 IPC

The communication between a Trusted Application and a Secure Driver must be done with an IPC. The Driver IPC API can only be used to pass notifications with one register of payload. Usually, this payload is a pointer to a buffer in the Trusted Application address space. Command IDs and data structures can be used to marshal call parameters onto this buffer.

3.4.4 Parameter Verification

A Secure Driver must validate all data that it gets from a Trusted Application. For example pointers to memory areas must be verified.

All given length parameters must be checked to not to exceed memory areas.

3.4.5 Interrupts, Threads, Exceptions and Messages

A Secure Driver starts off with one thread.

To wait for an IPC, a Secure Driver must call a blocking Driver API function.

To wait for interrupts, a Secure Driver must call a blocking Driver API function.

A Secure Driver can create multiple threads to allow waiting for various events.

A Secure Driver can send IPC between threads for synchronization.

In addition to IPC from Trusted Applications, a Secure Driver also has to handle system messages (e.g. Trusted Application close messages received from <t-base)

A Secure Driver can handle exceptions in its threads. Therefore an exception handler thread must be installed that waits for exceptions and restarts the failing threads.

3.4.6 Reliability and Security

Secure Drivers are secure critical components and should not allow an attacker to compromise <t-base security. In particular:

- Secure Drivers should avoid complexity and large size.
- Secure Drivers should react to all system messages appropriately and in a timely manner.
- Secure Drivers should avoid polling.
- Secure Drivers should not map in memory from <t-base.
- Secure Drivers should not allow a Trusted Application to access arbitrary memory.

3.4.7 Instances and Sessions

There can be only one instance of each Secure Driver.

A Secure Driver exports a Driver ID and Trusted Application can call this Driver ID.

A Secure Driver can handle only one Trusted Application at a time. A Secure Driver can implement sessions for Trusted Applications and use Trusted Application ID.

4 IMPLEMENTING A SECURE DRIVER

A Secure Driver consists of:

- A main thread that works as an exception handler thread
- An IPC handler thread for handling IPC messages from Trusted Applications and system messages from <-base
- An ISR handler thread if there is a need to attach to an interrupt and wait for that interrupt to occur

4.1 DRIVER MAIN THREAD/EXCEPTION HANDLER THREAD

Depending on the purpose of the Secure Driver, the main thread should implement at least following functionalities:

- Initialization of the task/thread control
- Starting IPC and ISR handler threads
- Implementing exception handler loop for handling exceptions caused by other local threads and restarting them when possible
- Taking care of needed HW initialization
- Support for power saving mode whenever the task is idle

4.2 IPC HANDLER THREAD

The task of the IPC handler thread is to handle incoming IPC messages and process them accordingly.

When IPC thread starts, it needs to send ready message ('MSG_RD' notification) to <t-base (only when called first time) and then wait IPC messages by calling the following Driver API:

```
drApiResult_t drApiIpcCallToIPCH(
    threadid_t        *pIpcPeer,
    message_t         *pIpcMsg,
    uint32_t          *pIpcData
);
```

The following is usage example:

```
    for (;;)
    {
        /*
         * When called first time sends ready message to IPC server
and
         * then waits for IPC requests
         */
        if (E_OK != drApiIpcCallToIPCH(&ipcClient, &ipcMsg, &ipcData))
```

```
        {
            continue;
        }

        /* Dispatch request */
        switch (ipcMsg)
        {
            case MSG_CLOSE_TRUSTED APPLICATION:
                /**
                 * Trusted Application close message
                 */
                ipcMsg = MSG_CLOSE_TRUSTED APPLICATION_ACK;
                ipcData = TLAPI_OK;
                break;
            case MSG_CLOSE_DRIVER:
                /**
                 * Driver close message
                 */
                ipcMsg = MSG_CLOSE_DRIVER_ACK;
                ipcData = TLAPI_OK;
                break;
            case MSG_GET_DRIVER_VERSION:
                /**
                 * Driver version message
                 */
                ipcMsg = (message_t) TLAPI_OK;
                ipcData = DRIVER_VERSION  ;
                break;
            case MSG_RQ:

                /* init tlRet value */
                tlRet = TLAPI_OK;

                /**
                 * Handle incoming IPC requests via TL API.
                */
                pMarshal=
(drMarshalingParam_ptr)drApiMapClientAndParams(
                                                ipcClient,
                                                ipcData);

                if (pMarshal)
                {
                    /* Process the request */
                    switch (pMarshal->functionId)
                    {
                    case FID_DR_SAMLE01:
                        /**
                         * Handle Sample01 request accordingly
                         */
                    default:
                        /* Unknown message has been received*/
                        tlRet = E_TLAPI_UNKNOWN_FUNCTION;
                        break;
                    }
```

```
                }

                /* Update response data */
                ipcMsg  = MSG_RS;
                ipcData = tlRet;
                break;
            default:
                /* Unknown message has been received*/
                ipcMsg  = MSG_RS;
                ipcData = E_TLAPI_DRV_UNKNOWN;
                break;
        }
    }
```

The *DrApiIpcCallToIPCH* provides the Trusted Application Task ID in pIpcPeer parameter and one word of payload from the Trusted Application in the pIpcData parameter. Common usage of this parameter is to point to a data structure with encoded call parameters. A Secure Driver can then map the Trusted Application into its address space and access the data structure.

4.2.1 Accessing Trusted Application data

A Secure Driver can map a Trusted Application memory into its own memory space with Driver API memory mapping functions. This allows the Secure Driver to interpret and use data referenced using pointers in a marshaled structure.

Security Considerations

Secure Drivers must be especially careful when handling such data coming from the Trusted Application memory: the content of the Trusted Application memory may change at any time, even between two consecutive memory accesses by the Secure Driver. This means that the Secure Driver should be carefully written to avoid any security problem if this happens. If values in the buffer are security critical, the Secure Driver should always read data only once from a shared buffer and then validate it. It must not assume that data written to the buffer can be read unchanged later on.

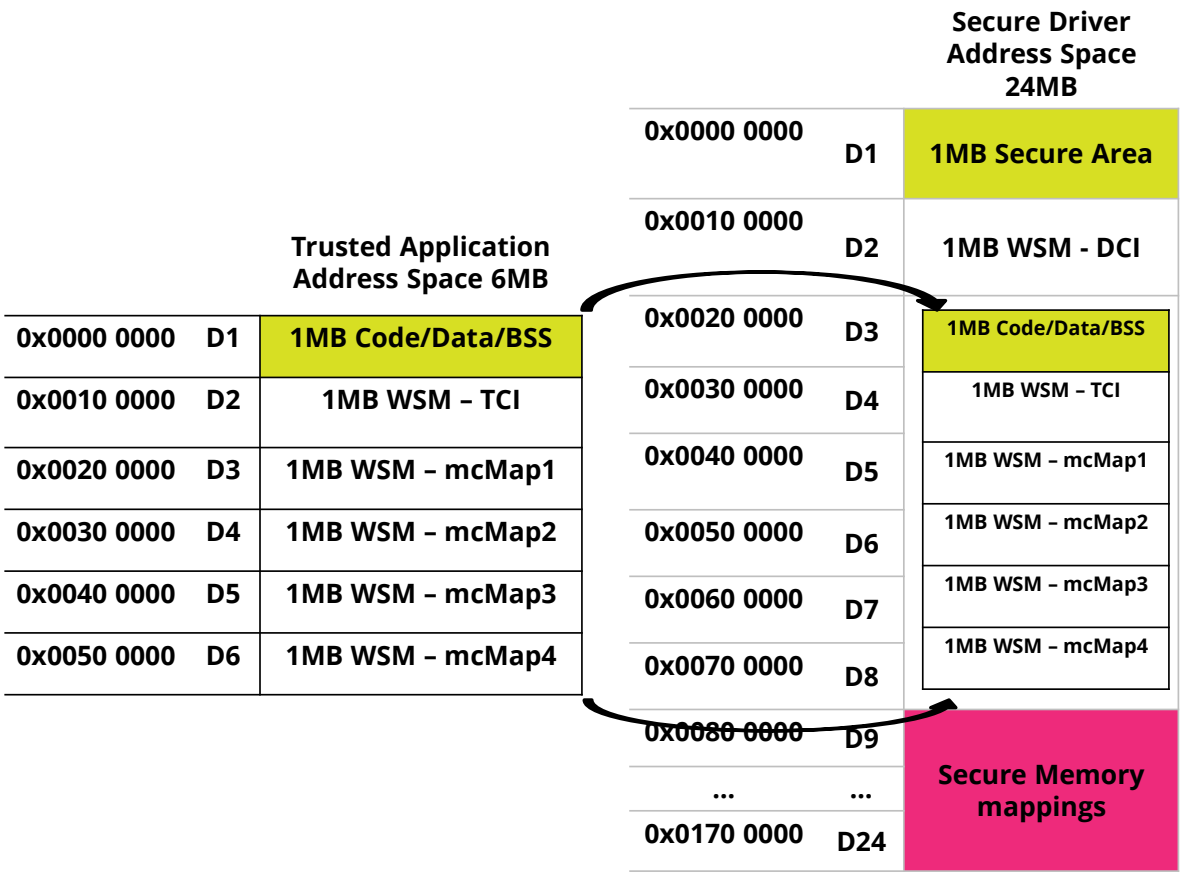

Figure 4: Figure 5: Mapping a Trusted Application into the Secure Driver

Parameters can be mapped from the Trusted Application memory space to the Secure Driver memory space with Driver API function:

```
drApiMarshalingParam_ptr drApiMapClientAndParams(
    threadid_t  ipcReqClient,
    uint32_t    params
);
```

Sample usage:

```
pMarshal= (drMarshalingParam_ptr)drApiMapClientAndParams(
                                        ipcClient,
                                        ipcData);
```

Trusted Application data will have different address in the Secure Driver and in the Trusted Application. This means that all Trusted Application data pointers must be converted before using them in the Secure Driver. The following Driver API function can be used to convert Trusted Application provided pointer to Secure Driver address space. The function returns NULL, if pointer is not in correct range. Such pointer does not point to Trusted Application virtual memory space.

```
 addr_t drApiAddrTranslateAndCheck(addr_t addr);
```

4.2.2 Unmapping Trusted Application

When the Secure Driver responds to the Trusted Application using *DrApiIpcCallToIPCH,* the IPCH will automatically unmap the Trusted Application from the Secure Driver.

4.3 VIRTUAL/PHYSICAL ADDRESS TRANSLATION

Secure Drivers can translate a virtual address (from its own address space) to physical address by using the following Driver API:

```
drApiResult_t drApiVirt2Phys(
        const taskid_t      taskid,
        const addr_t        virtAddr,
        addr_t *            physAddr
);
```

As a result physical address is returned upon success.

4.4 HOW TO MAP PHYSICAL MEMORY

In order to access device registers, peripheral memory or any other memory region, Secure Drivers map physical memory into its virtual address space. The following Driver API function can be used for that purpose:

```
drApiResult_t drApiMapPhys(
    const addr_t      startVirt,
    const uint32_t    len,
    const addr_t      startPhys,
    const uint32_t    attr
);
```

Secure Drivers can do unmapping by using the following Driver API:

```
drApiResult_t drApiUnmap(
    const addr_t    startVirt,
    const uint32_t  len
);
```

The following is a sample of how to map memory:

```
#define DR_VA_BUFFER (0x80000)
#define PHYS_MEM (0x12345678)

if (E_OK != drApiMapPhys(
             (page4KB_ptr) DR_VA_BUFFER,
             SIZE_4KB,
             (page4KB_ptr)(PHYS_MEM & ~(SIZE_4KB - 1)),
            MAP_READABLE | MAP_WRITABLE | MAP_UNCACHED))
    {
        /* Mapping failed */
    }
```

As a result 4KB memory starting from physical address *0x12345000* is mapped to virtual address *0x80000*.

Device and memory mapping attributes are listed below:

```
/* mapping does/shall have the ability to do read access. */
#define MAP_READABLE            (1U << 0)

/* mapping does/shall have the ability to do write access. */
#define MAP_WRITABLE            (1U << 1)

/* mapping does/shall have the ability to do program execution. */
#define MAP_EXECUTABLE          (1U << 2)

/* mapping does/shall have uncached memory access. */
#define MAP_UNCACHED            (1U << 3)

/* mapping does/shall have memory mapped I/O access.
#define MAP_IO                  (1U << 4)
```

Secure Drivers should map devices (peripheral registers) into the designated device mappings area at address 0x00080000 - 0x00E0000. Secure Drivers can map secure memory between 0x0080 0000 and 0x0180 0000. See section 3.4.2. Note that a Secure Driver has to manage its own virtual address space if it needs to map several devices or memory ranges.

4.5 HOW TO USE THREADS

When Secure Driver starts, it initially has a single thread called main thread. The main thread can start other threads by calling the following function:

```
drApiResult_t drApiStartThread(
    const threadno_t    threadNo,
    const addr_t        threadEntry,
    const stackTop_ptr  stackPointer,
    const uint32_t      priority,
    const threadno_t    localExceptionHandlerThreadNo
);
```

The following is a sample usage:

```
/* Priority definitions */
#define EXCH_PRIORITY       MAX_PRIORITY
#define IPCH_PRIORITY       (MAX_PRIORITY-1)
#define ISRH_PRIORITY       (MAX_PRIORITY-1)
…
…
/* Thread numbers */
#define DRIVER_THREAD_NO_EXCH      1
#define DRIVER_THREAD_NO_IPCH      2
#define DRIVER_THREAD_NO_ISRH      3

…
…
    if (E_OK != drApiStartThread(
```

```
                    DRIVER_THREAD_NO_IPCH,
                    FUNC_PTR(drIpch),
                    getStackTop(drIpchStack),
                    IPCH_PRIORITY,
                    DRIVER_THREAD_NO_EXCH))
    {
        /* Starting thread failed*/
    }
```

In this particular example, exception handler thread (thread #1) is registered as local excepting handler for IPC handler thread, meaning that if IPC handler thread causes any exception, the exception handler thread will be notified. If exception is something that can be recovered, the exception handler thread can restart the thread.

4.6 HOW TO HANDLE EXCEPTIONS

As stated earlier, the main thread can act as exception handler. Once it is registered as local exception handling of other threads while starting them, it will receive notifications when exceptions caused by other local threads occur. The following is a sample code that can be used to handle exceptions and restart exception causing thread:

```
/* Kernel exceptions */
#define TRAP_UNKNOWN         ( 0)    /* unknown exception. */
#define TRAP_SYSCALL         ( 1)    /* invalid syscall number. */
#define TRAP_SEGMENTATION    ( 2)    /* illegal memory access. */
#define TRAP_ALIGNMENT       ( 3)    /* misaligned memory access. */
#define TRAP_UNDEF_INSTR     ( 4)    /* undefined instruction. */
…
…
   for (;;)
    {
        /* Wait for exception */
        if ( E_OK != drApiIpcWaitForMessage(
                    &ipcPartner,
                    &mr0,
                    &mr1,
                    &mr2) )
        {
            /* Unable to receive IPC message */
            continue;
        }

        /*
            *mr0 holds threadid value of thread
            * that caused the exception
            */
        faultedThread = GET_THREADNO(mr0);

        /* Process exception */
        switch(mr1)
        {
            //--------------------------------------
            case TRAP_SEGMENTATION:
                /* Check which thread caused exception */
```

```
                switch(faultedThread)
                {
                    //-------------------------------------
                    case DRIVER_THREAD_NO_IPCH:
                        /* Update sp and ip accordingly */
                        ip = FUNC_PTR(drIpchLoop);
                        sp = getStackTop(drIpchStack);

                        /* Resume thread execution */
                        if (E_OK != drUtilsRestartThread(
                                        faultedThread,
                                        ip,
                                        sp))
                        {
                           /* failed */
                        }

                        break;
                    //-------------------------------------
                    default:
                        /* Unknown thread*/
                        break;
                }

                break;
            //-------------------------------------
            case TRAP_ALIGNMENT:
            case TRAP_UNDEF_INSTR:
                /**
                 * This should never happen.
                       * If it does, do the cleanup and exit gracefully
                 */
                break;
            //-------------------------------------
            default:
                /**
                 * Unknown exception occurred.
                 */
                break;
        }
    }
```

The implementation of '*drUtilsRestartThread()*' given in the above sample can be seen below. It uses '*drApiThreadExRegs()*' and '*drApiResumeThread()*' Driver API functions for restarting thread.

```
drApiResult_t drUtilsRestartThread(
    threadno_t threadNo,
    addr_t ip,
    addr_t sp )
{
    drApiResult_t ret  = E_INVALID;
    uint32_t      ctrl = THREAD_EX_REGS_IP | THREAD_EX_REGS_SP;
```

```
    /* Set ip and sp registers */
    ret = drApiThreadExRegs(threadNo,
                            ctrl,
                            ip,
                            sp);
    if (ret != E_OK)
    {
        return ret;
    }

    /* Resume thread */
    ret = drApiResumeThread(threadNo);

    return ret;
}
```

4.7 HOW TO HANDLE INTERRUPTS

In <t-base, interrupt service routines are run in their own threads. It means that a dedicated thread needs to be started for handling ISR. This can be done from the Secure Driver main thread by calling Driver API function *drApiStartThread()*. The following is a sample code:

```
    if (E_OK != drApiStartThread(
                    DRIVER_THREAD_NO_ISRH,
                    FUNC_PTR(drIsrh),
                    getStackTop(drIsrhStack),
                    ISRH_PRIORITY,
                    DRIVER_THREAD_NO_EXCH))
    {
        /* Starting thread failed*/
    }
```

In order to be able to use interrupts they need to be attached to the interrupt handler. This can be done by calling following Driver API function:

```
drApiResult_t drApiIntrAttach(
    intrNo_t intrNo,
    intrMode_t intrMode
);
```

In most cases, the mode parameter will be INTR_MODE_RAISING_EDGE, as interrupts usually indicate that a certain event has happened.

After attaching to an interrupt, you will also need to implement your own waiting loop for getting notified when interrupt occurs.

The following API is used to wait for an interrupt:

```
drApiResult_t drApiWaitForIntr(
    const intrNo_t  intrNo,
    const uint32_t  timeout,
    intrNo_t        *pIntrRet
);
```

When done, you can detach from a particular interrupt by calling the following Driver API function:

```
drApiResult_t drApiIntrDetach(
      intrNo_t intrNo
);
```

Normally interrupt handling thread is never terminated.

4.7.1 Communication between ISR thread and other local threads

As threads share the same address space there are several methods to implement synchronization and communication between threads. The method to be used should be selected based on the use case scenario the threads are to be used.

Parameters to ISR routine and back should be delivered via global variables. It is not possible to use IPC mechanism in interrupt service loop.

Sending events from ISR to other local threads can be done by using IPC signaling mechanism.

4.8 HOW TO USE SIGNALING FUNCTIONS

The following Driver API function can be used to signal en event to other drover threads:

```
drApiResult_t drApiIpcSignal(
    const threadid_t  receiver
);
```

A signal operation is asynchronous which means that the operation will return immediately without blocking the caller.

Receiver thread can call the following blocking function to receive signal:

```
drApiResult_t drApiIpcSigWait( void );
```

If no signal is pending the caller thread is blocked until a signal arrives.

5 USING A SECURE DRIVER

5.1 HOW TO INSTALL A SECURE DRIVER

Secure Drivers are handled like Trusted Applications and have a UUID and .tlbin file ending. Secure Drivers have to be placed in '*/data/app/mcRegistry*' folder on the device.

5.2 HOW TO LOAD A SECURE DRIVER

There are two options to load a Secure Driver:

- Secure Driver can be loaded by <t-base normal world daemon on startup. This is done by running <t-base daemon with '-r' option. In that case, the Secure Driver must have **.drbin* file name ending. The following is an example of the call:

  ./mcDriverDaemon -r /data/app/mcRegistry/01020304050000000000000000000000.drbin
- Secure Driver can be loaded dynamically by a TLC by simply opening a session and providing UUID of the Driver.

It is not possible for Trusted Applications to load Secure Drivers dynamically.

5.3 HOW TO CALL A SECURE DRIVER

A Trusted Applications can call a Secure Drivers through the `tlApi_callDriver()` function.

```
tlApiResult_t tlApi_callDriver(
      const uint32_t driver_ID,
      void* pMarParam)
```

It is recommended that Secure Driver developers provide a dedicated API and library to the Trusted Applications, which can be statically linked to the Trusted Application code.

This allows hiding the calls to the Secure Driver and parameter marshalling into a functional API.

The following illustrates a sample API implementation:

```
int32_t sample_api(
    uint8_t  *src,
    uint32_t  srclen )
{
    drMarshalingParam_t marParam = {
            .functionId = FID_DR_SAMPLE,
            .payload    = {
                        .sampleData = {
                                .src     = src,
```

```
                                    .dst     = dst,
                                    .srclen  = srclen,
                                }
            }
    };

    return tlApi_callDriver(SAMPLE_DR_ID, &marParam);
}
```

The function `tlApi_callDriver()` is a blocking call that forces Trusted Applications to wait for a response from a Secure Driver. Any data that should be sent back along with the response should be communicated via buffers allocated by the Trusted Application. This buffer can be on the stack of the Trusted Application-Secure Driver library. Pointers to these buffers can be transferred to the Secure Driver in marshaling structures when the Secure Driver is called.

6 <t-ddk

<t-ddk is the Driver Development Kit for <t-base. It provides documentation, libraries and sample code to develop Secure Drivers.

6.1 DRIVER API

A Secure Driver has specific set of functions available in Driver API. Files that are included in the API are listed in the table. Please see the following header files for more information about the APIs.

Table 1: DrApi header files and libraries

File name	Details
DrApi.lib	Library file that implements the Driver API
DrEntry.lib	Library file that implements runtime environment for each Driver
drStd.h	This is a wrapper for standard header files; it also specifies macros to declare stack area.
DrApi.h	Include file to take Driver API library into use
DrApiCommon.h	Include file defining data types
DrApiError.h	Error codes to be used in Trusted Application-Driver API
DrApiIpcMsg.h	Include file for Inter Process Communication API
DrApiLogging.h	Include file for Logging API
DrApiMcSystem.h	Include file for System API
DrApiMm.h	Include file for Memory Management API
DrApiThread.h	Include file for Thread and Interrupt API
version.h	Version of Driver API including major and minor number

6.2 SECURE DRIVER TEMPLATE

DrTemplate is a template of Secure Driver that shows:

- the usage of exception handler thread
- the usage of IPC handler thread
- the usage of DCI handler thread
- sample implementation of Driver APIs
- sample session management

6.2.1 DrTemplate structure

The Secure Driver template consists of the following components

- Exception handler thread (main thread):

  Responsible for handling exceptions caused by IPC and DCI handler threads. Exception handler thread number is #1 and it has the highest priority than IPC and DCI handler threads.

- IPC handler thread:

  Responsible for handling IPC messages coming from RTM IPCH and also from other Trusted Applications that use the APIs provided by the template. IPC handler thread number is #2. If IPC handler thread receives Trusted Application and Driver close acknowledgement request from RTM IPCH, Secure Driver needs to do cleanup if necessary and acknowledge to IPCH.

- DCI handler thread:

  Responsible for handling notifications that arrive from normal world DCI (Driver Control Interface) handler. This is exactly same as how Trusted Applications communicate with normal world via TCI. Data exchange between two worlds is possible via DCI buffer. It is possible to disable DCI handler during compilation time by setting 'USE_DCI_HANDLER' to 'NO'

- Trusted Application API:

  Trusted Application API provides interface to other Trusted Applications to request services from the Secure Driver. It is possible to disable Trusted Application API interface during compilation time by setting 'USE_TL_API' to 'NO'

- Session Management:

  Session management is responsible for keeping session registry and handling session registry data. Session management implementation is not a must in a Secure Driver implementation. Driver can also handle Trusted Applications without maintaining sessions. This will require that each execute requests will be handled at once. Session management also requires that IPC data sent by a Trusted Application is copied by the Secure Driver for later use. If session-less mode is used, Trusted Application memory can be mapped by the Secure Driver and then the Secure Driver directly accesses data.

6.2.2 What needs to be updated

You may want to:

- update file names, makefile.mk content
- update variable & structure names/members
- add/remove new structures, etc.
- add/remove new functions for handling DCI and IPC messages
- add cleanup functionality in the exception handler
- add additional threads, for example ISR handler thread
- General definitions
  - General Driver definitions can be found in '*drCommon.h*'. The definitions include thread numbers and priorities, various macros.
- Trusted Application API
  - if Trusted Application API is offered, update '*drApiMarshal.h*', '*tlDriverApi.c*' and '*tlDriverApi.h*' for defining new marshalling parameters, new function IDs, new APIs, etc..
  - You will also need to update '*drIpcHandler.c*' for handling incoming requests. As default, Trusted Application APIs are enabled, but this can be updated in makefile.mk
- DCI handler
  - If you would like to add new data structure definitions in DCI buffer, you need to modify '*dci.h*' and '*drTemplate_Api.h*'.
  - You will also need to modify '*drDciHandler.c*' for handling incoming DCI messages.
- IPC handler thread
  - Update incoming IPC message handling for functions. This may also require updates in the session management part if session management is in use.
  - You will also need to handle Trusted Application and Driver close acknowledgement requests accordingly in drIpcHandler.c and do necessary cleanup before sending any acknowledgement to IPCH
  - Update *SAMPLE_DR_ID* accordingly in '*drApiMarshal.h*'. This must be the same Driver ID value as in makefile.mk
- Exception handling
  - Update '*drExcHandler.c*' to do the cleanup before stopping threads in case of '*TRAP_UNDEF_INSTR*'.
  - After restarting IPC or DCI handler thread, you need to respond to Trusted Application or notify normal world and indicate with a valid error code if there is an outstanding request.
- Main entry point
  - You need to implement initialization functionality if your Driver needs certain initialization.
  - You can update '*doInitialization(void)*' in '*drMain.c*' with specific initialization code. For example certain HW initialization needs to be taken case before

launching Driver threads or if you would like to do physical memory mapping, etc.., this is the function where this can be handled

- Session Management
  - Session management can be optional
  - Session management is implemented in '*drSmgmt.c*'. Both function and data definitions can be found in *'drSmgmt.h*'.
  - If you would like to add new session states, you can update *'sessionState_t*.
  - If new session registry data members are required, you can update *'drSessionReg_t*' structure definition.
  - If needed, you can update both *'drSmgmt.c'* and '*drSmgmt.h*' for adding new functions for handling additional session management operations.
- makefile.mk
  - Update *DRIVER_UUID* accordingly
  - Update file names accordingly if you rename source files
  - *Update USE_DCI_HANDLER* and *USE_TL_API* default values accordingly

6.3 EXAMPLES

6.3.1 Async example

This is an example of asynchronous calls in <t-base: drApiIpcSignal function invoked in IPC thread of the Secure Driver sends a signal, which is caught by drApiIpcSigWait function in Interrupt thread, Interrupt thread runs a hardware or software timer that elapses and DrApiNotifyClient function is invoked to awake a calling Trusted Applications. The sample also contains platform dependent timer implementation, which uses assembler code, for Versatile and Arndale platforms.

6.3.2 Rot13 example

This example shows a complete calling path from the Application, Trusted Application Connector, Trusted Application, Secure Driver (without hardware access use).

6.4 BUILDING A SECURE DRIVER

In general the following command specifying chosen TOOLCHAIN, PLATFROM and MODE is used to build a driver:

```
TOOLCHAIN=GNU MODE=Debug PLATFORM=ARM_VE_A9X4_STD ./Locals/Build/build.sh
```

By default the following setting are used:

PLATFORM : ARM_VE_A9X4_STD

TOOLCHAIN : ARM

MODE : Debug