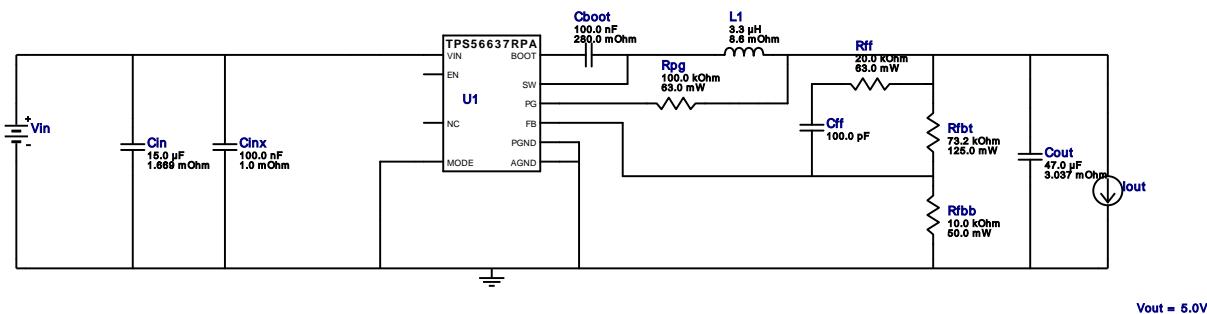


WEBENCH® Design Report

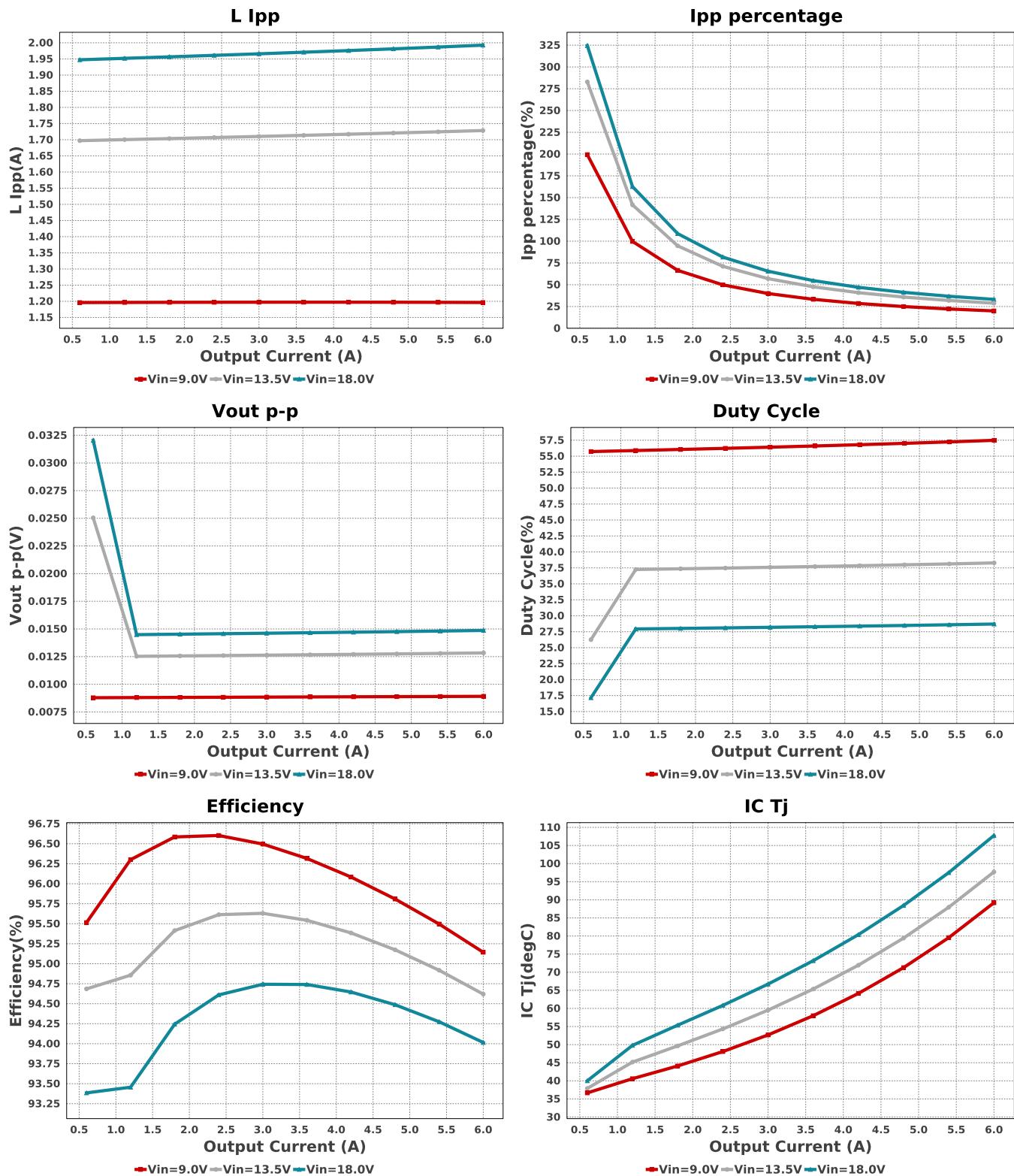
Design : 7 TPS56637RPAR
 TPS56637RPAR 9V-18V to 5.00V @ 6A

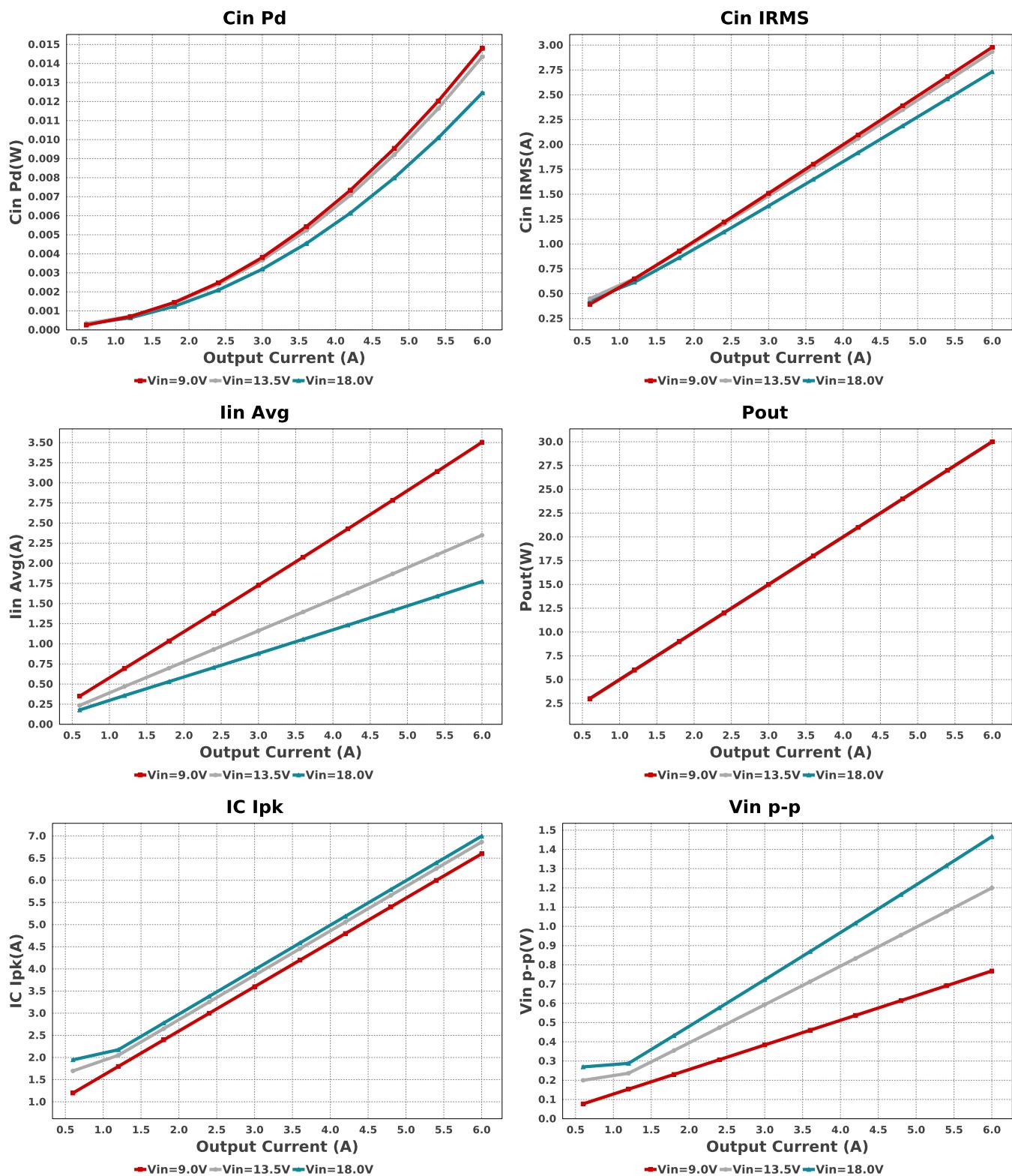
VinMin = 9.0V
 VinMax = 18.0V
 Vout = 5.0V
 Iout = 6.0A
 Device = TPS56637RPAR
 Topology = Buck
 Created = 2025-11-28 14:01:55.855
 BOM Cost = \$2.66
 BOM Count = 11
 Total Pd = 1.91W

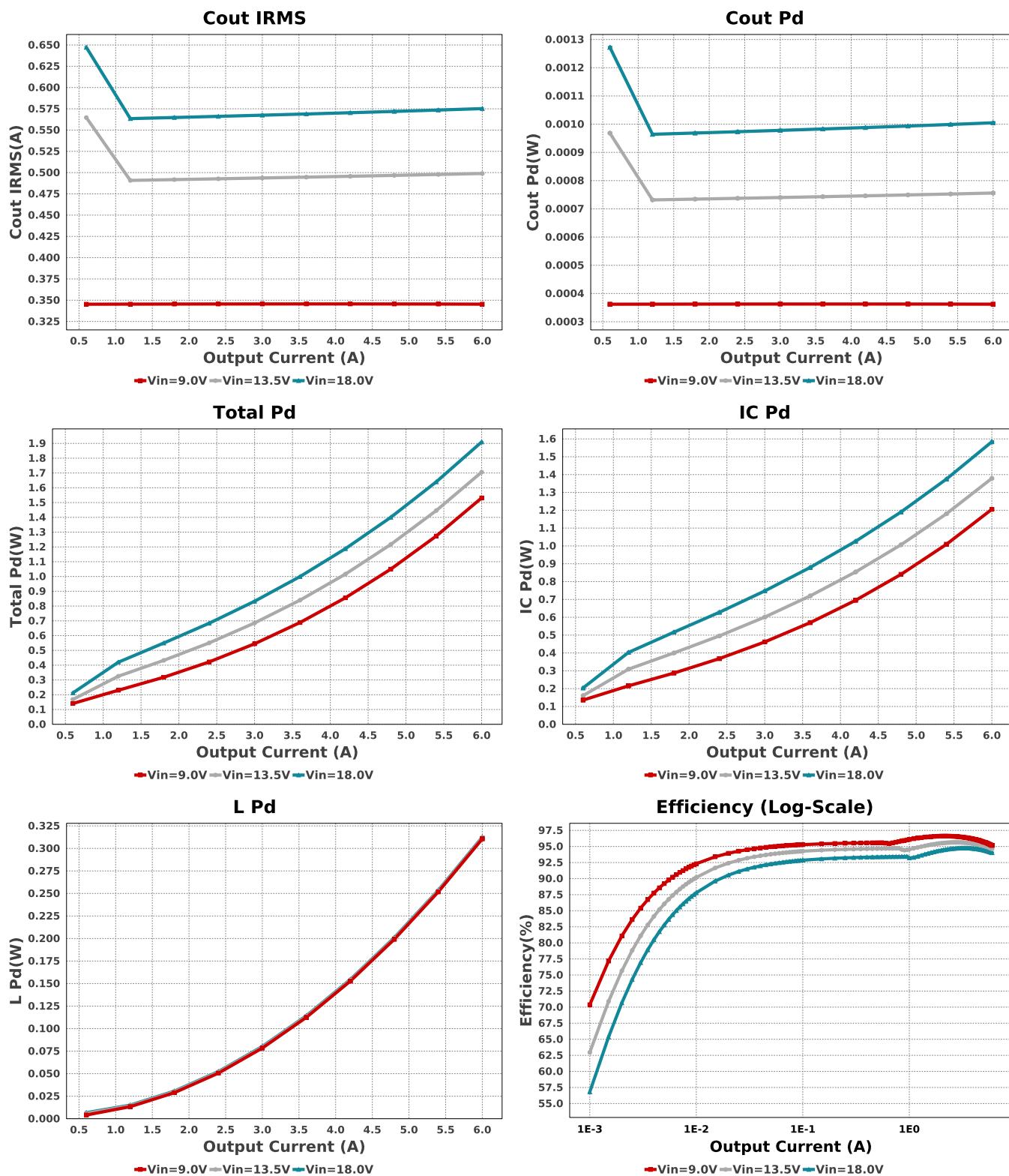


Electrical BOM

Name	Manufacturer	Part Number	Properties	Qty	Price	Footprint
Cboot	AVX	08053C104KAT2A Series= X7R	Cap= 100.0 nF ESR= 280.0 mOhm VDC= 25.0 V IRMS= 0.0 A	1	\$0.01	0805_7 mm ²
Cff	Kemet	C0201C101K3GACTU Series= C0G/NP0	Cap= 100.0 pF VDC= 10.0 V IRMS= 0.0 A	1	\$0.01	0201_2 mm ²
Cin	TDK	C2012X5R1V156M125AC Series= X5R	Cap= 15.0 uF ESR= 1.669 mOhm VDC= 35.0 V IRMS= 5.0498 A	1	\$0.20	0805_7 mm ²
Cinx	MuRata	GRM188R72A104KA35D Series= X7R	Cap= 100.0 nF ESR= 1.0 mOhm VDC= 100.0 V IRMS= 3.85 A	1	\$0.04	0603_5 mm ²
Cout	MuRata	GRM32ER61C476KE15L Series= X5R	Cap= 47.0 uF ESR= 3.037 mOhm VDC= 16.0 V IRMS= 4.59346 A	1	\$0.17	1210_280_15 mm ²
L1	Coilcraft	XAL7070-332MEB	L= 3.3 μH 8.6 mOhm	1	\$1.19	XAL7070_87 mm ²
Rfbb	Yageo	RC0201FR-0710KL Series= ?	Res= 10.0 kOhm Power= 50.0 mW Tolerance= 1.0%	1	\$0.01	0201_2 mm ²
Rfbt	Panasonic	ERJ-6ENF7322V Series= ERJ-6E	Res= 73.2 kOhm Power= 125.0 mW Tolerance= 1.0%	1	\$0.01	0805_7 mm ²
Rff	Vishay-Dale	CRCW040220K0FKED Series= CRCW..e3	Res= 20.0 kOhm Power= 63.0 mW Tolerance= 1.0%	1	\$0.01	0402_3 mm ²
Rpg	Vishay-Dale	CRCW0402100KFKED Series= CRCW..e3	Res= 100.0 kOhm Power= 63.0 mW Tolerance= 1.0%	1	\$0.01	0402_3 mm ²
U1	Texas Instruments	TPS56637RPAR	Switcher	1	\$1.00	RPA0010A_16 mm ²







Operating Values

#	Name	Value	Category	Description
1.	Cin IRMS	2.732 A	Capacitor	Input capacitor RMS ripple current
2.	Cin Pd	12.455 mW	Capacitor	Input capacitor power dissipation
3.	Cout IRMS	575.219 mA	Capacitor	Output capacitor RMS ripple current
4.	Cout Pd	1.005 mW	Capacitor	Output capacitor power dissipation
5.	IC Ipk	6.996 A	IC	Peak switch current in IC
6.	IC Pd	1.584 W	IC	IC power dissipation
7.	IC T _j	107.758 degC	IC	IC junction temperature
8.	IC Tolerance	10.0 mV	IC	IC Feedback Tolerance
9.	IC ThetaJA	49.1 degC/W	IC	IC junction-to-ambient thermal resistance
10.	Iin Avg	1.773 A	IC	Average input current

#	Name	Value	Category	Description
11.	Ipp percentage	33.21 %	Inductor	Inductor ripple current percentage (with respect to average inductor current)
12.	L Ipp	1.993 A	Inductor	Peak-to-peak inductor ripple current
13.	L Pd	312.45 mW	Inductor	Inductor power dissipation
14.	Cin Pd	12.455 mW	Power	Input capacitor power dissipation
15.	Cout Pd	1.005 mW	Power	Output capacitor power dissipation
16.	IC Pd	1.584 W	Power	IC power dissipation
17.	L Pd	312.45 mW	Power	Inductor power dissipation
18.	Total Pd	1.91 W	Power	Total Power Dissipation
19.	BOM Count	11	System Information	Total Design BOM count
20.	Duty Cycle	28.705 %	System Information	Duty cycle
21.	Efficiency	94.015 %	System Information	Steady state efficiency
22.	FootPrint	153.0 mm ²	System Information	Total Foot Print Area of BOM components
23.	Frequency	558.453 kHz	System Information	Switching frequency
24.	Iout	6.0 A	System Information	Iout operating point
25.	Mode	CCM	System Information	Conduction Mode
26.	Pout	30.0 W	System Information	Total output power
27.	Total BOM	\$2.66	System Information	Total BOM Cost
28.	Vin	18.0 V	System Information	Vin operating point
29.	Vin p-p	1.466 V	System Information	Peak-to-peak input voltage
30.	Vout	5.0 V	System Information	Operational Output Voltage
31.	Vout Actual	4.992 V	System Information	Vout Actual calculated based on selected voltage divider resistors
32.	Vout Tolerance	3.474 %	System Information	Vout Tolerance based on IC Tolerance (no load) and voltage divider resistors if applicable
33.	Vout p-p	14.866 mV	System Information	Peak-to-peak output ripple voltage

Design Inputs

Name	Value	Description
Iout	6.0	Maximum Output Current
VinMax	18.0	Maximum input voltage
VinMin	9.0	Minimum input voltage
Vout	5.0	Output Voltage
base_pn	TPS56637	Base Product Number
source	DC	Input Source Type
Ta	30.0	Ambient temperature

WEBENCH® Assembly

Component Testing

Some published data on components in datasheets such as Capacitor ESR and Inductor DC resistance is based on conservative values that will guarantee that the components always exceed the specification. For design purposes it is usually better to work with typical values. Since this data is not always available it is a good practice to measure the Capacitance and ESR values of C_{in} and C_{out} , and the inductance and DC resistance of L_1 before assembly of the board. Any large discrepancies in values should be electrically simulated in WEBENCH to check for instabilities and thermally simulated in WebTHERM to make sure critical temperatures are not exceeded.

Soldering Component to Board

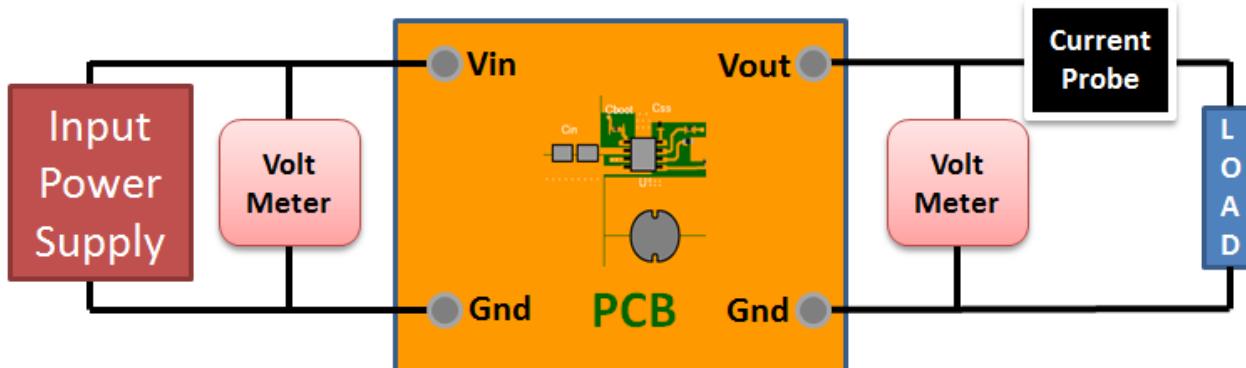
If board assembly is done in house it is best to tack down one terminal of a component on the board then solder the other terminal. For surface mount parts with large tabs, such as the DPAK, the tab on the back of the package should be pre-tinned with solder, then tacked into place by one of the pins. To solder the tab down to the board place the iron down on the board while resting against the tab, heating both surfaces simultaneously. Apply light pressure to the top of the plastic case until the solder flows around the part and the part is flush with the PCB. If the solder is not flowing around the board you may need a higher wattage iron (generally 25W to 30W is enough).

Initial Startup of Circuit

It is best to initially power up the board by setting the input supply voltage to the lowest operating input voltage 9.0V and set the input supply's current limit to zero. With the input supply off connect up the input supply to V_{in} and GND. Connect a digital volt meter and a load if needed to set the minimum I_{out} of the design from V_{out} and GND. Turn on the input supply and slowly turn up the current limit on the input supply. If the voltage starts to rise on the input supply continue increasing the input supply current limit while watching the output voltage. If the current increases on the input supply, but the voltage remains near zero, then there may be a short or a component misplaced on the board. Power down the board and visually inspect for solder bridges and recheck the diode and capacitor polarities. Once the power supply circuit is operational then more extensive testing may include full load testing, transient load and line tests to compare with simulation results.

Load Testing

The setup is the same as the initial startup, except that an additional digital voltmeter is connected between V_{in} and GND, a load is connected between V_{out} and GND and a current meter is connected in series between V_{out} and the load. The load must be able to handle at least rated output power + 50% (7.5 watts for this design). Ideally the load is supplied in the form of a variable load test unit. It can also be done in the form of suitably large power resistors. When using an oscilloscope to measure waveforms on the prototype board, the ground leads of the oscilloscope probes should be as short as possible and the area of the loop formed by the ground lead should be kept to a minimum. This will help reduce ground lead inductance and eliminate EMI noise that is not actually present in the circuit.



Design Assistance

1. Master key : AD9ABD3FB6EB8E71BDFD117B90420B13[v1]
2. **TPS56637 Product Folder** : <http://www.ti.com/product/TPS56637> : contains the data sheet and other resources.

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