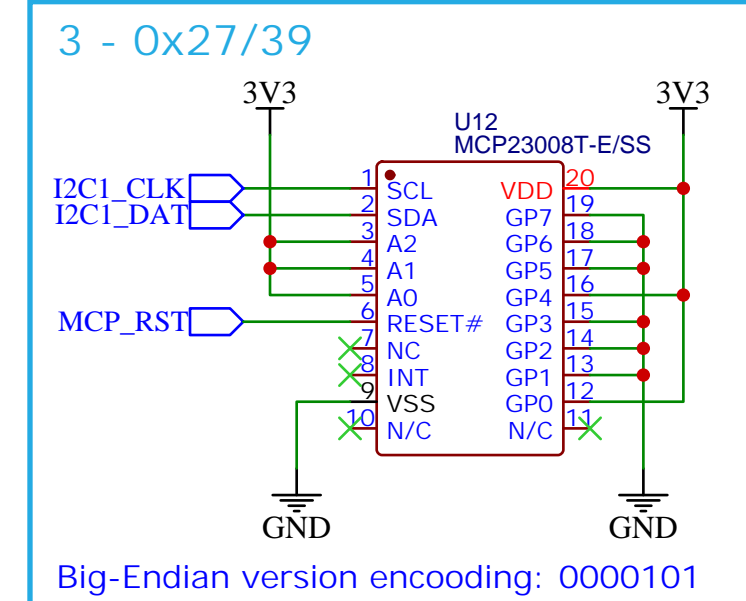
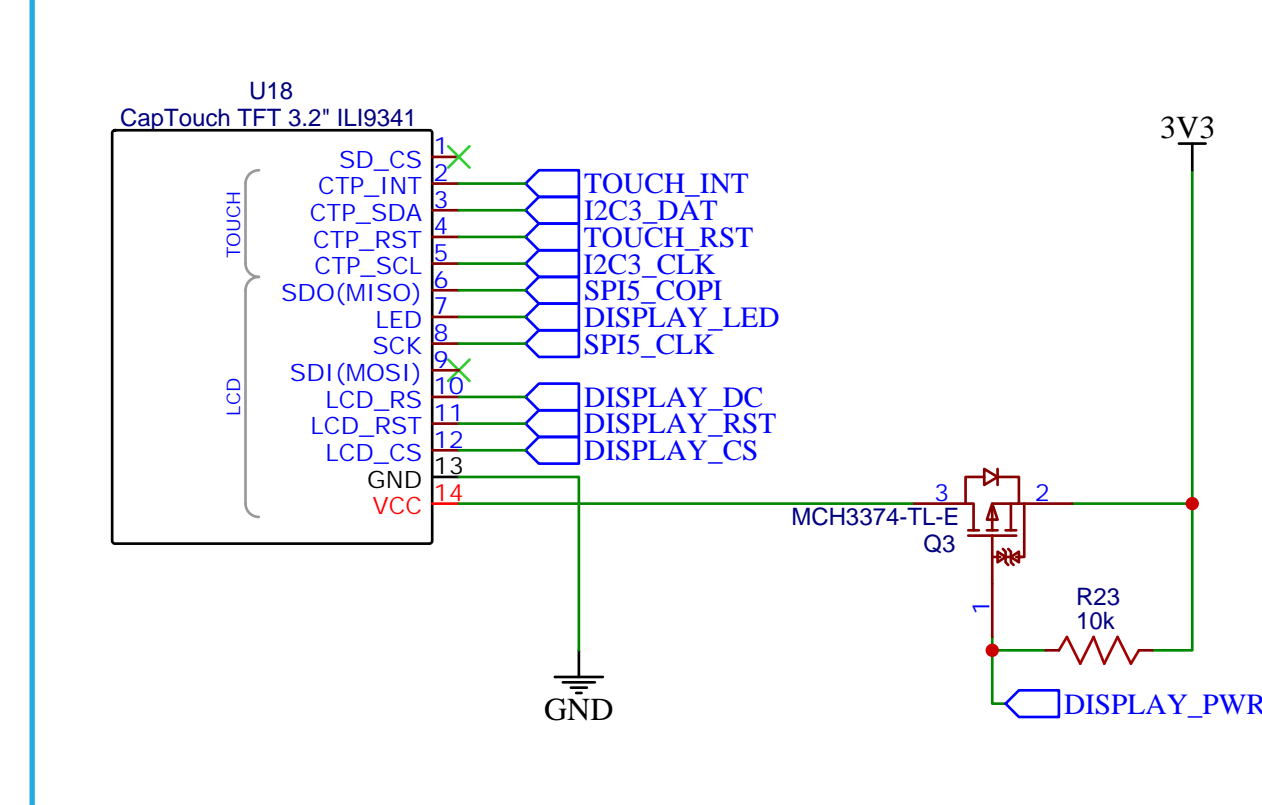
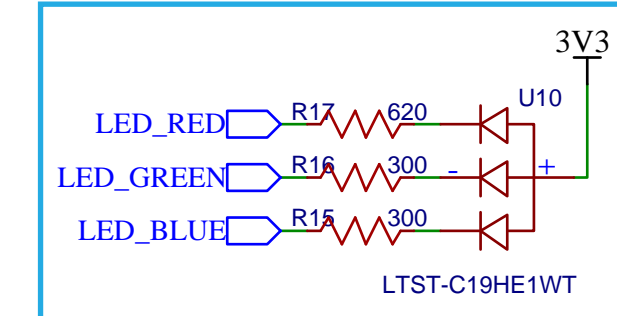
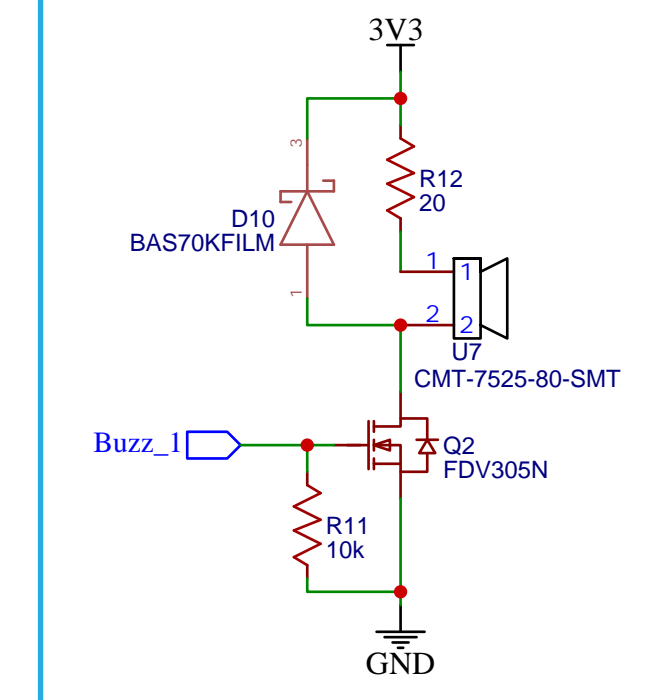
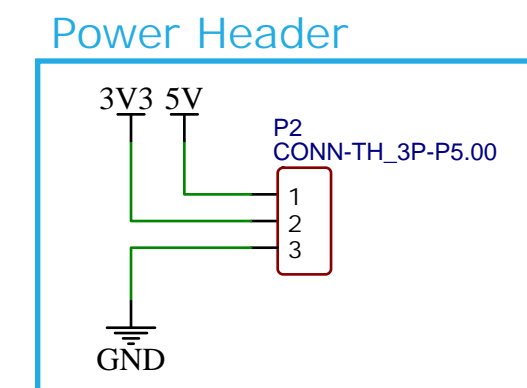
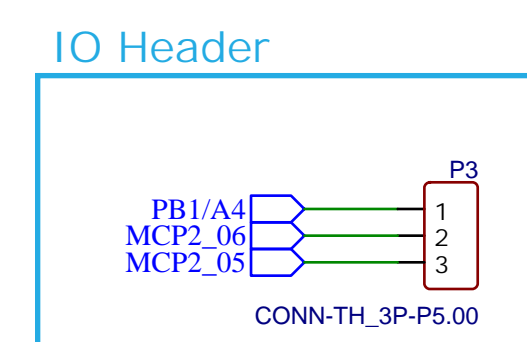
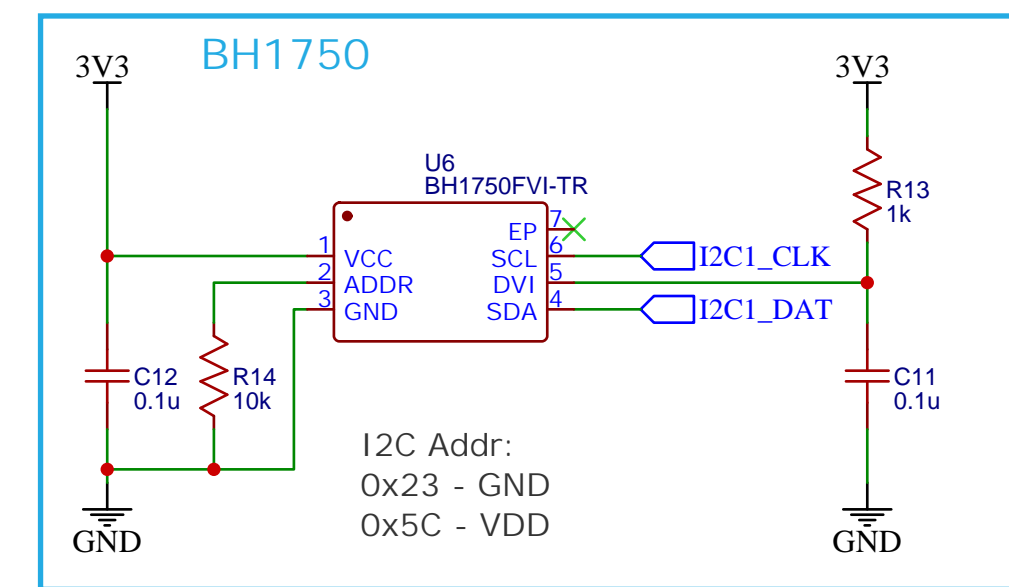
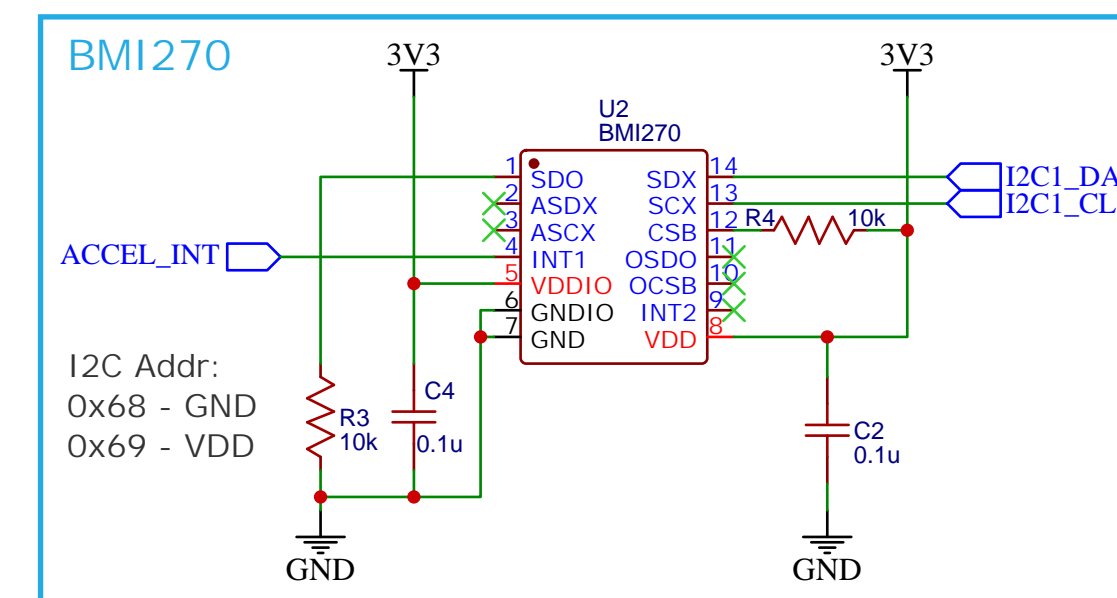
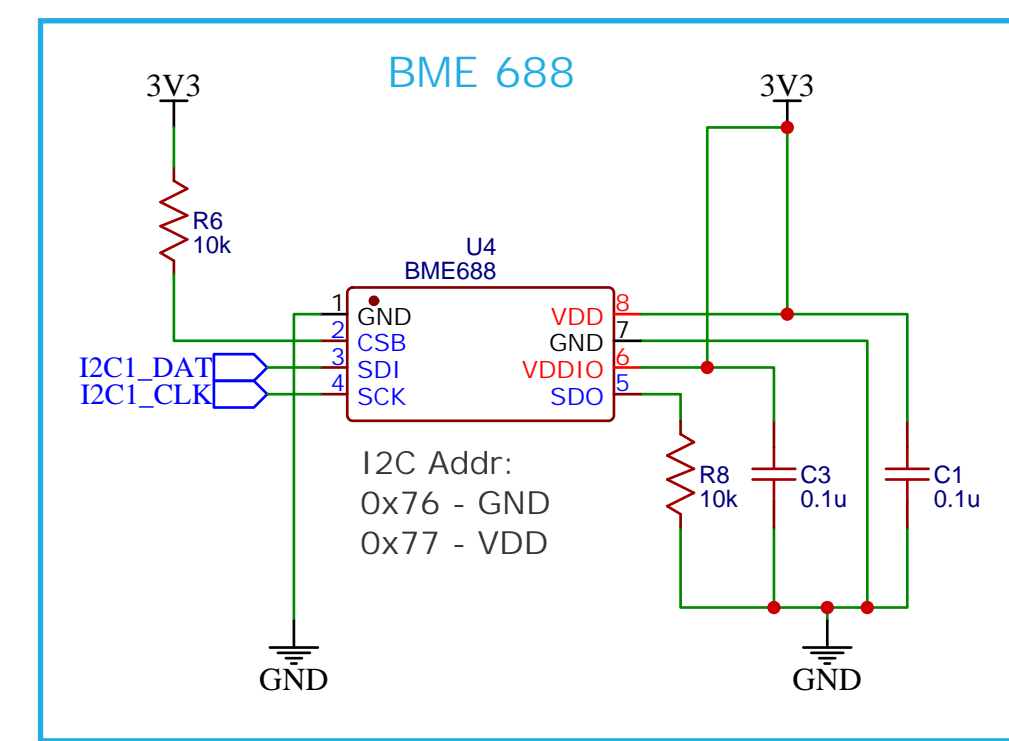
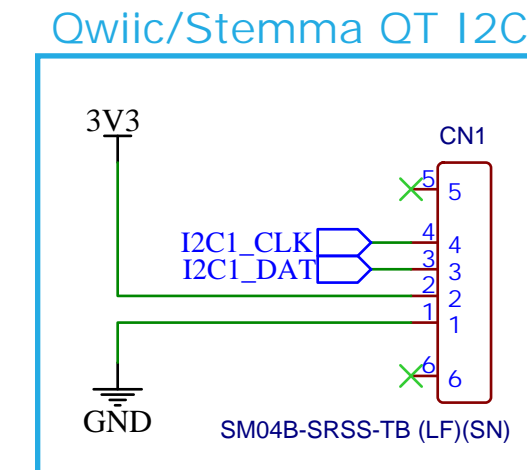
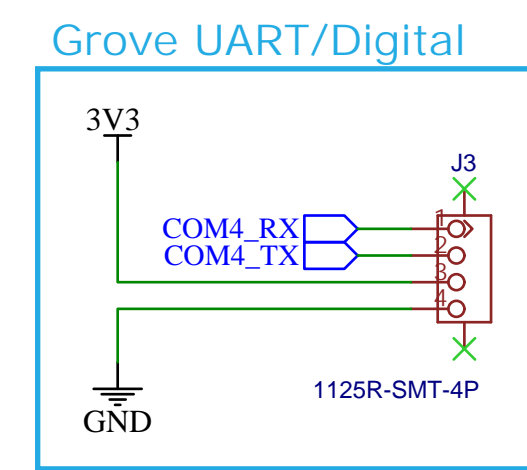
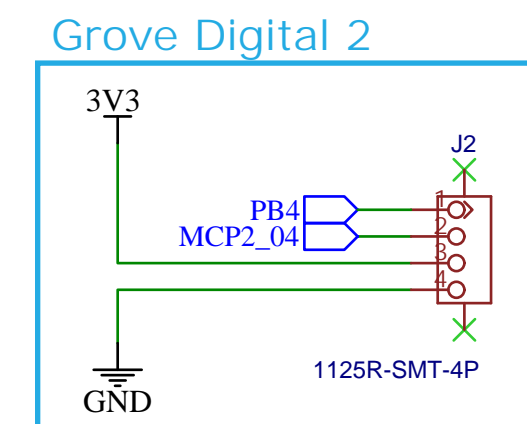
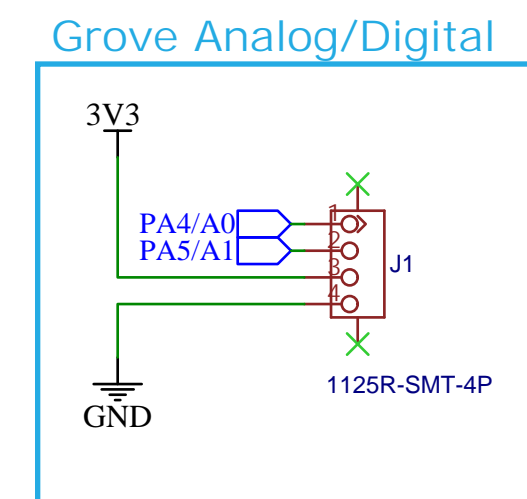
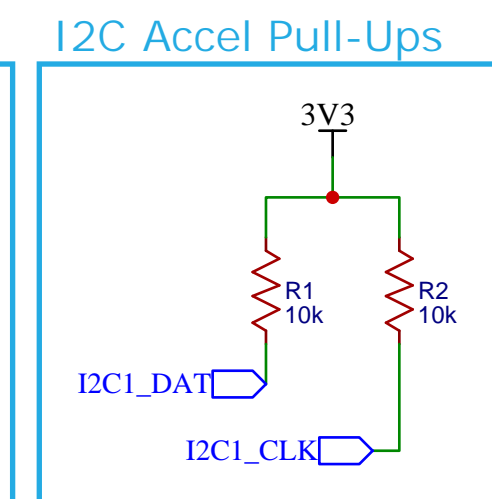
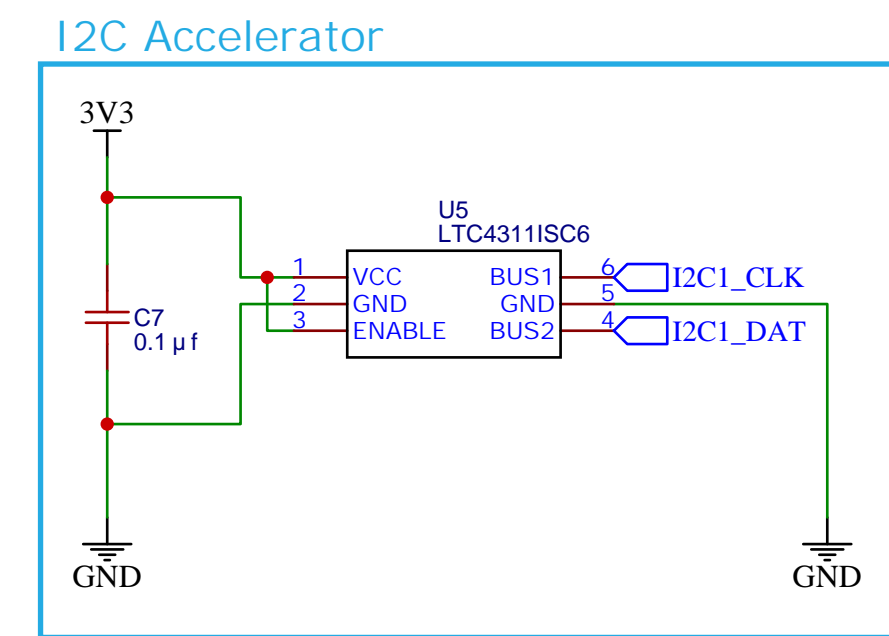


Reserved Interrupts  
10, 12, 13

Interrupt Table	
MCP1_INT	00
MCP2_INT	08
MBUS1_INT	02
Grove (A0)	04
Grove (A1)	05
Grove (PB14)	14
Grove (PB15)	15
IO1_A1	01
IO1_2	09
IO1_3	07
Grove (PB4)	04

PWM Timers	
RGB_RED	TIM8_
RGB_GREEN	TIM3_
RGB_BLUE	TIM8_
BUZZ_1	TIM2_
PB8	TIM4_
PB9	TIM4_

\*Note, D10 (PC7) can be configured as TIM8\_2 but requires Meadow.Core change.



Stackup/Controlled Impedance (JLC7628 Stackup):

```

USB: 90 Diff Pair
  Outer layer: 0.19mm width, 0.11mm spacing
  Inner layer: 0.13mm width, 0.11mm spacing

```