Will Jameson

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OBJECTIVE

Driven Engineering student at San Francisco State University seeking jobs and internships in electrical engineering; interests include digital design for audio processing, power systems, embedded systems, communication, and sustainable development.

EDUCATION

San Francisco State University | B.S. Electrical Engineering Expected Spring 2026

- Digital Design - STM32, FPGA, Verilog, GTKWave, Efinix Software, ModelSim, Quartus
- Software - Simulink, Solidworks, EAGLE, LTSpice, Matlab
- Circuit design with Operational Amplifiers

City College of San Francisco | A.S. Engineering 2024

- Experience in C++ and Matlab
- Proficiency with electronics testing equipment: Oscilloscopes, DMM's etc.
- Teacher's Assistant for Introductory Circuit Analysis
- CCSF Engineering Club Solar Regatta: Assisted with hull fabrication

San Francisco State University | B.A. Chinese Language 2009

- Study abroad Experience in China for 1 year.
- Helped SFSU Chinese Department with translations of documents.

PROJECT EXPERIENCE

Electrical Engineer for Mars Rover Proposal

- Designing DC/DC voltage converter for unstable power source to produce stable voltage.
- Interrupt based microcontroller firmware for servo motor based control system.

Open Avenues Build Foundation | FPGA Based LED Dimmer System

- Wrote programs in Verilog and simulated various functions of an FPGA using Icarus and GTKWave simulation software.
- Used pulse width and pulse frequency modulation to control dimmers on an Efinix Xyloni development board.
- Produced a short presentation on the process, result, and projected applications of using FPGA's to control signals with pulse width modulation.

RELEVANT WORK EXPERIENCE

Teacher's Assistant - CCSF Engineering Department San Francisco, CA | 2024

- Provide support for 30 students in Introductory Circuit Analysis
- Stand in as needed for supplemental lessons
- Lead weekly conference with students to cover and reinforce understanding of relevant topics
- Grade weekly homework assignments for correct procedure, understanding of concept, and accuracy