

DATA SHEET

LOW OHMIC CHIP RESISTORS

RL series

5%, 2%, 1% sizes 0402/0603/0805/1206/ 1210/1218/2010/2512

RoHS compliant & Halogen Free



YAGEO Phícomp



SCOPE

This specification describes RL0402 to RL2512 low ohmic chip resistors with lead-free terminations made by thick film process.

APPLICATIONS

- Converters
- Printer equipment
- Server board
- Telecom
- Consumer
- Car electronics

FEATURES

- AEC-Q200 qualified
- Halogen Free Epoxy
- RoHS compliant
- Hazardous wastes
- High component and equipment reliability
- Saving of PCB space
- Non-forbidden materials used in products/production
- Low resistances applied to current sensing
- MSL Class: MSL I

ORDERING INFORMATION - GLOBAL PART NUMBER & 12NC

Both part numbers are identified by the series, size, tolerance, packing type, temperature coefficient, taping reel and resistance value.

YAGEO BRAND ordering code

GLOBAL PART NUMBER (PREFERRED)

RL XXXX X X X XX XXXX L

(1) (2) (3) (4) (5) (6) (7)

(I) SIZE

0402 / 0603 / 0805 / 1206 / 1210 / 1218 / 2010 / 2512

(2) TOLERANCE

 $F = \pm 1\%$

 $G = \pm 2\%$

 $J = \pm 5\%$

"-" = Jumper ordering

(3) PACKAGING TYPE

R = Paper taping reel

K = Embossed taping reel

(4) TEMPERATURE COEFFICIENT OF RESISTANCE

- = Based on spec

(5) TAPING REEL

07 = 7 inch dia. Reel and standard power

10 = 10 inch dia. Reel and standard power

13 = 13 inch dia. Reel and standard power

7W = 7 inch dia. Reel and $2 \times$ standard power (0805 and 1206)

(6) RESISTANCE VALUE

There are $2\sim4$ digits indicated the resistor value. Letter R/K/M is decimal point. Detailed coding rules of resistance are shown in the table of "Resistance rule of global part number".

(7) DEFAULT CODE

Letter L is system default code for order only (Note)

Resistance rule of global part number

| Resistance code rule | e Example |
|-------------------------------|---|
| 0RXXX (1 to 976 mΩ) | 0RI = 0.I Ω 0RI2 = 0.I2 Ω 0RI05 = 0.I05 Ω |
| XRXX (1 to 9.76 Ω) | IR = I Ω IR5 = I.5 Ω 9R76 = 9.76 Ω |
| $XXRX$ (10 to 97.6 Ω) | $10R = 10 \Omega$ $97R6 = 97.6 \Omega$ |
| XXXR (100 to 976 Ω) | 100R = 100 Ω |
| XKXX (1 to 9.76 KΩ) | IK = 1,000 Ω 9K76 = 9760 Ω |
| XMXX (1 to 9.76 MΩ) | $IM = 1,000,000 \Omega$ $9M76 = 9,760,000 \Omega$ |

ORDERING EXAMPLE

The ordering code of a RL0603 chip resistor, value 0.56Ω with $\pm 1\%$ tolerance, supplied in 7-inch tape reel is: RL0603FR-070R56L.

NOTE

- All our R-Chip products meet RoHS compliant and Halogen Free. "LFP" of the internal 2D reel label mentions "Lead Free Process"
- 2. On customized label, "LFP" or specific symbol can be printed

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PHYCOMP BRAND ordering codes

Both GLOBAL PART NUMBER (preferred) and I2NC (traditional) codes are acceptable to order Phycomp brand products.

GLOBAL PART NUMBER (PREFERRED)

For detailed information of GLOBAL PART NUMBER and ordering example, please refer to page 2.

I2NC CODE

2350 / 2390 / 2322 XXX XXXXX L

| | (1) | | (| 2) (3) (4) | | | |
|------|---------|----------------------------|------|-------------------------|------------------------------|---------------------------|-----------|
| SIZE | TYPE | START IN ⁽¹⁾ | TOL. | RESISTANCE RANGE | EMBOSSED (2) TAPE ON REEL | PAPER/PE (2) TAPE ON R | |
| | | IIN \ ′ | (%) | KANGE | 4,000 | 5,000 | 10,000 |
| 0402 | LRC31 | 2350 | ±5% | 0.05 to I Ω | - | - | 513 20xxx |
| | LRC32 | 2350 | ±1% | 0.05 to I Ω | - | - | 513 22xxx |
| 0603 | LRC21 | 2350 | ±5% | 0.01 to 1 Ω | - | 512 10xxx | - |
| | LRC22 | 2350 | ±1% | 0.01 to 1 Ω | - | 512 12xxx | - |
| 0805 | LRCII | 2350 | ±5% | 0.01 to 1 Ω | - | 511 10xxx | - |
| | LRC12 | 2350 | ±1% | 0.01 to 1 Ω | = | 511 12xxx | = |
| | LRCIIP | 2350 | ±1% | 0.01 to 1 Ω | - | 511 15xxx | - |
| | LRC12P | 2350 | ±5% | 0.01 to 1 Ω | - | 511 17xxx | - |
| 1206 | LRC01 | 2350 | ±5% | 0.01 to 1 Ω | - | 510 10xxx | - |
| | LRC02 | 2350 | ±1% | 0.01 to 1 Ω | - | 510 12xxx | - |
| | LRC01P | 2350 | ±1% | 0.01 to 1 Ω | - | 51901xxx | - |
| | LRC02P | 2350 | ±5% | 0.01 to 1 Ω | - | 519 lxxxx | - |
| 1210 | LPRC101 | 2390 | ±5% | 0.01 to 0.0976 Ω | - | 735 90xxx | - |
| | LPRC101 | 2390 | ±5% | 0.1 to 1 Ω | - | 735 60xxx | - |
| | LPRC102 | 2390 | ±1% | 0.01 to 1 Ω | - | 735 3xxxx | - |
| 1218 | LPRC201 | 2322 | ±5% | 0.01 to 1 Ω | 735 64xxx | - | - |
| | LPRC201 | | ±1% | 0.01 to 1 Ω | 735 7xxx | - | - |
| 2010 | LPRCIII | | ±5% | 0.01 to 0.0976 Ω | 760 90xxx | - | - |
| | LPRCIII | | ±5% | 0.1 to 1 Ω | 760 60xxx | - | - |
| | LPRCIII | | ±1% | 0.01 to 0.0976 Ω | 761 90xxx | - | - |
| | LPRCIII | | ±1% | 0.1 to 1 Ω | 761 6xxx | - | - |
| 2512 | LPRC221 | | ±5% | 0.01 to 0.0976 Ω | 762 90xxx | - | - |
| | LPRC221 | | ±5% | 0.1 to 1 Ω | 762 60xxx | - | - |
| | LPRC221 | 2322 | ±1% | 0.01 to 0.0976 Ω | 763 90xxx | - | - |

| (1) | The resistors | have a l | 2-digit ord | ering code | starting w | ith 2350/2390/2322. |
|-----|---------------|----------|-------------|------------|------------|---------------------|
|-----|---------------|----------|-------------|------------|------------|---------------------|

763 6xxxx

 \pm 1% 0.1 to 1 Ω

| L | _ast | digit | of | 12NC |
|---|------|-------|----|------|
|---|------|-------|----|------|

| Resistance decade (3) | Last digit |
|-----------------------|------------|
| 0.01 to 0.0976Ω | 0 |
| 0.I to 0.976Ω | 7 |
| I to 9.76Ω | 8 |
| 10 to 97.6 Ω | 9 |
| 100 to 976 Ω | 1 |
| I to 9.76 $k\Omega$ | 2 |
| 10 to 97.6 $k\Omega$ | 3 |
| 100 to 976 $k\Omega$ | 4 |
| I to 9.76 $M\Omega$ | 5 |
| 10 to 97.6 MΩ | 6 |
| | |

| Example: | 0.02 Ω | = | 0200 or 200 |
|----------|---------------------|---|-------------|
| | 0.3Ω | = | 3007 or 307 |
| | IΩ | = | 1008 or 108 |
| | 33 k Ω | = | 3303 or 333 |
| | $10~\text{M}\Omega$ | = | 1006 or 106 |

ORDERING EXAMPLE

The ordering code of a RL0603 chip resistor, value 0.56 Ω with ±1% tolerance, supplied in tape of 5,000 units per reel is: 235051212567L or RL0603FR-070R56L.

NOTE

- I. All our R-Chip products meet RoHS compliant and Halogen Free. "LFP" of the internal 2D reel label mentions "Lead Free Process"
- 2. On customized label, "LFP" or specific symbol can be printed



LPRC221 2322

⁽²⁾ The subsequent 4 or 5 digits indicate the resistor tolerance and packaging. (In 12NC code, only 07" tape reel code is supplied. Supply of 10"/13" tape reel is requested in Global part number ordering code.)

⁽³⁾ The remaining 4 or 3 digits represent the resistance value with the last digit indicating the multiplier as shown in the table of "Last digit of 12NC".

⁽⁴⁾ Letter L is system default code for order only $^{(Note)}$.

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MARKING

RL0402 / RL0603: R<100 m Ω EXCEPT 10/20/30/40/50/60 m Ω



No marking

RL0603: R≥100 mΩ, R = 10/20/30/40/50/60 mΩ



E-24 series / Non-E series (R= $25/40/50/60/250/400/500 \text{ m}\Omega$):3 digits

The "R" is used as a decimal point; the other 2 digits are significant.

RL0805 / RL1206 / RL1210 /RL1218 / RL2010 / RL2512



E-24 series / Non-E series (R= $25/40/50/60/250/400/500 \text{ m}\Omega$): 4 digits

The "R" is used as a decimal point; the other 3 digits are significant.

For further marking information, please see special data sheet "Chip resistors marking".

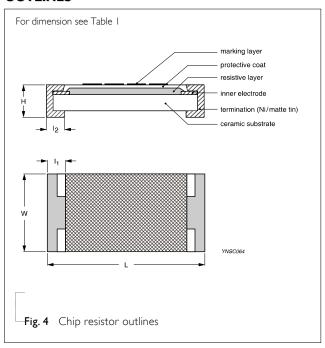
CONSTRUCTION

The resistors are constructed out of a high-grade ceramic body. Internal metal electrodes are added at each end and connected by a resistive paste. The composition of the paste is adjusted to give the approximate required resistance and laser cutting of this resistive layer that achieves tolerance trims the value. The resistive layer is covered with a protective coat and printed with the resistance value. Finally, the two external terminations (matte tin) are added. See fig. 4.

DIMENSIONS

| Table | Table I For outlines see fig. 4 | | | | | | | | |
|--------|---------------------------------|------------|------------|---------------------|---------------------|--|--|--|--|
| TYPE | L (mm) | W (mm) | H (mm) | I _I (mm) | I ₂ (mm) | | | | |
| RL0402 | 1.00 ±0.10 | 0.50 ±0.05 | 0.35 ±0.05 | 0.20 ±0.10 | 0.25 ±0.10 | | | | |
| RL0603 | 1.60 ±0.10 | 0.80 ±0.10 | 0.45 ±0.10 | 0.25 ±0.15 | 0.25 ±0.15 | | | | |
| RL0805 | 2.00 ±0.10 | 1.25 ±0.10 | 0.50 ±0.10 | 0.35 ±0.20 | 0.35 ±0.20 | | | | |
| RL1206 | 3.10 ±0.10 | 1.60 ±0.10 | 0.55 ±0.10 | 0.45 ±0.20 | 0.40 ±0.20 | | | | |
| RL1210 | 3.10 ±0.10 | 2.60 ±0.15 | 0.55 ±0.10 | 0.50 ±0.20 | 0.50 ±0.20 | | | | |
| RL1218 | 3.05 ±0.15 | 4.60 ±0.20 | 0.55 ±0.10 | 0.45 ±0.25 | 0.50 ±0.25 | | | | |
| RL2010 | 5.00 ±0.10 | 2.50 ±0.15 | 0.55 ±0.10 | 0.60 ±0.20 | 0.50 ±0.20 | | | | |
| RL2512 | 6.35 ±0.10 | 3.20 ±0.15 | 0.55 ±0.10 | 0.60 ±0.20 | 0.50 ±0.20 | | | | |

OUTLINES



ELECTRICAL CHARACTERISTICS

Table 2

| Туре | Power P ₇₀ | Operating Temp. range | Resistance range & tolerance | | T. C. R. (ppm/°C) | Jumper criteria |
|---------|-----------------------|-----------------------|------------------------------|-------------------------------------|--|---|
| RL0402 | 1/16W | | | $50m\Omega \le R < I\Omega$ | | Max. resistance $20m\Omega$ Rated current $1.5A$ |
| RL0603 | 1/10W | -55°C to +155°C | | $10m\Omega \le R < I\Omega$ | | Max. resistance 20mΩ Rated current 2A |
| DI OOOF | 1/8W | _ | | $10m\Omega \le R < I\Omega$ | | Max. resistance $20m\Omega$ Rated current $2.5A$ |
| RL0805 | 1/4W | -55°C to +125°C | | $10m\Omega \le R < I\Omega$ | See following table "T.C.R RL series" | |
| D1 1004 | 1/4W | -55°C to +155°C | E24 ±1%, ±2%, ±5% | $10\text{m}\Omega \leq R < 1\Omega$ | | Max. resistance $20m\Omega$ Rated current $3.5A$ |
| RL1206 | 1/2W | -55°C to +125°C | | $10m\Omega \le R < I\Omega$ | | |
| RL1210 | 1/2W | | | $10m\Omega \le R < 1\Omega$ | | |
| RL1218 | IW | | | $10m\Omega \le R < 1\Omega$ | | |
| RL2010 | 3/4W | − -55°C to +155°C | | $10m\Omega \le R < 1\Omega$ | | |
| RL2512 | IW | | | $10m\Omega \le R < 1\Omega$ | | |

| TYPE / | RESISTANCE RANGE | TEMPERATURE COEFFICIENT OF RESISTANCE | | | | | | | |
|---------|--|---------------------------------------|--|----------------|--|--|--|------------|-----------------------------|
| RL0402 | 50 O 10 11 O | 50mΩ ≤R< | <100mΩ | | I00mΩ ≤ | R<500mΩ | | 500mΩ : | ≤R <iω< th=""></iω<> |
| NLU-102 | 50mΩ≤R <iω< th=""><th>±1000 p</th><th>pm/°C</th><th></th><th>±800 ¡</th><th>opm/°C</th><th></th><th>±300 p</th><th>pm/°C</th></iω<> | ±1000 p | pm/°C | | ±800 ¡ | opm/°C | | ±300 p | pm/°C |
| RL0603 | 10 040 410 | I0mΩ ≤R≤36m | ιΩ | 36n | nΩ <r≤91mω< th=""><th>91m<u>Ω</u> <r≤500< th=""><th>)mΩ</th><th>500</th><th>)mΩ <r<iω< th=""></r<iω<></th></r≤500<></th></r≤91mω<> | 91m <u>Ω</u> <r≤500< th=""><th>)mΩ</th><th>500</th><th>)mΩ <r<iω< th=""></r<iω<></th></r≤500<> |)mΩ | 500 |)mΩ <r<iω< th=""></r<iω<> |
| NLU003 | I0mΩ≤R <iω< th=""><td>±1,500 ppm/°</td><td colspan="2">″°C ±1,200 ppm/°C</td><td>±800 ppm/</td><td>°C</td><td>±.</td><td>300 ppm/°C</td></iω<> | ±1,500 ppm/° | ″°C ±1,200 ppm/°C | | ±800 ppm/ | °C | ±. | 300 ppm/°C | |
| RL0805 | | I0mΩ ≤R≤I8mΩ | I8mΩ <i< th=""><th>R≤47m<u>Ω</u></th><th>47mΩ <r≤91mω< th=""><th>91m<u>Ω</u> <r≤360m<u>Ω</r≤360m<u></th><th>360mΩ <f< th=""><th>R<500mΩ</th><th>500mΩ ≤R<iω< th=""></iω<></th></f<></th></r≤91mω<></th></i<> | R≤47m <u>Ω</u> | 47mΩ <r≤91mω< th=""><th>91m<u>Ω</u> <r≤360m<u>Ω</r≤360m<u></th><th>360mΩ <f< th=""><th>R<500mΩ</th><th>500mΩ ≤R<iω< th=""></iω<></th></f<></th></r≤91mω<> | 91m <u>Ω</u> <r≤360m<u>Ω</r≤360m<u> | 360mΩ <f< th=""><th>R<500mΩ</th><th>500mΩ ≤R<iω< th=""></iω<></th></f<> | R<500mΩ | 500mΩ ≤R <iω< th=""></iω<> |
| NE0003 | _ | ±1,500 ppm/°C | ±1,200 | ppm/°C | ±1,000 ppm/°C | ±600 ppm/°C | ±300 p | pm/°C | ±200 ppm/°C |
| | | I0mΩ ≤R≤I8mΩ | I8mΩ <i< th=""><th>R≤47m<u>Ω</u></th><th>47m<u>Ω</u> <r≤91m<u>Ω</r≤91m<u></th><th>91m<u>Ω</u> <r≤360m<u>Ω</r≤360m<u></th><th>360mΩ <f< th=""><th>R≤500mΩ</th><th>500mΩ <r<iω< th=""></r<iω<></th></f<></th></i<> | R≤47m <u>Ω</u> | 47m <u>Ω</u> <r≤91m<u>Ω</r≤91m<u> | 91m <u>Ω</u> <r≤360m<u>Ω</r≤360m<u> | 360mΩ <f< th=""><th>R≤500mΩ</th><th>500mΩ <r<iω< th=""></r<iω<></th></f<> | R≤500mΩ | 500mΩ <r<iω< th=""></r<iω<> |
| RL1206 | 10mΩ≤R< Ω | ±1,500 ppm/°C | ±1,200 | ppm/°C | ±1,000 ppm/°C | ±600 ppm/°C | ±300 p | pm/°C | ±200 ppm/°C |
| RL1210 | _ | ±1,500 ppm/°C | ±1,000 | ppm/°C | ±800 ppm/°C | ±600 ppm/°C | ±300 ppm/°C | | ±200 ppm/°C |
| RL2010 | _ | ±1,500 ppm/°C | 00 ppm/°C ±1,200 ppm/°C | | ±1,000 ppm/°C | ±600 ppm/°C | ±300 p | pm/°C | ±200 ppm/°C |
| RL2512 | | ±1,500 ppm/°C | ±1,200 | ppm/°C | ±800 ppm/°C | ±600 ppm/°C | ±300 p | pm/°C | ±200 ppm/°C |
| סוכוום | 10.040410 | 10m <u>Ω</u> ≤R≤30m | Ω | 30n | nΩ <r≤56mω< th=""><th>56mΩ <r≤180< th=""><th>)mΩ</th><th>180</th><th>)mΩ <r<iω< th=""></r<iω<></th></r≤180<></th></r≤56mω<> | 56mΩ <r≤180< th=""><th>)mΩ</th><th>180</th><th>)mΩ <r<iω< th=""></r<iω<></th></r≤180<> |)mΩ | 180 |)mΩ <r<iω< th=""></r<iω<> |
| RL1218 | 10mΩ≤R<1Ω | ±2,000 ppm/° | С | ±I | ,000 ppm/°C | ±700 ppm/ | ,C | ±2 | 250 ppm/°C |

FOOTPRINT AND SOLDERING PROFILES

For recommended footprint and soldering profiles, please see the special data sheet "Chip resistors mounting".

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RL

PACKING STYLE AND PACKAGING QUANTITY

Table 3 Packing style and packaging quantity

| PACKING STYLE | REEL DIMENSION | RL0402 | RL0603 | RL0805 | RL1206 | RL1210 | RL1218 | RL2010 | RL2512 |
|--------------------------|----------------|--------|--------|--------|--------|--------|--------|--------|--------|
| Paper taping reel (R) | 7" (178 mm) | 10,000 | 5,000 | 5,000 | 5,000 | 5,000 | | | |
| | 10" (254 mm) | 20,000 | 10,000 | 10,000 | 10,000 | 10,000 | | | |
| | 13" (330 mm) | 50,000 | 20,000 | 20,000 | 20,000 | 20,000 | | | |
| Embossed taping reel (K) | 7" (178 mm) | | | | | | 4,000 | 4,000 | 4,000 |

NOTE

1. For paper/embossed tape and reel specification/dimensions, please see the special data sheet "Chip resistors packing".

FUNCTIONAL DESCRIPTION

OPERATINGTEMPERATURE RANGE

Normal Power: Range: -55 °C to +155 °C (Fig. 5) Double Power: Range: -55 °C to +125 °C (Fig. 6)

POWER RATING

Each type rated power at 70 °C:

RL0402=1/16 W;

RL0603=1/10 W;

RL0805=1/8 W, 1/4W;

RL1206=1/4 W, 1/2W;

RL1210=1/2 W;

RL1218=1 W;

RL2010=3/4 W;

RL2512=1 W.

RATED VOLTAGE

The DC or AC (rms) continuous working voltage corresponding to the rated power is determined by the following formula:

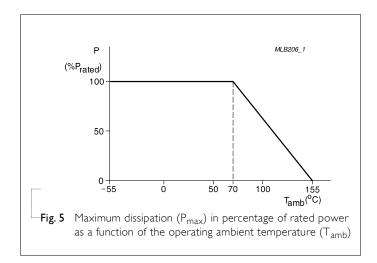
$$V = \sqrt{(P \times R)}$$

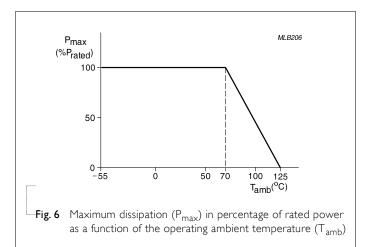
Where

V = Continuous rated DC or AC (rms) working voltage (V)

P = Rated power (W)

 $R = Resistance value (\Omega)$





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Chip Resistor Surface Mount RL SERIES 0402 to 2512

TESTS AND REQUIREMENTS

Table 4 Test condition, procedure and requirements

| TEST | TEST METHOD | PROCEDURE | REQUIREMENTS |
|------------------------|-------------------------|--|---|
| Life/ | IEC 60115-1 4.25.1 | 1,000 hours at 70±2°C applied RCWV | $\pm (2\% + 0.5 \text{m}\Omega)$ |
| Endurance | MIL-STD-202 Method 108A | 1.5 hours on, 0.5 hour off, still air required | |
| High Temperature | IEC 60068-2-2 | I,000 hours at maximum operating | ±(1% +0.5mΩ) |
| Exposure | | temperature depending on specification, unpowered | |
| | | No direct impingement of forced air to the parts | |
| | | Normal power : Tolerances: 155±5°C | |
| | | Double power : Tolerances: 125±5℃ | |
| Moisture Resistance | MIL-STD-202 Method 106G | Each temperature / humidity cycle is defined at 8 hours, 3 cycles / 24 hours for 10d with 25 °C / 65 °C 95% R.H., without steps 7a & 7b, | $\pm (2\% + 0.5 \text{m}\Omega)$ |
| | | unpowered | |
| | | Parts mounted on test-boards, without condensation on parts | |
| Thermal Shock | MIL-STD-202 Method 107G | -55/+125 °C | ±(1% +0.5mΩ) |
| | | Number of cycles required is 300. | |
| | | Devices mounted | |
| | | Maximum transfer time is 20 seconds. Dwell time is 15 minutes. | |
| Short time overload | IEC60115-1 4.13 | RL standard power: 2.5 times rated voltage for 5 sec at room temperature | \pm (2% +0.5m Ω) No visible damage |
| | | RL high power: 5 times rated power for 5 sec at room temperature | |
| Board Flex/ Bending | IEC 60115-1 4.33 | Device mounted on PCB test board as described, only I board bending required | $\pm (1\% + 0.5 \text{m}\Omega)$ No visible damage |
| | | 3 mm bending | |
| | | Bending time: 60±5 seconds | |
| | | Ohmic value checked during bending | |

Chip Resistor Surface Mount RL SERIES 0402 to 2512

| TEST | TEST METHOD | PROCEDURE | REQUIREMENTS |
|-----------------------------------|------------------|---|---|
| Solderability - Wetting | J-STD-002 test B | Electrical Test not required Magnification 50X SMD conditions: Ist step: method B, aging 4 hours at 155 °C dry heat 2nd step: leadfree solder bath at 245±3 °C Dipping time: 3±0.5 seconds | Well tinned (≥95% covered) No visible damage |
| - Leaching | J-STD-002 test D | Leadfree solder, 260 °C, 30 seconds immersion time | No visible damage |
| - Resistance to Soldering Heat | IEC 60115-1 4.18 | Condition B, no pre-heat of samples. Leadfree solder, 260 °C, 10 seconds immersion time Procedure 2 for SMD: devices fluxed and cleaned with isopropanol | \pm (1% +0.5m Ω) No visible damage |

Product specification

Chip Resistor Surface Mount RL SERIES 0402 to 2512

REVISION HISTORY

| REVISION | DATE | CHANGE NOTIFICATION | DESCRIPTION |
|-----------|---------------|---------------------|-------------------------------------|
| Version 2 | May 31, 2017 | - | - Add 10" packing |
| Version I | Dec. 16, 2015 | - | - Extend 0805 T.C.R. range |
| Version 0 | Nov. 11, 2014 | - | - First issue of this specification |

[&]quot;Yageo reserves all the rights for revising the content of this datasheet without further notification, as long as the products itself are unchanged. Any product change will be announced by PCN."

