

# USB Power Delivery ENGINEERING CHANGE NOTICE

**Title: tFirstSourceCap Clarification**

**Applied to: USB Power Delivery Specification Revision 3.1**

**Version 1.8**

<b>Brief description of the functional changes proposed:</b>
Clarification on tFirstSourceCap timer usage after SoftReset

<b>Benefits as a result of the proposed changes:</b>
More clarity

<b>An assessment of the impact to the existing revision and systems that currently conform to the USB specification:</b>

<b>An analysis of the hardware implications:</b>
NA

<b>An analysis of the software implications:</b>
NA

<b>An analysis of the compliance testing implications:</b>
Need to check that tests use correct timer

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## Actual Change Requested

### (a). Section 6.6.3.3, Page 217

#### From Text:

After Port Partners are Attached or after a Hard Reset or after a Power Role Swap or after a Fast Role Swap a Source **shall** send its first *Source\_Capabilities* Message within *tFirstSourceCap* of  $V_{BUS}$  reaching *vSafe5V*. This ensures that the Sink receives a *Source\_Capabilities* Message before the Sink's *SinkWaitCapTimer* expires.

#### To Text:

After Port Partners are Attached or after a Hard Reset or after a Power Role Swap or after a Fast Role Swap a Source **shall** send its first *Source\_Capabilities* Message within *tFirstSourceCap* of  $V_{BUS}$  reaching *vSafe5V*.

After Soft Reset, a source **shall** send its first *Source\_Capabilities* Message within *tFirstSourceCap* after last bit of the *GoodCRC* Message *EOP* corresponding to *Accept* Message.

This ensures that the Sink receives a *Source\_Capabilities* Message before the Sink's *SinkWaitCapTimer* expires.