

Q1.

TLB.

valid	tag	physical Page Number.
1	18	1
1	3	6
1	3212	52
1	11	12

address	page #	TLB	Page table
4669	1	miss	Page fault
2227	0	miss	hit.
13916	3	miss	hit.
34587	8	miss	fault
48870	11	miss	hit.
12608	3	hit	hit.
49225	12	miss	fault.

Page table

Valid.	Physical Page or in Disk
1	5
0	Disk 0
0	Disk
1	6
1	9
1	11
0	Disk
1	4
0	Disk 1
0	Disk
1	3
1	12

1

2.

Q2. TLB.

Valid	tag	Physical Page #
1	15	11
1	4	9
1	31	0
0	6	05

address	Page #	TLB	Page table.
4669	0	miss	hit.
2227	0	hit	hit.
13916	1	miss	fault.
34587	4	miss	hit.
48870	5	miss	hit.
12608	1	hit	hit.
49225	6	miss	fault

Valid	Physical Page or in Disk
1	5
0	Disk 0
0	Disk
1	6
1	9
1	11
0	Disk 1
1	4
0	Disk
0	Disk
1	3
1	12

advantage: need small Page table  
 disadvantage: more overhead bring page from



3 page table size:

$$32 \text{ bits} = 8 \text{ KB} \Rightarrow 13 \text{ bits}$$

$$32 - 13 = 19 \text{ bits. } 2^{19} = 512 \text{ K entries.}$$

$$512 \text{ K} * 4 \text{ Bytes} = 2 \text{ MB.}$$

$$2 \text{ MB} * 5 = 10 \text{ MB.}$$

4. That page was kicked off by other applications:

or that page has some error ... etc.

25.

set 0	<del>0</del> <del>4</del> <del>12</del> <del>16</del> 0
	<del>8</del> <del>12</del> <del>16</del>
set 1	

0	2	4	8	10	12	14	16	0
X	X	X	X	X	X	X	X	X

miss: 9

hit: 0

26.

Set 0	0
	<del>8</del> <del>4</del> <del>8</del> <del>12</del> <del>16</del>
Set 1	

0	2	4	8	10	12	14	16	0
X	X	X	X	X	X	X	X	✓

27.

X[0]	5	5	6	6
X[1]	3	5	5	3

doesn't ensure coherency:  $X[0] = 5 \neq X[1] = 2$ .

28.

C	0	0	2	2	3
D	0	1	1	3	3



9. benchmark A: shared:  $20 + 0.12\% \times 180 = 20.216$   
 private:  $5 + 0.3\% \times 180 = 5.54$

benchmark B: shared:  $20 + 0.03\% \times 180 = 20.054$   
 private:  $6 + 0.06\% \times 180 = 5.108$

private is better.

10  $16KB \div 64B \div 4 = 256 \text{ sets} \Rightarrow 8 \text{ bits. index}$

$64B \Rightarrow 6 \text{ bits. block offsets}$

page size:  $16KB \Rightarrow 14 \text{ bits for. page offsets}$

