


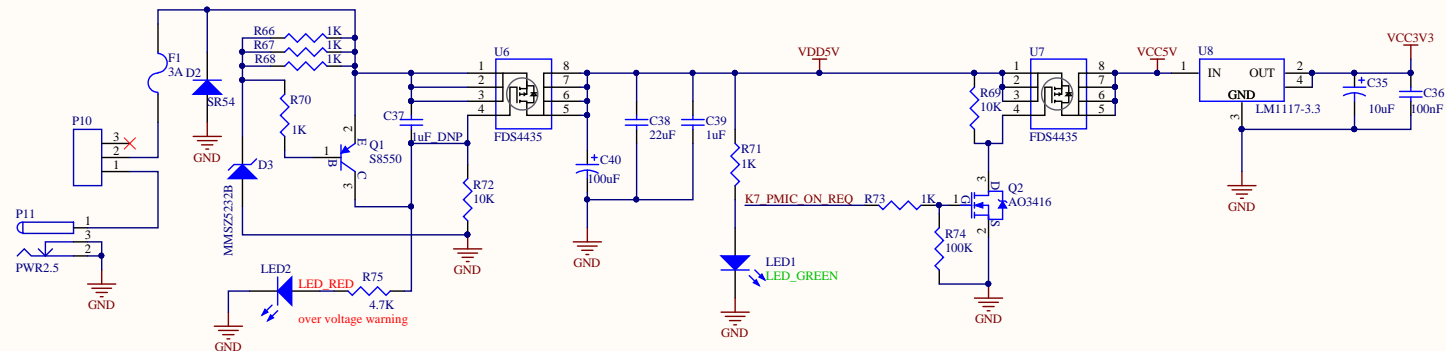
Revision History

Rev.code	Date	Description
V1.0	2018.11.12	初版
V1.1	2018.12.25	1、将开发板丝印由OK1050-S改为OKRT10xx-S 2、将网络PHY芯片的30、31脚由原来的1K下拉改为4.7K上拉（配置为100M自动协商） 3、网口座的LED灯串阻由0R改为220R 4、调整底板部分丝印

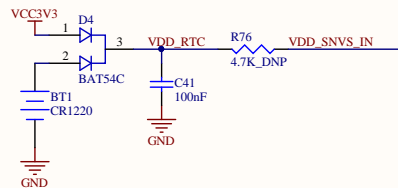
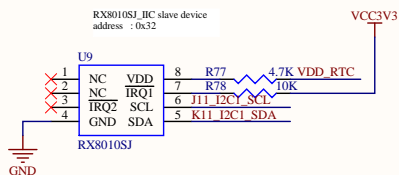
图纸名称	RELEASE-SchDoc		
设计	郝成东	标准化	
审核	*	批准	
工艺	*	日期	
版本	1.0	页码	第 1 页 共 7 页



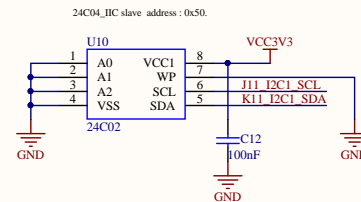
POWER



RTC



E2PROM



P12	1	2	3	4	5	6	7	8	9	10	11	12	13	14
GND	1	2	3	4	5	6	7	8	9	10	11	12	13	14
J2 SD1 D3	2	25	26	27	28	29	30	31	32	33	34	35	36	37
J3 SD1 CLK0/SP1 CS0	3	24	25	26	27	28	29	30	31	32	33	34	35	36
K1 SD1 D1/SP1 D1	4	23	24	25	26	27	28	29	30	31	32	33	34	35
M6 ONOFF	5	22	23	24	25	26	27	28	29	30	31	32	33	34
E8 GPIO2 IO02	6	21	22	23	24	25	26	27	28	29	30	31	32	33
D7 ENET MDC	7	20	21	22	23	24	25	26	27	28	29	30	31	32
C11 ENET2 RX DATA1	8	19	20	21	22	23	24	25	26	27	28	29	30	31
D11 ENET2 RX EN	9	18	19	20	21	22	23	24	25	26	27	28	29	30
E10 ENET2 TX EN	10	17	18	19	20	21	22	23	24	25	26	27	28	29
D10 ENET2 TX DATA1	11	16	17	18	19	20	21	22	23	24	25	26	27	28
D9 BT_CFG_6	12	15	16	17	18	19	20	21	22	23	24	25	26	27
GND	13	14	15	16	17	18	19	20	21	22	23	24	25	26

Header 2 x 13_DNP

P13	1	2	3	4	5	6	7	8	9	10	11	12	13	14
GND	1	2	3	4	5	6	7	8	9	10	11	12	13	14
F11 BOOT_MODE0/SAL1_TX_SYNC	2	25	26	27	28	29	30	31	32	33	34	35	36	37
A7 SEMC_RDY	3	24	25	26	27	28	29	30	31	32	33	34	35	36
B13 ENET1_TX_CLK	4	23	24	25	26	27	28	29	30	31	32	33	34	35
E12 ENET1_RX_DATA0	5	22	23	24	25	26	27	28	29	30	31	32	33	34
C13 ENET1_RX_ER	6	21	22	23	24	25	26	27	28	29	30	31	32	33
B12 ENET1_TX_DATA0	7	20	21	22	23	24	25	26	27	28	29	30	31	32
F13 SAL1_RXD	8	19	20	21	22	23	24	25	26	27	28	29	30	31
SAL1_RXD	9	18	19	20	21	22	23	24	25	26	27	28	29	30
F14 SAL1_TXD	10	17	18	19	20	21	22	23	24	25	26	27	28	29
M11 ENET1_nRST	11	16	17	18	19	20	21	22	23	24	25	26	27	28
G11 ENET2_nRST	12	15	16	17	18	19	20	21	22	23	24	25	26	27
VCC3V3	13	14	15	16	17	18	19	20	21	22	23	24	25	26

Header 2 x 13_DNP

VCC3V3

R15 10K_DNP F13 SAL1_RXD JTAG_MOD

P14	1	2	3	4	5	6	7	8	9	10	11	12	13	14
K14 UART1_TXD	1	2	3	4	5	6	7	8	9	10	11	12	13	14
L14 UART1_RXD	2	25	26	27	28	29	30	31	32	33	34	35	36	37
M7 POR_B	3	24	25	26	27	28	29	30	31	32	33	34	35	36
D14 WDOG_B	4	23	24	25	26	27	28	29	30	31	32	33	34	35
D13 SD1_CD	5	22	23	24	25	26	27	28	29	30	31	32	33	34
L10 CAN2_RXD	6	21	22	23	24	25	26	27	28	29	30	31	32	33
M12 UART2_RXD	7	20	21	22	23	24	25	26	27	28	29	30	31	32
M14 CSI_PWDN	8	19	20	21	22	23	24	25	26	27	28	29	30	31
K11 I2C1_SDA	9	18	19	20	21	22	23	24	25	26	27	28	29	30
K10 CSI_HSYNC	10	17	18	19	20	21	22	23	24	25	26	27	28	29
VCC3V3	11	16	17	18	19	20	21	22	23	24	25	26	27	28
V CORE5V	12	15	16	17	18	19	20	21	22	23	24	25	26	27
V CORE5V	13	14	15	16	17	18	19	20	21	22	23	24	25	26

Header 2 x 13_DNP

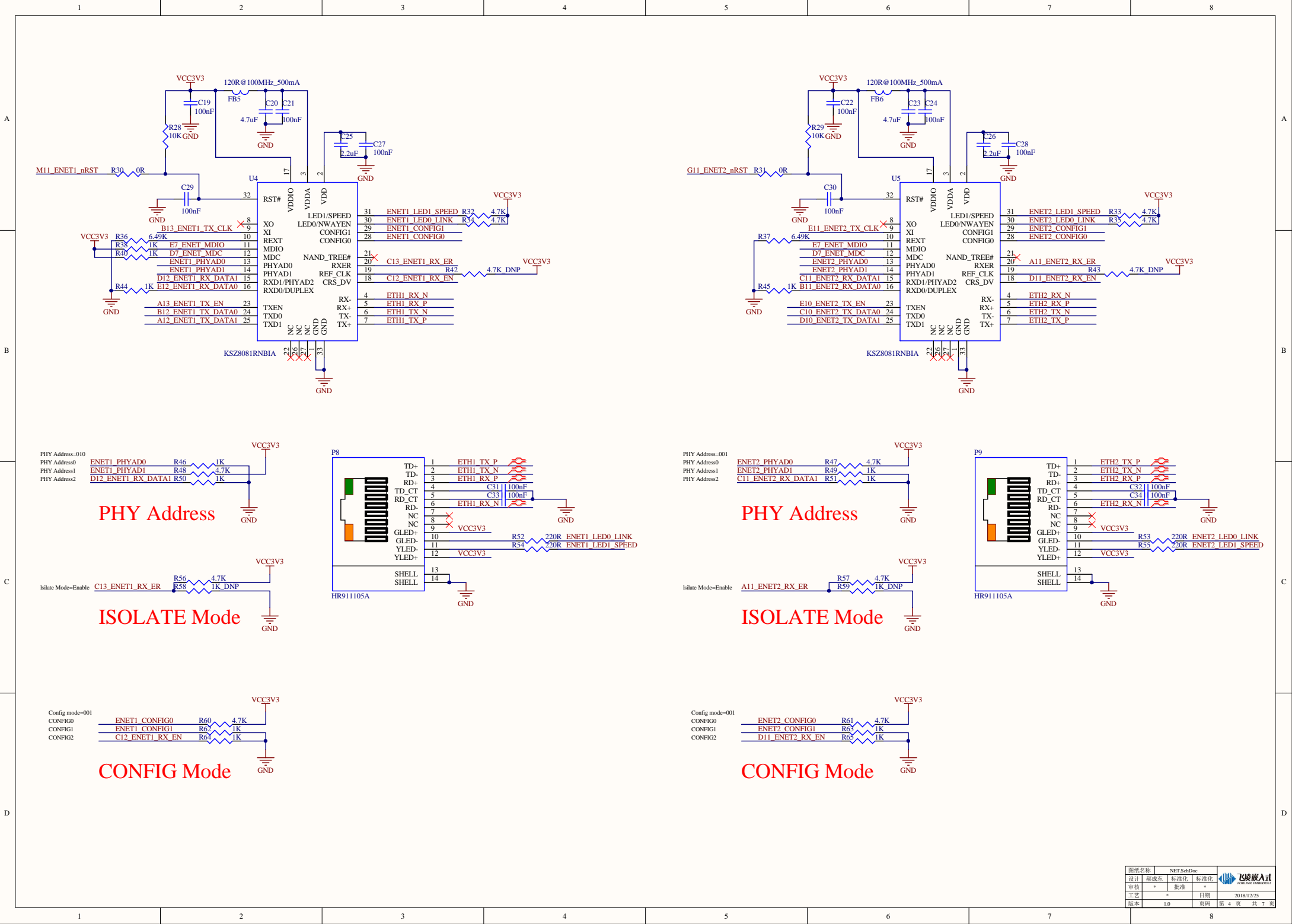
P22	1	2	3	4	5	6	7	8	9	10
G12 CSI_D3/ROW0	1	2	3	4	5	6	7	8	9	10
J14 CSI_D2/COL0	2	25	26	27	28	29	30	31	32	33
L13 CSI_D7/ROW2	3	24	25	26	27	28	29	30	31	32
H13 CSI_D9/ROW3	4	23	24	25	26	27	28	29	30	31
L12 CSI_PIXCLK	5	22	23	24	25	26	27	28	29	30
H11 CSI_D4/COL1	6	21	22	23	24	25	26	27	28	29
H12 CSI_D5/ROW1	7	20	21	22	23	24	25	26	27	28
J13 CSI_D6/COL2	8	19	20	21	22	23	24	25	26	27
M13 CSI_D8/COL3	9	18	19	20	21	22	23	24	25	26
K12 CSI_MCLK	10	17	18	19	20	21	22	23	24	25

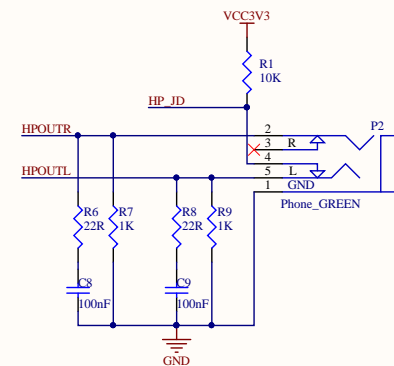
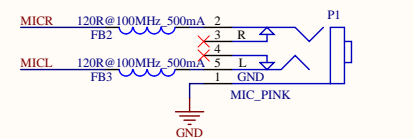
Header 5x2_DNP

P15	1	2	3	4	5	6	7	8
GND	1	2	3	4	5	6	7	8
M4 FlexSPI_D0_B	1	2	3	4	5	6	7	8
P2 FlexSPI_CLK_B	2	3	4	5	6	7	8	9
VCC3V3	3	4	5	6	7	8	9	10

Header 4x2_DNP

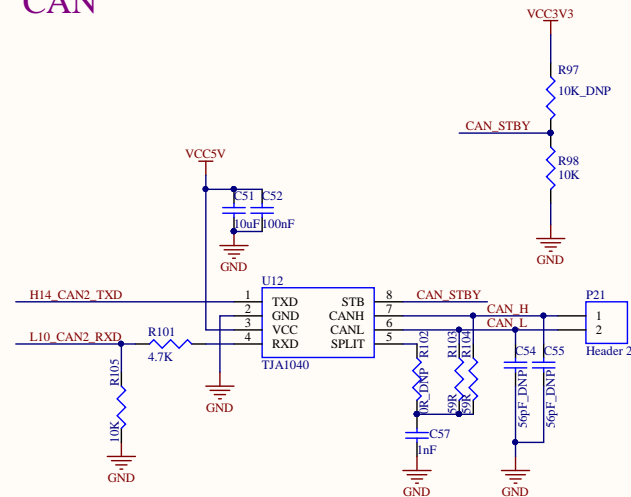
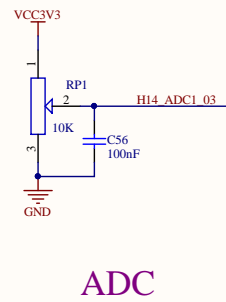
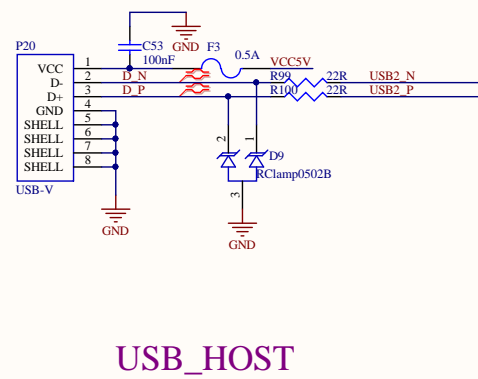
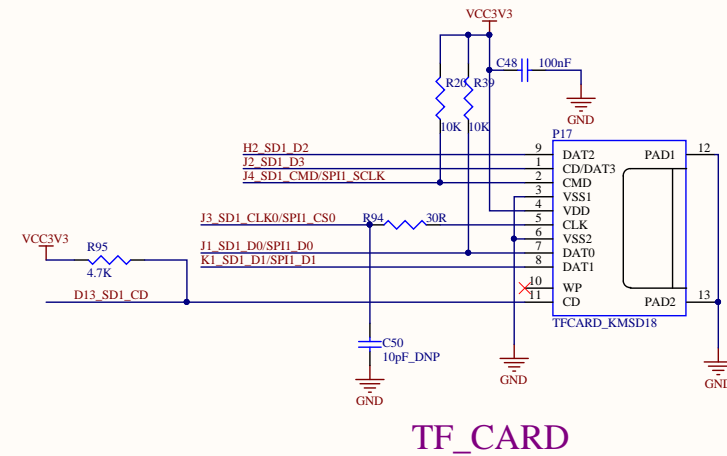
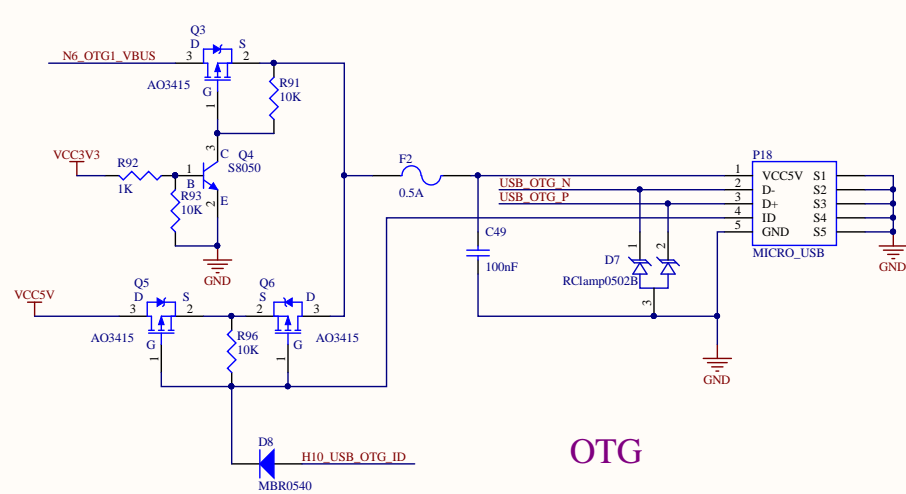
KEY_PAD

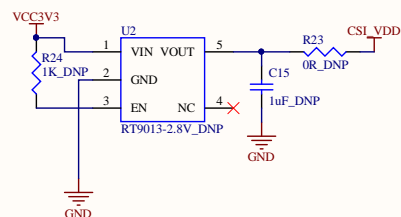
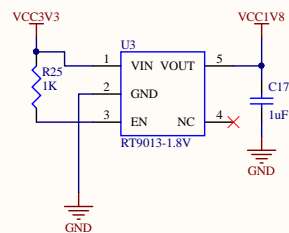
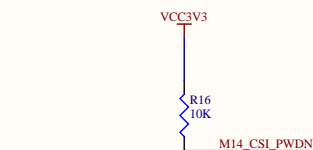




Pin connection diagram for the SPI interface. The diagram shows a blue box representing the SPI peripheral with pins 1 through 10. Pin 1 is connected to J4_SD1_CMD/SPI1_SCLK. Pin 2 is connected to J1_SD1_D0/SPI1_D0. Pin 3 is connected to J3_SD1_CLK0/SPI1_CS0. Pin 4 is connected to J1_SD1_D1/SPI1_D1. Pin 5 is connected to J3_SD1_D1/SPI1_D1. Pin 6 is connected to J1_SD1_D2/SPI1_D2. Pin 7 is connected to J3_SD1_D2/SPI1_D2. Pin 8 is connected to J1_SD1_D3/SPI1_D3. Pin 9 is connected to J3_SD1_D3/SPI1_D3. Pin 10 is connected to J1_SD1_D4/SPI1_D4. The VCC5V pin is connected to the positive supply, and the GND pin is connected to ground.

SPI





OV7725

