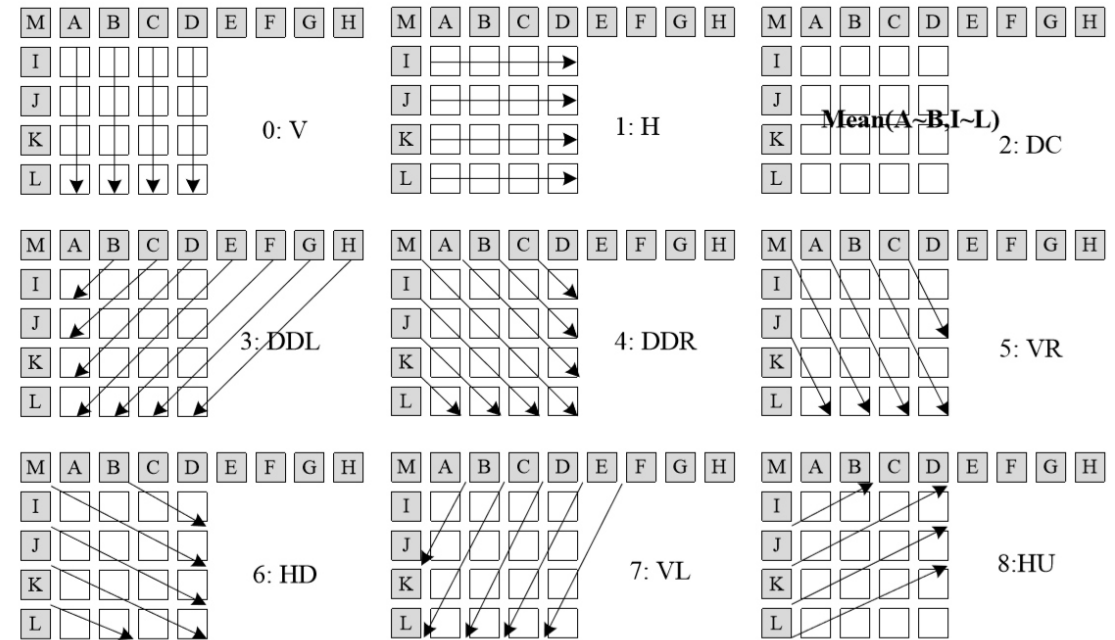


# ABOUT THE PROJECT

*Key words: H.264, video codec, intra prediction*

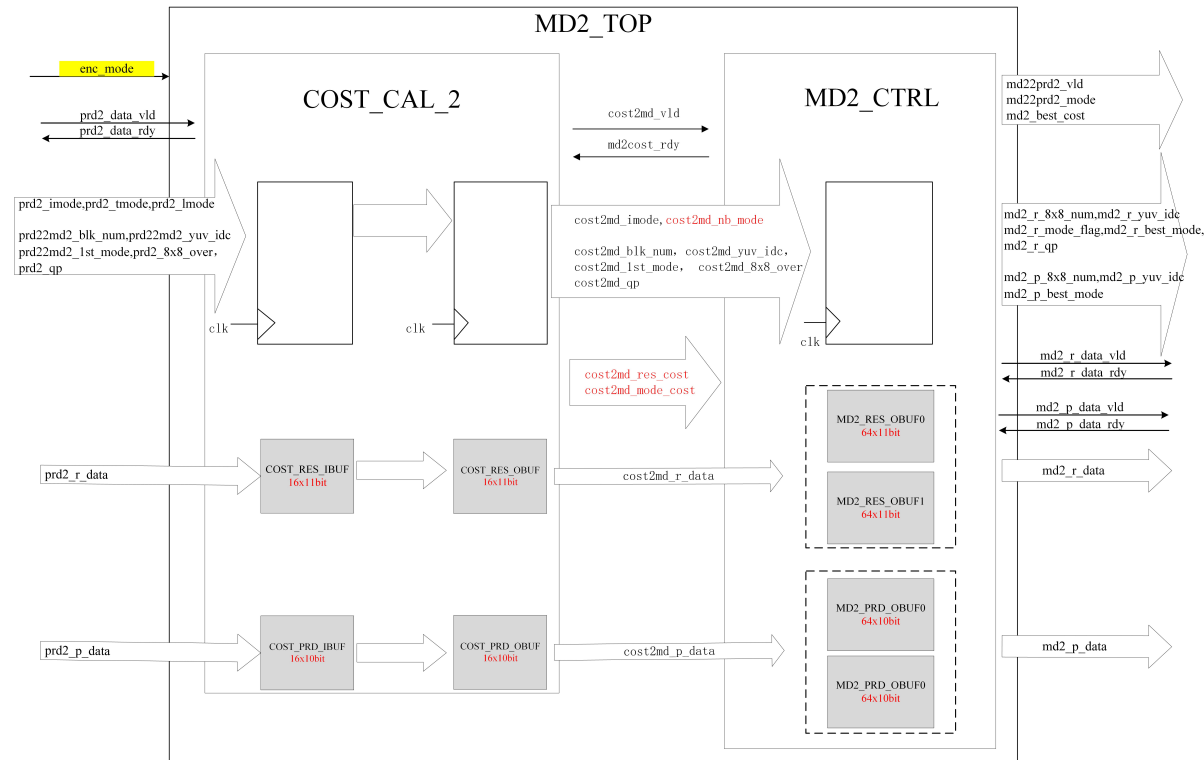
## H.264 standard



- The project aims to design a commercial video codec chip in cooperation with an enterprise.

# RECENT WORK

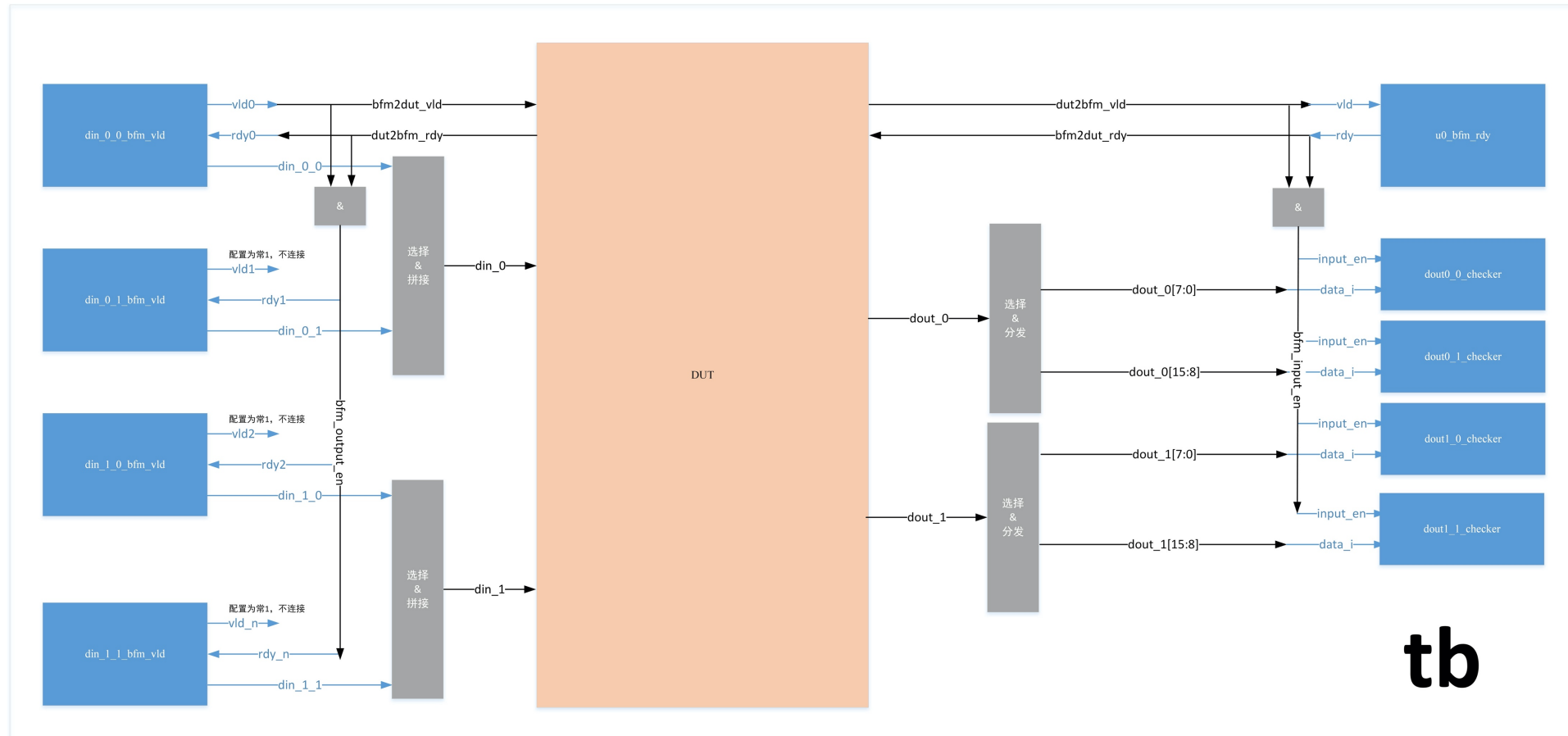
- Participated in the design of several hardware modules such as reference pixel prediction and mode selection according to H.264 codec standards.



- VLSI Architecture of Mode Decision(MD). MD is used to choose the best prediction mode based on the calculation of specific cost.

# RECENT WORK

- Constructed the bus function model (BFM) with SystemVerilog, containing functions of random valid/ready handshake response, incentive vector generation and DUT output data check, and providing support for hardware design verification of the project.



Architecture of testbench based on BFM.