

	alu_ctrl	mult_rf_ren	mult_rf_wen	hi_lo_sel	wbmux1_sel	
ADD	010	00	00		0	0
SUB	110	00	00		0	0
AND	000	00	00		0	0
OR	001	00	00		0	0
SLT	111	00	00		0	0
MULTU	000	11	00		0	0
MFHI	000	00	10		1	1
MFLO	000	00	01		0	1
JR	000	00	00		0	0
SLL	000	00	00		0	0
SLR	000	00	00		0	0

	branch	jump	reg_dst	we_reg	alu_src
R-type	0	00	1	1	0
ADDI	0	00	0	1	1
BEQ	1	00	0	0	0
J-type	0	11	0	0	0
SW	0	00	0	0	1
LW	0	00	0	1	1
JAL	0	11	0	1	0

wbmux3_sel	left_or_right
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0	0
0	0
0	0
0	0
0	0
0	0
0	0
0	0
0	0
0	0
1	0
1	1

we_dm	dm2reg	alu_op	jalsel
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0	0	11	0
0	0	00	0
0	0	01	0
0	0	00	0
1	0	00	0
0	1	00	0
0	0	00	1