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Single CFOA-based active Negative Group Delay circuits for signal anticipation

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ABSTRACT

The group delay of a signal prior to data monitoring is a crucial consideration in today's real-time applications, especially for those that involve long sensor arrays or high-order filters. In this article, nine new second-order Negative Group Delay (NGD) circuits based on Current Feedback Operation Amplifier (CFOA) are proposed, and their transfer functions are demonstrated. These circuits have a wide range of applications, from audio to mechanical signals and sensor signal anticipation. An example design procedure is provided for one of the introduced circuits. A time-domain analysis is performed using both a single-tone sinusoidal and a band-limited audio recording in the frequency range of 1 Hz to 500 Hz. The article assesses the change in the signal using Root Mean Square Error (RMSE) and cross-correlation. Furthermore, the relationship between the NGD value and the operation range of the circuit is investigated and verified experimentally. The results show that an NGD value of approximately 100 μ s can be achieved with an amplitude error of 0.86% for a single-tone input and 1.54% for an audio recording, and an operation range of about 650 Hz.

1. Introduction

Negative Group Delay (NGD) is an intriguing physical phenomenon that can be utilized for signal prediction. In these circuits, the output visually appears to be time-advanced compared to the input, making signal prediction possible, but this effect does not violate the causality principle [1]. The crucial aspect is that the input signal must be band-limited, meaning that sudden jumps in the input signal are not allowed, and the derivatives of the input signal must remain bounded. Furthermore, NGD typically occurs within a specific frequency band in these circuits [2]. Although NGD can be observed in passive circuits, active elements are necessary if the output signal is to be used to drive other circuits or systems. The phase delay of a system refers to the phase shift of the single sinusoidal input to the output of that system, while the group delay denotes the time delay of the signal envelope. Group delay is an important term for systems that process signals with multiple frequency components, which is the norm in real-world applications [3]. A nonlinear group delay means that different phase delays occur for the different frequency components of the input, resulting in distortion in the output. There have been studies of NGD in both physics and electronics disciplines. The causality and applicability of NGD, as well as its physical aspects, are discussed in Refs. [4–9],

which show that the system does not violate causality. The output is a reshaped signal such that its peak is ahead of the input.

In this article, we presented nine new second-order single Current Feedback Operation Amplifier (CFOA)-based NGD circuits, obtained by computer-aided design method [10], which may be useful for communication or control applications. Section 2 introduces the NGD circuit theory, describes the CFOA, a literature review of active NGD topologies, and presents the new circuits. Section 3 describes the circuit design method, while a non-ideal study of a selected NGD circuit is given in Section 4. Section 5 is focused on the simulation and experimental validations. Performance comparison with selected state-of-the-art solutions is shown in Section 6. Finally, Section 7 concludes the study.

2. Description of the theory, CFOA, and active NGD topologies

This section introduces the NGD circuit theory. The description of the CFOA and the literature review of active NGD topologies are presented.

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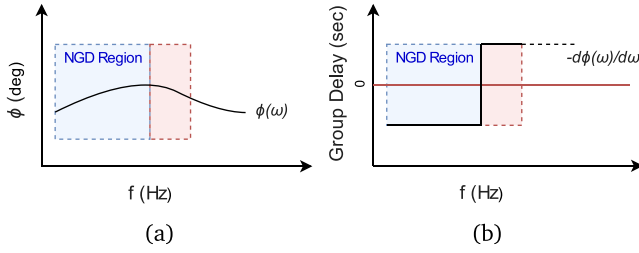


Fig. 1. (a) Phase response example of a stable NGD circuit and (b) its group delay.

Table 1
The NGD circuit topologies in the literature.

Reference	Circuit topology	Transfer function
[6]	OA Based Active RLC Filter	$\frac{b_2 s^2 + b_1 s + b_0}{a_2 s^2 + a_1 s + a_0}$, $a_1 = b_1, a_0 = b_0$
[11]	Passive RLC Network	$\frac{(a_1 b_0 + 1)b_1 s + a_1}{b_1 a_1 s}$
[12]	OA Based Differentiator	$\frac{b_1 s + b_0}{b_1 s + a_0 + b_0}$
[14,17]	Passive RC Filter	$\frac{b_1 s + b_0}{b_1 s + a_0 + b_0}$
[15]	OA Based Active RC Filter	$\frac{b_1 s + b_0}{a_1 s + a_0}$
[16]	Cascaded CFOA-Based Active Filter	$\frac{b_2 s^2 + b_1 s + b_0}{a_1 s + a_0}$

2.1. Theory and mathematical description

In a linear time-invariant (LTI) system, for the output y and the input x , the transfer function (TF) of the system is given as (1).

$$H(j\omega) = \frac{Y(j\omega)}{X(j\omega)}, \quad (1)$$

where $Y(j\omega)$ and $X(j\omega)$ are the output and the input signal's Laplace transforms, with $(s \rightarrow j\omega)$, respectively. For the given system TF, the phase and group delay are respectively defined as (2) and (3).

$$\phi(\omega) = \arg[H(j\omega)] = \arg(Y) - \arg(X), \quad (2)$$

$$\tau_g(\omega) = -\frac{d\phi(\omega)}{d\omega}. \quad (3)$$

Eq. (3) indicates that a phase that increases monotonically with frequency results in a negative group delay. This, however, implies an unstable system. A stable system with negative group delay may have a phase response as shown in Fig. 1, where the phase increases to a certain point and then decreases. In this case, the group delay is negative as long as the phase is increasing with frequency [11].

The summary of the previously proposed NGD circuit (NGDC) topologies can be found in Table 1. An intriguing electronic circuit with negative group delay is presented in [12]. There have been several studies on NGD systems utilizing active RC and RLC filters with operational amplifiers (OA) [6,12,13]; passive RC and RLC networks [11,14]; or through mathematical modeling of NGD-based circuits using Taylor series prediction [15]. Only one topology is a cascaded NGD circuit based on CFOA [16]. The NGD values reported in the studies vary from ns to μ s. In the next section, novel CFOA-based active NGDCs will be introduced, followed by a design and application example.

2.2. The CFOA and proposed active NGD topologies

The mathematical description of the CFOA is given in (4) [18].

$$\begin{bmatrix} v_- \\ i_z \\ v_w \end{bmatrix} = \begin{bmatrix} 1 & 0 & 0 \\ 0 & 1 & 0 \\ 0 & 0 & 1 \end{bmatrix} \begin{bmatrix} v_+ \\ i_- \\ v_z \end{bmatrix}. \quad (4)$$

This article introduces nine novel circuits obtained by computer-aided design method [10]. In Fig. 2, all presented circuits have a second-order TF using five passive components, as listed in Table 2. Although, in practice, circuits employing only grounded capacitors are preferred, new IC technologies offer floating capacitor realization possibility as a double poly (poly1-poly2) or metal-insulator-metal (MIM) capacitor [19]. Alternatively, P-channel MOS (PMOS) varactors also implement floating capacitors [3]. In the introduced CFOA-based NGD circuits, the highest-order s parameter coefficient in the denominator has the same value as the highest-order s parameter coefficient in the numerator. Moreover, the coefficient of the denominator's s parameter can always be made smaller than the coefficient of the numerator's s parameter. The general TF for the second-order $H_2(s)$ system is given by (5).

$$H_2(s) = \frac{a_0 + a_1 s + a_2 s^2}{b_0 + b_1 s + b_2 s^2}, \quad (5)$$

while from (5), the phase and group delay responses can be expressed as (6) and (7):

$$\phi_2(\omega) = \arg[H_2(j\omega)] = \arctan\left(\frac{a_1 \omega}{a_0 - a_2 \omega^2}\right) - \arctan\left(\frac{b_1 \omega}{b_0 - b_2 \omega^2}\right), \quad (6)$$

$$\tau_{g2}(\omega) = -\frac{d\phi_2(\omega)}{d\omega}. \quad (7)$$

NGD can be achieved in the second-order TFs where a_1 is smaller than b_1 and both are positive numbers. The operation ranges and parameters are defined in the design section.

3. NGD circuit design method

This section describes a CFOA-based NGD circuit synthesis method. After the topological description, the detailed NGD analysis is introduced.

3.1. Description of the design example

The circuit shown in Fig. 2(a) has been selected as an example to demonstrate the design method. Its TF is given in (8).

$$H_{2(a)}(s) = \frac{G_1 G_2 + G_1 G_3 + C_1 G_1 s + C_1 C_2 s^2}{G_1 G_3 + (C_1 G_1 - C_2 G_2)s + C_1 C_2 s^2}. \quad (8)$$

3.1.1. Stability constraints

First, the stability constraints of the circuit are determined. The stability condition for the TF given in (8) can be found in (9).

$$C_1 R_2 > C_2 R_1. \quad (9)$$

3.1.2. The phase and group delay calculation

The phase response of the system can be expressed as (10).

$$\phi_{2(a)}(\omega) = \arctan\left(\frac{C_1 G_1 \omega}{G_1 G_2 + G_1 G_3 - C_1 C_2 \omega^2}\right) - \arctan\left(\frac{(C_1 G_1 - C_2 G_2)\omega}{G_1 G_3 - C_1 C_2 \omega^2}\right). \quad (10)$$

Using (3) and (10), the group delay is given in (11).

$$\tau_{g2(a)}(\omega) = \frac{(C_1 G_1 - C_2 G_2)(G_1 G_3 + C_1 C_2 \omega^2)}{(C_1 G_1 - C_2 G_2)^2 \omega^2 + (G_1 G_3 - C_1 C_2 \omega^2)^2} - \frac{C_1 G_1 [G_1 (G_2 + G_3) + C_1 C_2 \omega^2]}{C_1^2 G_1^2 \omega^2 + [G_1 (G_2 + G_3) - C_1 C_2 \omega^2]^2}. \quad (11)$$

In the design parameters section, the component values are selected to meet the conditions (12) at low frequencies.

$$\frac{C_1 G_1 \omega}{G_1 G_2 + G_1 G_3 - C_1 C_2 \omega^2} < 1, \quad \frac{(C_1 G_1 - C_2 G_2)\omega}{G_1 G_3 - C_1 C_2 \omega^2} < 1. \quad (12)$$

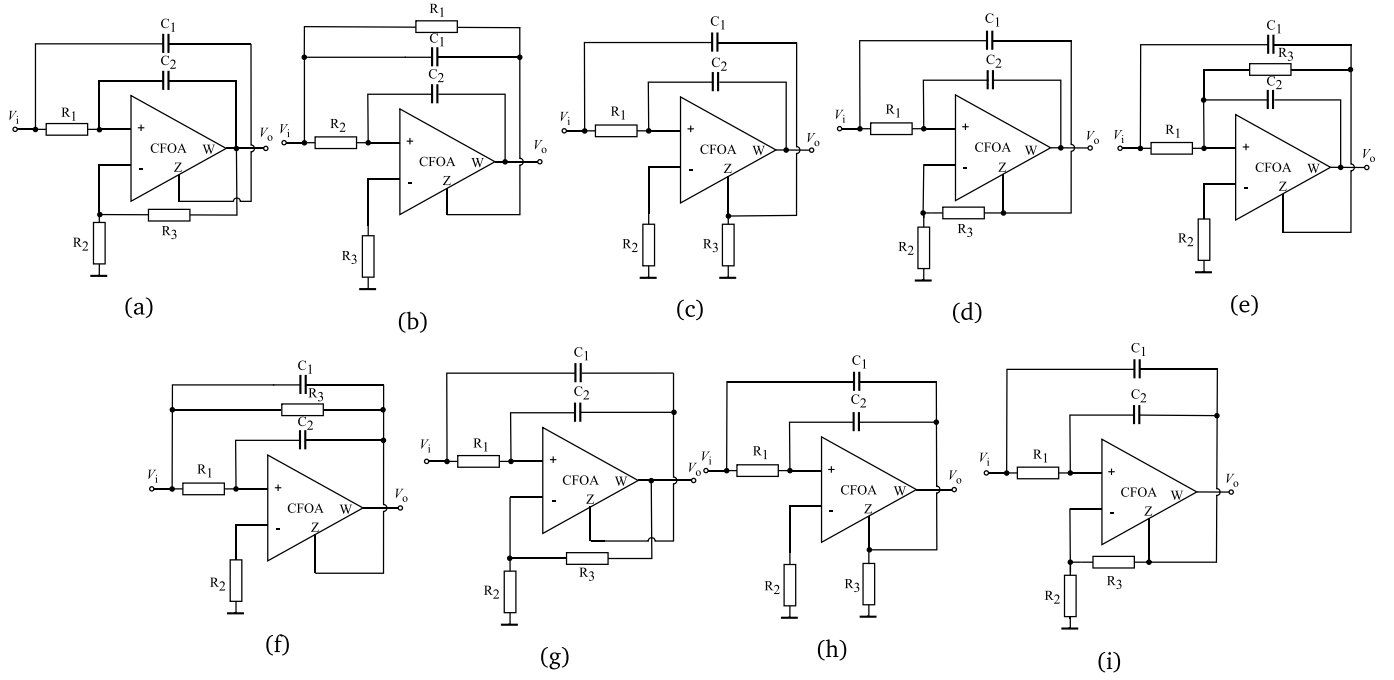


Fig. 2. (a)–(i) Proposed second-order NGD circuits.

Table 2

The transfer functions $H(s)$ of proposed second-order circuits, depicted in Fig. 2, $1/G_n = R_n$.

Circuit	Transfer function	Matching condition	# Passive components
(a)	$\frac{G_1 G_2 + G_1 G_3 + C_1 G_1 s + C_1 C_2 s^2}{G_1 G_3 + (C_1 G_1 - C_2 G_2)s + C_1 C_2 s^2}$	$C_1 G_1 > C_2 G_2$	5
(b)	$\frac{G_1 G_2 + G_2 G_3 + (C_1 G_2 + C_2 G_1)s + C_1 C_2 s^2}{G_1 G_2 + (C_1 G_2 + C_2 G_1 - C_2 G_3)s + C_1 C_2 s^2}$	$C_1 G_2 + C_2 G_1 > C_2 G_3$	5
(c)	$\frac{G_1 G_2 + C_1 G_1 s + C_1 C_2 s^2}{G_1 G_3 + (C_1 G_1 + C_2 G_3 - C_2 G_2)s + C_1 C_2 s^2}$	$C_1 G_1 + C_2 G_3 > C_2 G_2$	5
(d)	$\frac{G_1 G_2 + 2G_1 G_3 + C_1 G_1 s + C_1 C_2 s^2}{2G_1 G_3 + (C_1 G_1 - C_2 G_2)s + C_1 C_2 s^2}$	$C_1 G_1 > C_2 G_2$	5
(e)	$\frac{G_1 G_3 + G_1 G_2 + (C_1 G_1 + C_1 G_3)s + C_1 C_2 s^2}{G_1 G_3 - G_2 G_3 + (C_1 G_1 + C_1 G_3 - C_2 G_2)s + C_1 C_2 s^2}$	$C_1 G_1 + C_1 G_3 > C_2 G_2$; $G_1 > G_2$	5
(f)	$\frac{G_1 G_3 + G_1 G_2 + (C_1 G_1 + C_2 G_1 + C_2 G_3)s + C_1 C_2 s^2}{G_1 G_3 + (C_1 G_1 + C_2 G_1 + C_2 G_3 - C_2 G_2)s + C_1 C_2 s^2}$	$C_1 G_1 + C_2 G_1 + C_2 G_3 > C_2 G_2$	5
(g)	$\frac{G_1 G_2 + G_1 G_3 + (C_1 G_1 + C_2 G_1)s + C_1 C_2 s^2}{G_1 G_3 + (C_1 G_1 + C_2 G_1 - C_2 G_2)s + C_1 C_2 s^2}$	$C_1 G_1 + C_2 G_1 > C_2 G_2$	5
(h)	$\frac{G_1 G_2 + (C_1 G_1 + C_2 G_1)s + C_1 C_2 s^2}{G_1 G_3 + (C_1 G_1 + C_2 G_1 - C_2 G_2 + C_2 G_3)s + C_1 C_2 s^2}$	$C_1 G_1 + C_2 G_1 + C_2 G_3 > C_2 G_2$	5
(i)	$\frac{G_1 G_2 + 2G_1 G_3 + (C_1 G_1 + C_2 G_1)s + C_1 C_2 s^2}{2G_1 G_3 + (C_1 G_1 + C_2 G_1 - C_2 G_2)s + C_1 C_2 s^2}$	$C_1 G_1 + C_2 G_1 > C_2 G_2$	5

The values of the capacitors are in the order of 10^{-9} Farads, while the conductance values are in the order of 10^{-4} Siemens.

The phase response in (10) is approximated as (13):

$$\phi_{2(a)}(\omega) \cong \frac{C_1 G_1 \omega}{G_1 G_2 + G_1 G_3 - C_1 C_2 \omega^2} - \frac{(C_1 G_1 - C_2 G_2) \omega}{G_1 G_3 - C_1 C_2 \omega^2}. \quad (13)$$

3.1.3. Zero cross frequency calculation

Using (3) and (13), the approximate group delay can be expressed as (14).

$$\tau_{g2(a)}(\omega) = \frac{(C_1 G_1 - C_2 G_2)(G_1 G_3 + C_1 C_2 \omega^2)}{(C_1 C_2 \omega^2 - G_1 G_3)^2} - \frac{G_1 C_1 (G_1 G_2 + G_1 G_3 + C_1 C_2 \omega^2)}{(C_1 C_2 \omega^2 - G_1 G_2 - G_1 G_3)^2} \quad (14)$$

The first term of (14) is positive at low frequencies in accordance with the stability condition (9). Meanwhile, the second term of (14) is negative at low frequencies. The second term of (14) is the dominant

factor in our case in terms of the NGD value, and it is assumed to determine the frequency where the group delay changes from negative to positive. The frequency at which the group delay changes from negative to positive is estimated by finding the root of the denominator of the second term and is given in (15).

$$\omega_{zero_cross_2(a)} \cong \sqrt{\frac{R_2 + R_3}{R_1 R_2 R_3 C_1 C_2}}. \quad (15)$$

Note that this is a rough estimate of the zero crossing frequency and does not give its actual value. The exact value can be determined from the simulation graph. Considering above given conditions and to simplify the design complexity, (14) is reduced to (16). This reduced form is used in the group delay calculations within the stability region.

$$\tau_{g2(a)}(\omega) \cong -\frac{C_1 G_1}{G_1 G_3 + G_1 G_2 - C_1 C_2 \omega^2} + \frac{C_1 G_1 - C_2 G_2}{G_1 G_3 - C_1 C_2 \omega^2}. \quad (16)$$

At low frequencies, the group delay becomes as in (17):

$$\tau_{g2(a)}(\omega) \cong -\frac{C_1 G_1}{G_1 G_3 + G_1 G_2} + \frac{C_1 G_1 - C_2 G_2}{G_1 G_3}. \quad (17)$$

3.2. Design simplification & constraints

To simplify the calculation of NGD, equal resistances are selected, as expressed by Eq. (18):

$$R_1 = R_2 = R_3. \quad (18)$$

Moreover, the capacitance values are selected as:

$$C_2 = \alpha C_1, \text{ where } 0 < \alpha \leq 1. \quad (19)$$

By rearranging Eqs. (15) and (17) with the assumption of equal resistor values, the expressions (20) are obtained.

$$\tau_{g2(a)}(\omega) \cong C_1 R_1 \left(\frac{1}{2} - \alpha \right), \quad \omega_{zero_cross_2(a)} \cong \sqrt{\frac{2}{\alpha}} \frac{1}{R_1 C_1}. \quad (20)$$

The magnitude of the NGD decreases as the operating frequency increases. Therefore, it is necessary to maximize the magnitude of both values at the same time when the group delay is negative.

3.3. Performance metric

The zero crossing of the NGD function is given in (15), where the NGD value changes from negative to positive at that frequency. Therefore, a Figure of Merit (FoM) is determined as [17]:

$$\text{FoM} = \omega_{zero_cross_2(a)} \times \tau_{g2(a)}(\omega) \cong \sqrt{\frac{2}{\alpha}} \left(\frac{1}{2} - \alpha \right), \quad (21)$$

where $0 < \alpha \leq 1$. Note that the product of the zero crossing point and the NGD value is given in (21). By minimizing (21), the NGD can be achieved when $\alpha = 1$, which is the stability margin point where the two capacitors are equal in value. Eq. (21) also shows that the stability boundary occurs where the magnitude of the NGD value and the operating frequency range are maximized. To achieve a stable system, R_2 is selected to be larger than R_1 , when the capacitors are selected equal valued. The parameters for this selection are introduced in the Section 5.

It is important to note that in some cases, the gain can be reduced to zero when the group delay is negative. Therefore, the gain of the system at low frequencies is expressed as (22):

$$|H(\omega \cong 0)| \cong 1 + \frac{R_3}{R_2}. \quad (22)$$

Eq. (22) shows that a gain exists in the frequencies of operation where NGD is achieved.

4. Non-ideal and parasitic effects analysis

The present section is focused on a non-ideal study of the proposed NGD circuit.

4.1. Description of non-ideal CFOA

An equivalent non-ideal circuit of CFOA, including parasitic impedance, is shown in Fig. 3. Using standard notation, relations between its individual terminals can be described by the following hybrid matrix (23):

$$\begin{bmatrix} i_+ \\ v_- \\ i_Z \\ v_W \end{bmatrix} = \begin{bmatrix} Y_+ & 0 & 0 & 0 \\ \beta(s) & Z_- & 0 & 0 \\ 0 & \alpha(s) & Y_Z & 0 \\ 0 & 0 & \gamma(s) & Z_W \end{bmatrix} \begin{bmatrix} v_+ \\ i_- \\ v_Z \\ i_W \end{bmatrix}, \quad (23)$$

where $Y_+ = sC_+ + 1/R_+$, $Y_Z = sC_Z + 1/R_Z$ are parasitic admittances and $Z_k = R_k$ ($k = -$ and W) are the parasitic resistances at relevant

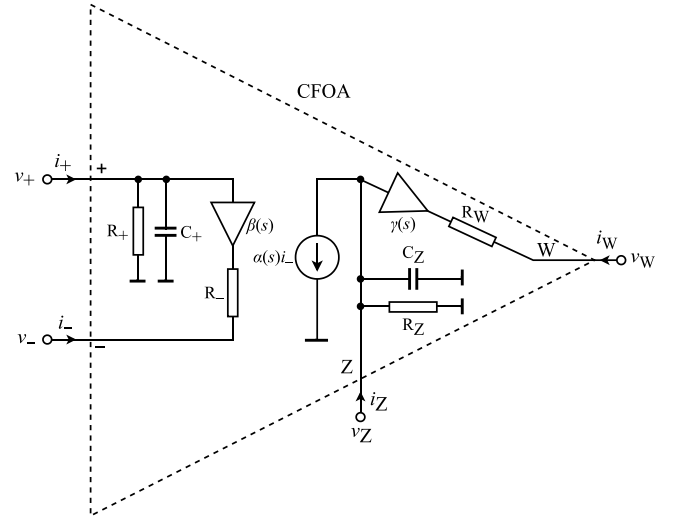


Fig. 3. An equivalent non-ideal circuit of CFOA including parasitic impedances.

terminals of the CFOA, respectively. Parameters $\alpha(s)$, $\beta(s)$, and $\gamma(s)$ are, respectively, frequency-dependent non-ideal current and voltage gains. Ideally, these parameters are equal to unity. Using a single-pole model, they can be defined as (24):

$$\alpha(s) = \frac{\alpha_o}{1 + \tau_\alpha s}, \quad \beta(s) = \frac{\beta_o}{1 + \tau_\beta s}, \quad \gamma(s) = \frac{\gamma_o}{1 + \tau_\gamma s}. \quad (24)$$

Here, α_o is DC current, and β_o and γ_o are DC voltage gains of the CFOA, respectively. The bandwidths $1/\tau_\alpha$, $1/\tau_\beta$, and $1/\tau_\gamma$ depend on the fabrication of devices. In current technologies, also used for the fabrication of Analog Devices AD844AN [20], the order of a few gigahertz is ideally equal to infinity. Hence, at low and medium frequencies, i.e., $f \ll (1/(2\pi)) \times \min\{1/\tau_\alpha, 1/\tau_\beta, 1/\tau_\gamma\}$, (24) becomes:

$$\alpha(s) \cong \alpha = 1 + \epsilon_{\alpha_i}, \quad \beta(s) \cong \beta = 1 + \epsilon_{\beta_v}, \quad \gamma(s) \cong \gamma = 1 + \epsilon_{\gamma_v}, \quad (25)$$

whereas ϵ_{α_i} , ϵ_{β_v} , and ϵ_{γ_v} are current and voltage tracking errors, respectively, and satisfy the inequalities $|\epsilon_{\alpha_i}| \ll 1$, $|\epsilon_{\beta_v}| \ll 1$, and $|\epsilon_{\gamma_v}| \ll 1$.

Considering the non-ideal current and voltage gains of the CFOA and re-analyzing the proposed circuits depicted in Fig. 2, a routine analysis yields non-ideal TFs of the circuits, presented in Table 3.

4.2. Non-ideal analysis of the design example

For a complete analysis of the circuit, it is also important to consider in detail the non-idealities of the readily available CFOA device Analog Devices AD844AN [20], as also shown in Fig. 3, where:

- the parasitic resistance R_+ and parasitic capacitance C_+ appear between the high-impedance terminal + of the CFOA and ground and their values are $R_+ = 10 \text{ M}\Omega$ || $C_+ = 2 \text{ pF}$, respectively,
- the non-zero parasitic resistance R_- at current input terminal – has value $R_- = 50 \text{ }\Omega$,
- the parasitic resistance R_Z and parasitic capacitance C_Z appear between the auxiliary terminal Z of the CFOA and ground and their values are $R_Z = 3 \text{ M}\Omega$ || $C_Z = 4.5 \text{ pF}$, respectively,
- the non-zero parasitic resistance R_W at voltage output terminal W has value $R_W = 15 \text{ }\Omega$.

Taking into account the non-ideal current and voltage gains of the CFOA and simultaneously the effect of non-idealities as mentioned above and re-analyzing the proposed NGD circuit shown in Fig. 2(a), the coefficients a_m and b_m for $m = \{0, 1, 2\}$ of non-ideal TF $H''_{2(a)}(s)$, phase $\phi''_{2(a)}(\omega)$, and group delay $\tau''_{g2(a)}(\omega)$ responses in (5)–(7) are as

Table 3Non-ideal transfer functions $H'(s)$ of proposed second-order circuits, depicted in Fig. 2, $1/G_n = R_n$.

Circuit	Transfer function	Matching condition	# Passive components
(a)	$\frac{\gamma(\alpha\beta G_1 G_2 + \alpha\beta G_1 G_3 + C_1 G_1 s + C_1 C_2 s^2)}{\alpha\gamma G_1 G_3 + [C_1 G_1 - \alpha\beta\gamma C_2 G_2 + \alpha\gamma C_2 G_3(1 - \beta)]s + C_1 C_2 s^2}$	$C_1 G_1 + \alpha\gamma C_2 G_3 > \alpha\beta\gamma C_2(G_2 + G_3)$	5
(b)	$\frac{\gamma[G_1 G_2 + \alpha\beta G_2 G_3 + (C_2 G_1 + C_1 G_2)s + C_1 C_2 s^2]}{G_1 G_2 + (C_1 G_2 + C_2 G_1 - \alpha\beta\gamma C_2 G_3)s + C_1 C_2 s^2}$	$C_1 G_2 + C_2 G_1 > \alpha\beta\gamma C_2 G_3$	5
(c)	$\frac{\gamma(\alpha\beta G_1 G_2 + C_1 G_1 s + C_1 C_2 s^2)}{G_1 G_3 + (C_1 G_1 - \alpha\beta\gamma C_2 G_2 + C_2 G_3)s + C_1 C_2 s^2}$	$C_1 G_1 + C_2 G_3 > \alpha\beta\gamma C_2 G_2$	5
(d)	$\frac{\gamma(\alpha\beta G_1 G_2 + \beta G_1 G_3 + \alpha\beta G_1 G_3 + C_1 G_1 s + C_1 C_2 s^2)}{G_1 G_3 + \alpha G_1 G_3 + [C_1 G_1 - \alpha\beta\gamma C_2 G_2 + C_2 G_3(1 + \alpha - \beta\gamma - \alpha\beta\gamma)]s + C_1 C_2 s^2}$	$C_1 G_1 + C_2 G_3(1 + \alpha) > \beta\gamma C_2[\alpha(G_2 + G_3) + G_3]$	5
(e)	$\frac{\gamma[\alpha\beta G_1 G_2 + G_1 G_3 + (C_1 G_1 + C_1 G_3)s + C_1 C_2 s^2]}{G_1 G_3 - \alpha\beta G_2 G_3 + [C_1 G_1 - \alpha\beta\gamma C_2 G_2 + C_1 G_3 + C_2 G_3(1 - \gamma)]s + C_1 C_2 s^2}$	$C_1 G_1 + C_1 G_3 + C_2 G_3 > \gamma C_2(\alpha\beta G_2 + G_3); G_1 > \alpha\beta G_2$	5
(f)	$\frac{\gamma[\alpha\beta G_1 G_2 + G_1 G_3 + (C_1 G_1 + C_2 G_1 + C_2 G_3)s + C_1 C_2 s^2]}{G_1 G_3 + (C_1 G_1 + C_2 G_1 - \alpha\beta C_2 G_2 + C_2 G_3)s + C_1 C_2 s^2}$	$C_1 G_1 + C_2 G_1 + C_2 G_3 > \alpha\beta C_2 G_2$	5
(g)	$\frac{\gamma[\alpha\beta G_1 G_2 + \alpha\beta G_1 G_3 + (C_1 G_1 + C_2 G_1)s + C_1 C_2 s^2]}{\alpha\gamma G_1 G_3 + [C_1 G_1 + C_2 G_1 - \alpha\beta C_2 G_2 - \alpha C_2 G_3(\beta - \gamma)]s + C_1 C_2 s^2}$	$C_1 G_1 + C_2 G_1 + \alpha\gamma C_2 G_3 > \alpha\beta C_2(G_2 + G_3)$	5
(h)	$\frac{\gamma[\alpha\beta G_1 G_2 + (C_1 G_1 + C_2 G_1)s + C_1 C_2 s^2]}{G_1 G_3 + (C_1 G_1 + C_2 G_1 - \alpha\beta C_2 G_2 + C_2 G_3)s + C_1 C_2 s^2}$	$C_1 G_1 + C_2 G_1 + C_2 G_3 > \alpha\beta C_2 G_2$	5
(i)	$\frac{\gamma[\alpha\beta G_1 G_2 + \beta G_1 G_3 + \alpha\beta G_1 G_3 + (C_1 G_1 + C_2 G_1)s + C_1 C_2 s^2]}{G_1 G_3 + \alpha G_1 G_3 + [C_1 G_1 + C_2 G_1 - \alpha\beta C_2 G_2 + C_2 G_3(1 + \alpha - \beta - \alpha\beta)]s + C_1 C_2 s^2}$	$C_1 G_1 + C_2[G_1 + G_3(1 + \alpha)] > \beta C_2[\alpha(G_2 + G_3) + G_3]$	5

$$\begin{aligned}
a_0 &= \beta G_1[\alpha\gamma(G_2 + G_3) + G_3 G_Z R_W], \\
a_1 &= \gamma C_1(G_1 + G_+)(G_2 R_- + G_3 R_- + 1) + G_1 R_W[\beta G_3(C_1 + C_Z) + C_2 G_Z(1 + G_2 R_- + G_3 R_-)], \\
a_2 &= (G_2 R_- + G_3 R_- + 1)[\gamma C_1(C_2 + C_+) + C_2 G_1 R_W(C_1 + C_Z)], \\
b_0 &= (G_1 + G_+)(G_Z + \alpha\gamma G_3 + G_2 G_Z R_- + G_3 G_Z R_W + G_2 G_3 G_Z R_W R_- + G_3 G_Z R_-), \\
b_1 &= G_3\{R_-[R_W\{G_Z[C_2(G_1 + G_2 + G_+) + G_2 C_+] + G_2(C_1 + C_Z)(G_1 + G_+)\} + G_Z(C_2 + C_+) + (C_1 + C_Z)(G_1 + G_+)] \\
&\quad + R_W\{G_Z[C_2(1 - \beta) + C_+] + (C_1 + C_Z)(G_1 + G_+)\} - \alpha\gamma[C_2(\beta - 1) - C_+]\} + G_2 R_-[C_2 G_Z R_W(G_1 + G_+) \\
&\quad + G_Z(C_2 + C_+) + (C_1 + C_Z)(G_1 + G_+)] + C_2 G_Z R_W(G_1 + G_+) + G_Z(C_2 + C_+) - \alpha\beta\gamma C_2 G_2 + (C_1 + C_Z)(G_1 + G_+), \\
b_2 &= C_2\{R_W\{G_3\{G_1[(G_1 + G_2 + G_+)(C_1 + C_Z) + G_Z C_+] + G_2[(G_1 + G_+)(C_1 + C_Z) + C_+ G_Z]\} - G_3(C_1 + C_Z)(\beta - 1) \\
&\quad + (G_1 + G_+)(C_1 + C_Z) + C_+ G_Z\} + (1 + (G_2 + G_3)R_-)(C_1 + C_Z)\} + C_+(C_1 + C_Z)[G_3 R_W(G_2 R_- + 1) + R_-(G_2 + G_3) + 1].
\end{aligned} \tag{26}$$

Box I.

in Box I. The effect of non-idealities on the proposed NGD circuit can be significantly minimized by properly selecting external passive components and/or by the precise design of the CFOA.

5. Simulations and experimental validations

The present section is focused on the simulation and experimental validations.

5.1. Simulation results

In Fig. 4, it is shown that the negative group delay is almost constant in the stability region with constant R_3 . The empty space on the right part of the plot represents positive group delay, while the empty space on the left side represents the unstable region. Additionally, when R_1 and R_2 are equal, R_3 affects the group delay value. However, it can be seen in Fig. 4 that R_3 does not affect whether the group delay is positive or negative when R_1 and R_2 are equal. For design purposes, let us also assume R_3 is equal to R_1 and R_2 as a starting point. This results in the flexibility of achieving NGD with the single parameter $C_1 = C_2$, without dependence on gain. The NGD and NGD range can be seen in Fig. 5. The capacitor values are selected as 2.2 nF, 22 nF, and 220 nF. The higher the capacitor value, the higher the NGD, but this also decreases the operation frequency given in (15).

The resistors are selected as:

$$R_1 = R_2\beta = R_3 = 10 \text{ k}\Omega. \tag{27}$$

Table 4

Description of the 4th-order SK-LPF with Bessel response.

Pass-band/Stop-band Frequency (Hz)	100/1 k
Gain (V/V)	1
Stop-band Attenuation (dB)	-65.9
Group Delay (μ s)	3400

Using Eq. (22) gives (28):

$$|H(\omega \cong 0)| \cong 2. \tag{28}$$

To achieve a NGD operation range of up to 650 Hz, the capacitors are selected as follows:

$$C_1 = C_2 = 22 \text{ nF}. \tag{29}$$

As mentioned, this selection is based on Eq. (15). To mitigate the risk of instability in the system, the value of resistor R_2 is set 2% higher than the other resistors, with a scaling factor of $\beta = 0.98$. The parameters specified in (27) and (29) are used to generate the results shown in Fig. 5, which demonstrates that the base-band gain is independent of the NGD value. The simulation results and theoretical calculations of the group delay are in good agreement at low frequencies, up to 650 Hz, as demonstrated in Fig. 6. The simulation was performed using the Analog Devices AD844AN [20] and OP200 [21] SPICE models and LT Spice simulation software.

The proposed NGD circuit was tested using a 4th-order Sallen-Key low-pass filter (SK-LPF) topology with Bessel response, as depicted in

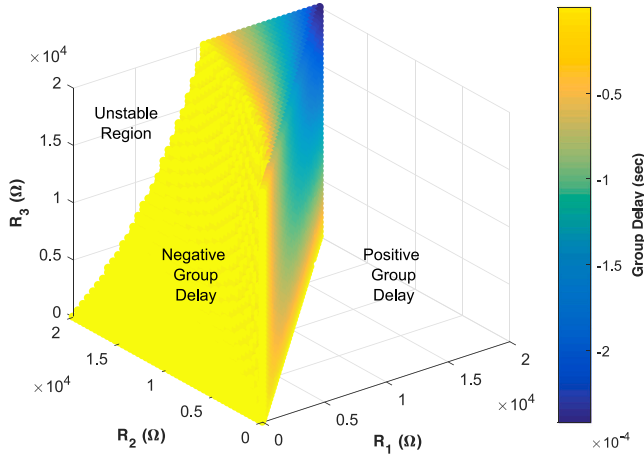


Fig. 4. Group delay vs. resistors @100 Hz, R_1 , R_2 , and R_3 swept from 1 Ω to 20 k Ω , $C_1 = C_2 = 22$ nF.

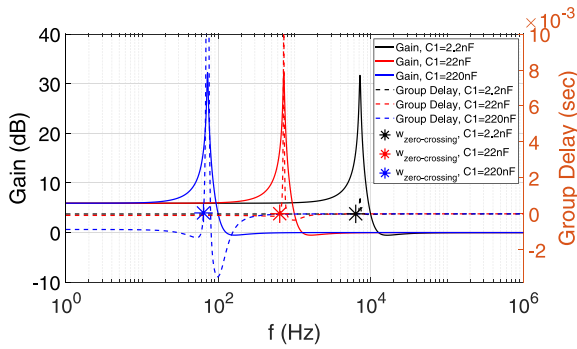


Fig. 5. AC analysis of the circuit II. $R_1 = R_2 = R_3 = 10$ k Ω , $C_1 = C_2$ is 2.2 nF, 22 nF, and 220 nF, $\beta = 0.98$ factor of R_2 for the stability.

Table 5

4th-Order SK-LPF passive component values.

Components	Values	
	Simulation	Experiments
$R_{1s1,2s1,1s2,2s2}$ (k Ω)	9.76, 11.8, 5.76, 6.65	9.64, 11.76, 5.71, 6.57
$C_{1s1,2s1,1s2,2s2}$ (nF)	110, 100, 261, 100	116, 100, 262, 95

Fig. 7. The simulation was again performed using the Analog Devices AD844AN [20] and OP200 [21] SPICE models and LT Spice simulation software. The parameters of the SK-LPF are summarized in Table 4, and the passive component values selected for the SK-LPF are listed in Table 5. The test was conducted by applying a pulse input signal to the test circuit and operation range of NGD up to 650 Hz was achieved. To illustrate the time advancement at the output of the NGD, a zoom of time-domain responses is provided in Fig. 8(a). To validate the circuit in a practical application, the SK-LPF in the circuit shown in Fig. 7 was substituted with an audio signal. The input audio signal was filtered to be within the operational range of the NGD, up to 500 Hz. The time-domain responses are presented in Fig. 8(b) and Fig. 8(c). The simulation results are in good agreement with theory.

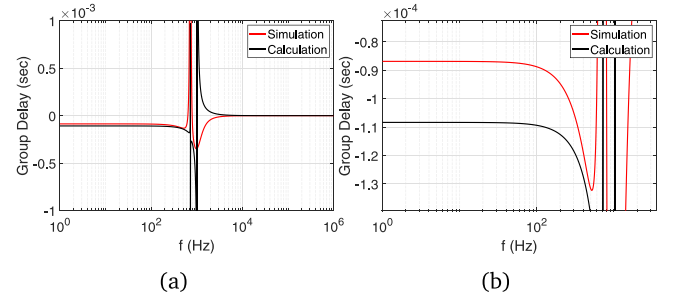


Fig. 6. Calculated and simulated (a) group delay-frequency responses, (b) zoom at low frequency.

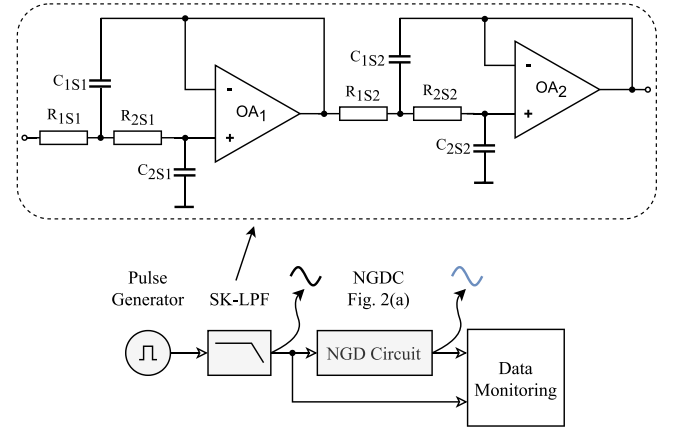


Fig. 7. Block diagram of the test system.

Table 6

The component values of NGD circuit shown in Fig. 2(a).

Components	Values
$R_{1,2,3}$ (k Ω)	9.8, 10.26, 9.8
$C_{1,2}$ (nF)	22, 22

Table 7

Performance comparison of the circuit in Fig. 2(a).

Parameter	Calculation	Simulation	Experiment
NGD (μ s)	108	101.6–112.5	98.4
Flat Group Delay Range (Hz)	1–650	1–650	700

5.2. Experimental verification

The experiment was performed to demonstrate the correlation between the simulated and measured values of the NGD circuit. The circuit in Fig. 2(a) was used for the setup, which consisted of an Analog Devices AD844AN [20], an OA OP200 [21] for the SK-LPF, a signal generator AFG3032C Tektronix, a signal analyzer MDO3104 Tektronix, and a voltage supply SPD-3606 GW INSTTEK; see Fig. 9. Amplifiers were supplied by ± 12 V. The component values used for the 4th-order SK-LPF and NGD circuit are listed in Table 5 and Table 6, respectively, and correspond to the circuit schematic in Fig. 7. The measurement results are depicted in Fig. 10. Moreover, to demonstrate the impact of capacitor values ($C_1 = C_2 = 2.2$ nF) on the operation range of the NGD circuit, the system was subjected to a sinusoidal input, as shown in Fig. 11. The results are in good agreement with the theory, as demonstrated in Table 7.

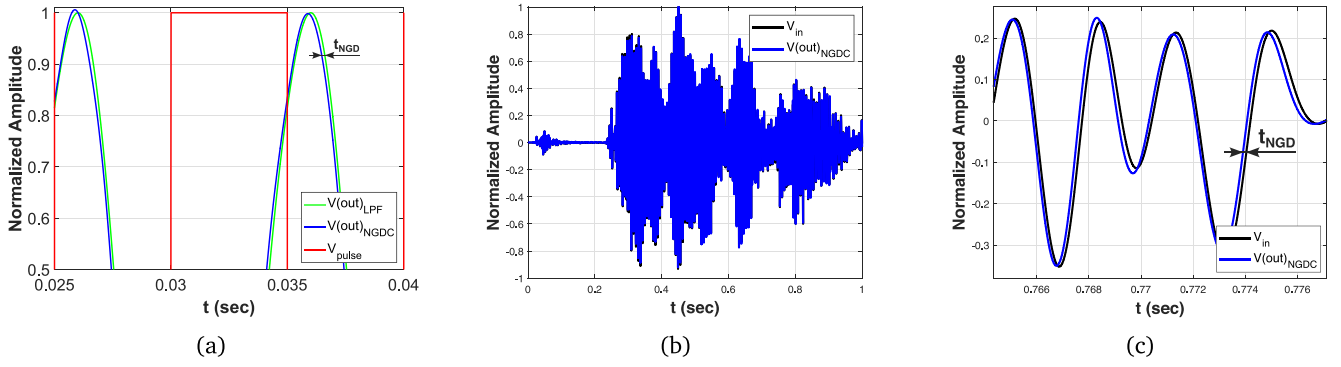


Fig. 8. Time domain results: (a) Simulated NGDC output with SK-LPF output (zoom in $0.025 < t < 0.04$ s, (b) audio input and NGDC output with $C_1 = C_2 = 22$ nF, a 1 second record; (c) its zoom. (Red—square wave input signal, green—output of SK-LPF, blue—output of NGDC.)

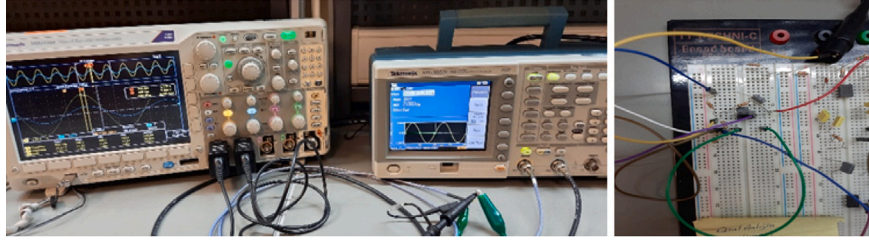


Fig. 9. Measurement setup.

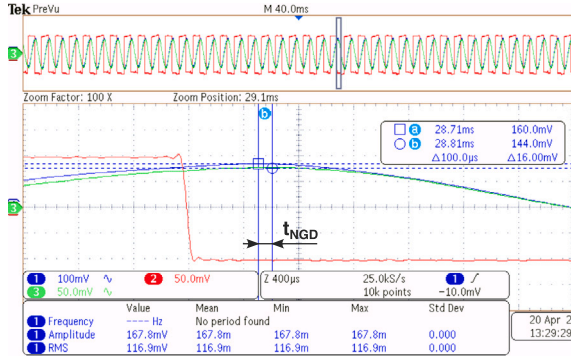


Fig. 10. Measured time domain results of a NGDC with 100 μ s value. (Red—square wave input signal, green—output of SK-LPF, blue—output of NGDC.)

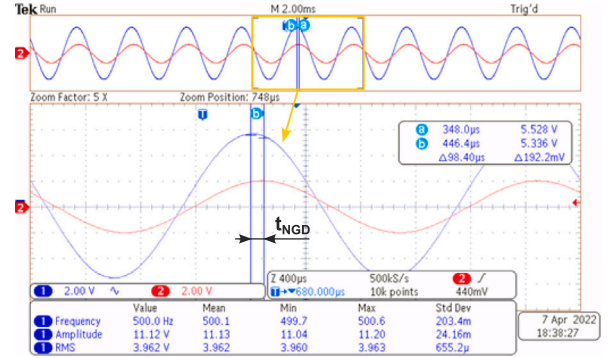


Fig. 11. Measured time domain results of a NGDC with 98.40 μ s value. (Red—input of the NGD circuit, blue—output of the NGD circuit.)

Table 8

Performance comparison with state-of-the-art solutions.

Reference	This study	[16]	[17](a)
NGD	100 μ s	800 ns	100 μ s
Flat Group Delay Range	650 Hz	10 kHz	$\cong 150$ Hz ^a
RMSE (Single Tone)	0.0086	0.0674	0.0013
RMSE (Audio Record)	0.0154	0.0012	0.0053

^a The NGD operation range is 1 kHz [17].

6. Performance comparison

Performance comparison with selected state-of-the-art solutions is given in Table 8. The Root Mean Square Error (RMSE) was calculated as follows:

$$\text{RMSE} = \sqrt{\frac{1}{b-a} \sum_{i=a}^b (x_{in}[i] - x_{out}[i + \tau_{ngd}/T_{sample}])^2}. \quad (30)$$

In Eq. (30), $x_{in}[i]$ and $x_{out}[i + \tau_{ngd}/T_{sample}]$ represent the normalized input to the NGD circuit and the normalized delayed output of the NGD

circuit, respectively. a and b are the starting and ending indices of the sampled signals. The input and output signals of the NGD circuit were sampled with $T_{sample} < |\tau_{ngd}|$ to perform the calculations in MATLAB, where i is the index of the sampled input and output signals. It is noted that the term τ_{ngd}/T_{sample} is selected as an integer and τ_{ngd} is a negative value of the group delay.

7. Conclusion

Nine new single CFOA-based second-order NGD topologies, including a comprehensive design methodology, are presented. Presented circuits perform slightly differently in the time and frequency domain due to the real performance of the CFOA and matching conditions. The simulation results demonstrate that these active NGD circuits are suitable for low-frequency applications, with a group delay of approximately 100 μ s achieved in the frequency range of 1 Hz to 650 Hz. The proposed parameters offer a unique advantage as the NGD value can be achieved without dependency on gain, providing a specific NGD operation range.

CRediT authorship contribution statement

Onat Baloglu: Conceptualization, Software, Validation, Formal analysis, Investigation, Data curation, Writing – original draft, Writing – review & editing, Visualization. **Oguzhan Cicekoglu:** Conceptualization, Methodology, Formal analysis, Writing – original draft, Writing – review & editing, Supervision, Project administration. **Norbert Herencsar:** Conceptualization, Methodology, Resources, Writing – original draft, Writing – review & editing, Visualization, Supervision, Funding acquisition.

Declaration of competing interest

The authors declare that they have no known competing financial interests or personal relationships that could have appeared to influence the work reported in this paper.

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