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# Process Algebra Having Inherent Choice: Revised Semantics for Concurrent Systems<sup>1</sup>

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## Abstract

Process algebras are standard formalisms for compositionally describing systems by the dependencies of their observable synchronous communication. In concurrent systems, parallel composition introduces resolvable nondeterminism, i.e., nondeterminism that will be resolved in later design phases or by the operating system. Sometimes it is also important to express inherent nondeterminism for equal (communication) labels. Here, we give operational and axiomatic semantics to a process algebra having a parallel operator interpreted as concurrent and having a choice operator interpreted as inherent, not only w.r.t. different, but also w.r.t. equal next-step actions. In order to handle the different kinds of nondeterminism, the operational semantics uses  $\mu$ -automata as underlying semantical model. Soundness and completeness of our axiom system w.r.t. the operational semantics is shown.

*Keywords:* nondeterminism, process algebra, axiom system, expansion theorem,  $\mu$ -automata

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## 1 Introduction

Process algebras, see [2] for an overview, are standard formalisms for compositionally describing systems based, e.g., on synchronous communication on an abstract level by the dependencies of their observable communication. They serve as a domain for semantical foundations of programming or modeling languages and are also used as modeling languages [6].

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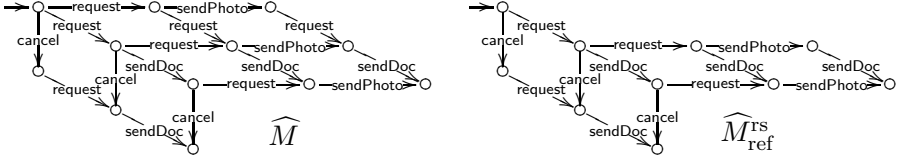


Fig. 1. The transition system  $\widehat{M}$  corresponding to the semantics of (1) and a refinement  $\widehat{M}_{\text{ref}}^{\text{rs}}$  of  $\widehat{M}$  w.r.t. ready simulation

**Example 1.1** Suppose there are two processes running concurrently on a single processor computer that both have the possibility to send print jobs to a printer. Prior to sending the data to the printer, any process must gain exclusive access to the printer by synchronizing on an action **request**. After that, the print job can be sent. In our example, the first process sends a photo (**sendPhoto**), whereas the second sends a document (**sendDoc**). Furthermore, the first process can be disrupted by a user via action **cancel**. This simplified concurrent system (in a parallel environment) can be modeled in process algebra based on synchronous communication by

$$(1) \quad (\text{request.sendPhoto} + \text{cancel}) \parallel (\text{request.sendDoc})$$

where the printer and the user that can disrupt belong to the environment. Here, operator  $\parallel$  denotes parallel composition,  $a.B$  denotes action prefix, and  $+$  denotes the choice operator. The semantics of (1) in terms of transition system is given by  $\widehat{M}$  of Figure 1.

In the transition system of the above example two kinds of choice can be distinguished: (i) External choice represented by outgoing edges with different labels. This choice occurs in implementations and remains undecided until in an execution the environment decides which of the possible actions is performed. (ii) Internal (nondeterministic) choice represented by outgoing edges with the same labels. This nondeterminism is resolved by the scheduler<sup>4</sup> of the operating system, since the two processes run on a single processor computer: The scheduler will decide which process may perform its **request** action, i.e., synchronize to get exclusive access to the printer. This refinement, performed by adding the scheduler, is formally made precise by ready simulation [5], which essentially allows the removal of multiple outgoing edges with the same label, as long as one remains (see Figure 1).

Nondeterminism that will be removed in later design phases or by the operating system is called *resolvable nondeterminism*, whereas nondeterminism that has to remain in implementations and is decided independently for each execution (e.g., by random), is called *inherent nondeterminism*. Consequently, the nondeterminism introduced by abstracting from schedulers in a concurrent setting is resolvable. Note, however, that in standard semantics based on bisimulation the abstraction from schedulers can be regarded as inherent (since transition systems with bisimulation cannot express resolvable nondeterminism). We consider this to be counterintuitive in most cases, because schedulers usually do not show “random” behaviour that is determined independently for each execution.

<sup>4</sup> In a generative rather than reactive setting, different outgoing labels can also express internal choice and thus be resolved by the scheduler. However, this paper only considers reactive systems.

Branching time logics, like the modal  $\mu$ -calculus [17], are often used for describing properties of process algebras. Unfortunately, they are not preserved under refinement based on ready simulation. Consider, e.g., the statement that there is an immediate **request** action enabled such that afterwards no document can be sent to the printer. This property is described by the  $\mu$ -calculus formula

$$(2) \quad \langle \text{request} \rangle ([\text{sendDoc}] \text{false}).$$

Property (2) holds in  $\widehat{M}$  of Figure 1, but not in its refinement  $\widehat{M}_{\text{ref}}^{\text{rs}}$  of Figure 1. This illustrates that branching times properties applied to a concurrent setting need to be interpreted w.r.t. sets of schedulers:  $\langle a \rangle \phi$  requires the existence of a scheduler such that  $\phi$  holds after the execution of  $a$ , and  $[a] \phi$  states that, independent of which scheduler is chosen,  $\phi$  will hold after the execution of  $a$ . Consequently, property (2) has to be understood as follows: There is a scheduler for the next step such that **request** is enabled and its execution (which is deterministic if the scheduler is given) leads to a state where **sendDoc** is for any scheduler disabled. Since the refinement in Figure 1 specializes schedulers,  $\mu$ -calculus formula (2) is not preserved.

In Example 1.1, we illustrated that resolvable nondeterminism naturally arises through parallel composition. In the following examples we argue that also inherent nondeterminism, i.e., internal choice that remains in running implementations, occurs in applications:

**Example 1.2** In the situation of Example 1.1, assume a faulty channel between the first process and the printer. Then it is possible that a signal **request** (e.g., encoded as 1) can be turned into a signal **cancel** (e.g., encoded as 0). This is reflected by the process algebra term

$$(3) \quad (\text{request.sendPhoto} + \text{request} + \text{cancel}) \| (\text{request.sendDoc}).$$

We get inherent nondeterminism w.r.t. the same label **request**, because in case of a faulty transmission it has to be handled as an action **cancel**. The nondeterminism is inherent, because refinement cannot decide on the existence of a fault, this is decided for every execution.

**Example 1.3** In the situation of Example 1.1, consider **request** to be an abstract action [25] for more refined labels like **requestLowRes** and **requestHighRes**, where the first establishes access to the low resolution printing features of the printer, and the second gains access to the high resolution features. Then, based on the printing capabilities offered by the printer, the usual photo can be sent (**sendPhoto**) or a low resolution alternative (**sendLowResPhoto**). This is reflected by the following process algebra term having inherent nondeterminism w.r.t. the same label **request**:

$$(4) \quad (\text{request.sendPhoto} + \text{request.sendLowResPhoto} + \text{cancel}) \| (\text{request.sendDoc}).$$

Note that in these scenarios we have both inherent nondeterminism, introduced by the choice operator of the first process, and resolvable nondeterminism, introduced by the unknown scheduler of the parallel composition. In case there is no resolvable nondeterminism, i.e., if we consider concrete systems only with inherent nondeterminism, transition systems together with bisimulation as underlying equivalence notion is an appropriate semantical model. Here, bisimulation equivalence

is a specialization of ready simulation for concrete systems, i.e., a ready simulation between concrete systems implies bisimilarity. The  $\mu$ -calculus yields a suitable logic, since it characterizes transition systems up to bisimulation.

Nevertheless, transition systems are not an appropriate model whenever inherent and resolvable nondeterminism occur in a single setting, like in Examples 1.2 and 1.3. This is because a choice for an underlying equivalence or preorder has to be made: Bisimulation interprets nondeterminism as inherent, ready simulation as resolvable. Furthermore, when using resolvable nondeterminism, we expect a three-valued satisfaction interpretation over the  $\mu$ -calculus, with the possibility that a formula is neither satisfied nor falsified. For example, we expect that  $\langle \text{request} \rangle ((\langle \text{sendDoc} \rangle \text{true}) \vee (\langle \text{sendLowResPhoto} \rangle \text{true}))$  holds in (4), since all its implementations do, but property (2) is “unknown” for (4), since there are implementations that satisfy the property and there are implementations that do not.

**Contribution.** We give an operational and an axiomatic semantics to a process algebra having a parallel operator interpreted in a concurrent (rather than distributed) setting and a choice operator interpreted as inherent, not only w.r.t. different, but also w.r.t. equal next-step actions. In particular, we adjust the semantics of [3], which has an inherent as well as a resolvable choice operator, to our interpretation of parallel composition, since in their interpretation parallel composition yields inherent nondeterminism. In order to handle the two kinds of nondeterminism adequately, a semantical model with two kinds of transition relations, as in [3] and [14], is used, namely  $\mu$ -automata [16] with their standard refinement notion and their three-valued satisfaction relation over the  $\mu$ -calculus. The two transition relations are defined using structural operational semantics. One relation corresponds to the execution of actions, the other corresponds to the removing of the underspecification for the next action execution, which is called concretization. In order to develop an axiomatic semantics, the process algebra is extended by further operators, especially by a choice operator corresponding to resolvable nondeterminism. From our axiom system, we derive an expansion theorem, which expresses the parallel composition operator in terms of choice operators. Soundness and completeness of this axiom system w.r.t. the operational semantics is shown.

## 2 Syntax

In order not to distract from the technical problems and their solutions, we only present a simple process algebra that does not have recursion, sequential composition, and parallel composition with a synchronization mechanism. Our process algebra consists of action prefix, inherent nondeterminism, parallel composition, and action renaming. Note that a (CSP-based) hiding operator is a special case of a renaming operator, where the action is renamed to the internal action. Here, we follow the philosophy that internal actions are observable. In other words, we do not consider weak equivalence or weak refinement notions. Furthermore, we assume that nondeterminism obtained via “mixed choices” is resolved inside the environment: For example, if the system provides actions  $a$  and  $b$  and the environment their

corresponding counterparts, then the environment decides if  $a$  or  $b$  is executed.<sup>5</sup> The renaming/hiding operator is also of special interest in our setting, since it introduces nondeterminism, too. For example, the process that provides action  $a$  leading to  $B_1$  and that provides action  $b$  leading to  $B_2$  does not contain nondeterminism, but after hiding  $a$  and  $b$ , i.e., renaming  $a$  and  $b$  to the internal action, a nondeterminism occurs, since now an internal step either leads to  $B_1$  or to  $B_2$ . Again, the two interpretations of inherent or resolvable nondeterminism are possible for the renaming operator. But in our setting the nondeterminism obtained through hiding (and therefore implicitly for renaming) should be a resolvable nondeterminism, which is argued as follows: The system has a stimulus for any hidden action. These can be considered to be provided by an additional component continuously providing hidden actions. Then the scheduler of the parallel composition decides, which parallel component executes next. Consequently, hiding (and therefore renaming) yields resolvable nondeterminism with our interpretation of schedulers of parallel components, which assumes that schedulers do not behave randomly.

Before we formally present our process algebra, we introduce some notations. Let  $\mathcal{Act}$  denote the set of all actions, let  $|S|$  denote the cardinality of a set  $S$ , and let  $\mathbb{P}(S)$  denote its power set. Furthermore,  $\circ$  denotes relational composition. For a binary relation  $\rho \subseteq S \times I$  with subsets  $X \subseteq S$  and  $Y \subseteq I$  we write  $X \circ \rho$  for  $\{i \in I \mid \exists x \in X : (x, i) \in \rho\}$  and  $\rho \circ Y$  for  $\{s \in S \mid \exists y \in Y : (s, y) \in \rho\}$ . For a ternary relation  $\rightsquigarrow \subseteq S \times \mathcal{Act} \times I$  we write  $s \xrightarrow{a} i$  for  $(s, a, i) \in \rightsquigarrow$ , and we write  $\xrightarrow{a}$  for the binary relation  $\{(s, i) \mid s \xrightarrow{a} i\}$ , thus  $\{s\} \circ \xrightarrow{a} = \{i \in I \mid s \xrightarrow{a} i\}$ . Furthermore, we write  $s \not\xrightarrow{a}$ , iff  $\{s\} \circ \xrightarrow{a} = \emptyset$ .

PA, the set of all basis process algebra terms, is generated by

$$B ::= 0 \mid a.B \mid B + B \mid B \parallel B \mid B\langle a/b \rangle,$$

where  $a, b \in \mathcal{Act}$ . Process 0 describes a deadlocked process, i.e., no further actions can be executed. We sometimes omit symbol 0 by writing  $a$  instead of  $a.0$ . Process  $a.B$  allows the execution of action  $a$  resulting in the process  $B$ . Inherent choice is described by  $B_1 + B_2$ , and  $B_1 \parallel B_2$  describes parallel composition. The parallel composition has implicit resolvable nondeterminism, introduced by abstraction from a scheduler favoring one of the two sides.  $B\langle a/b \rangle$  describes the process where the execution of  $a$  in  $B$  becomes the execution of  $b$ . All other action execution, including action  $b$ , remains unaffected. Note that this renaming process also introduces resolvable nondeterminism as described in the beginning of this section.

### 3 Operational Semantics

#### 3.1 $\mu$ -automata

As underlying semantical model we use  $\mu$ -automata [16] in the notation of [9], except that we omit fairness constraints and that we do not consider propositions.

<sup>5</sup> Otherwise, the system has to resolve this nondeterminism in which case two communications instead of one have to be modeled: The environment sends its provided actions and the system answers which action it chooses.

**Definition 3.1** [ $\mu$ -automata] A  $\mu$ -automaton  $M$  w.r.t.  $\mathcal{Act}$  is a tuple  $(S, \tilde{S}, s^i, \rightrightarrows, \mapsto)$  such that  $(s \in S)$  is the set of OR-states,  $(\tilde{s} \in \tilde{S})$  the set of BRANCH-states (disjoint from  $S$ ),  $s^i \in S$  its initial element,  $\rightrightarrows \subseteq S \times \tilde{S}$  the OR-transition relation, and  $\mapsto \subseteq \tilde{S} \times \mathcal{Act} \times S$  the BRANCH-transition relation.

The BRANCH-states do not contain underspecification for the next action execution, whereas OR-states can be underspecified in that sense. This underspecification is resolved via the OR-transition relation (for this reason also called *concretization relation*), which is made precise by the standard refinement notion of  $\mu$ -automata:

**Definition 3.2** [ $\mu$ -refinement] A relation  $R \subseteq (S_1 \times S_2) \cup (\tilde{S}_1 \times \tilde{S}_2)$  is a  $\mu$ -refinement between two  $\mu$ -automata  $M_1$  and  $M_2$  if  $(s_1^i, s_2^i) \in R$  and

- $\forall (s_1, s_2) \in R, \tilde{s}_1 \in (\{s_1\} \circ \rightrightarrows) : \exists \tilde{s}_2 \in (\{s_2\} \circ \rightrightarrows) : (\tilde{s}_1, \tilde{s}_2) \in R,$
- $\forall (\tilde{s}_1, \tilde{s}_2) \in R, a \in \mathcal{Act}, s_1 \in (\{\tilde{s}_1\} \circ \mapsto^a) : \exists s_2 \in (\{\tilde{s}_2\} \circ \mapsto^a) : (s_1, s_2) \in R,$  and
- $\forall (\tilde{s}_1, \tilde{s}_2) \in R, a \in \mathcal{Act}, s_2 \in (\{\tilde{s}_2\} \circ \mapsto^a) : \exists s_1 \in (\{\tilde{s}_1\} \circ \mapsto^a) : (s_1, s_2) \in R.$

$M_1$   $\mu$ -refines  $M_2$  if there exists a  $\mu$ -refinement  $R$  between  $M_1$  and  $M_2$ .

Labeled transition systems are straightforwardly embedded into  $\mu$ -automata by using OR-states having exactly one outgoing transition. The restriction of  $\mu$ -refinement onto these systems coincides with bisimulation. As commonly known,  $\mu$ -refinement yields a partial order. Furthermore,  $\mu$ -automata come with a three-valued satisfaction relation over the  $\mu$ -calculus, which is preserved under refinement.

### 3.2 Operational semantics rules

In order to define the operational semantics we use two different kinds of expressions: one where underspecification for the next step is allowed (PA, corresponding to the OR-states) and one where it is not ( $\text{PA}^{con}$ , corresponding to the BRANCH-states), i.e., where the resolvable nondeterminism for the next execution is resolved. Then additionally to the step transition relation ( $\mapsto$ , corresponding to BRANCH-transition relation) from  $\text{PA}^{con}$  to PA, a concretization relation ( $\rightrightarrows$ , corresponding to the OR-transition relation) from PA to  $\text{PA}^{con}$ , which resolves the resolvable nondeterminism for the next execution, is used. Formally,  $\text{PA}^{con}$  denotes the set of all process algebra terms generated by

$$P ::= [0] \mid [a.\hat{B}] \mid P + P \mid P \mid \rangle_{\hat{B}, A, \tilde{B}} P \mid P \langle a/b, v \rangle,$$

where  $a, b \in \mathcal{Act}$ ,  $A \subseteq \mathcal{Act}$ ,  $\hat{B}, \tilde{B} \in \text{PA}$ , and  $v \in \{\mathbf{d}, \mathbf{s}\}$ . The intuition of the operators is similar to the one of the operators given in Section 2, except that here the scheduler of the parallel composition and of the renaming operator is determined for the next step. First, we explain the intuition of parallel composition  $P_1 \mid \rangle_{B_1, A, B_2} P_2$ , at first neglecting  $B_1$  and  $B_2$ , which will be explained later, and using the notation  $P_1 \mid \rangle_A P_2$  instead. Here,  $A$  specifies a scheduler, which is not necessary in standard semantics based on ready simulation, since there schedulers are adequately handled via the ready simulation. However, the scheduler needs to be modeled if

both resolvable nondeterminism and inherent choice should appear in one setting. The scheduler information is interpreted as follows: The right side is favored in  $P_1 \parallel_A P_2$  for actions from  $A$ , whereas the left side is favored for actions from  $\text{Act} \setminus A$ . This favoring concerns only the next step, i.e., after the execution of an action, any scheduling is allowed again. This is even the case if an action in parallel to  $P_1 \parallel_A P_2$  is executed, e.g., if  $P_3$  executes action  $a$  leading to  $B_3$  in  $(P_1 \parallel_A P_2) \parallel_A P_3$  the resulting process is, roughly speaking,  $(P_1 \parallel P_2) \parallel B_3$ , where all next step scheduling is removed. Note that this approach is more appropriate than the approach where the partial scheduler is kept (in which case  $(P_1 \parallel_A P_2) \parallel B_3$  would be the result), since the scheduler is global and therefore can depend on any past execution.<sup>6</sup> Furthermore, associativity of  $\parallel$  would be lost in the alternative approach, which is illustrated later in Example 3.5. In order to model the undoing of the scheduler information efficiently, the parallel composition stores the original processes of its components (here  $B_1$  and  $B_2$ ) and replaces the non-executing component by its stored original one, where no scheduler information is present. This will be clarified by the transition rules.

The scheduler information of the next execution  $v$  is added to the renaming operator: The execution of the action corresponding to the source label  $a$  of the renaming, which becomes  $b$ , is favored in  $B\langle a/b, \mathbf{s} \rangle$ , whereas the execution of the action corresponding to the destination label  $b$  is favored in  $B\langle a/b, \mathbf{d} \rangle$ . Here, favoring means that for  $B\langle a/b, \mathbf{d} \rangle$ , process  $B$  may only execute  $a$  (which will be renamed to  $b$ ) if  $B$  cannot execute  $b$  and analogously for  $B\langle a/b, \mathbf{s} \rangle$ , where  $a$  is favored. Again, this scheduling of the renaming operator only applies for the next action execution, i.e., after the execution of any action, possibly different from  $b$ , the current favoring is removed.

The concretization relation  $\Rightarrow$  is given in the upper section of Table 1, where the underspecification of the next step is resolved, and the step transition relation  $\mapsto$  is presented in the lower section of Table 1, where actions are executed resulting in processes having underspecification for the next step executions. We give some comments on  $\Rightarrow$ : The resolution of next-step-underspecification has to take place in every subpart that can potentially make the next execution, consequently resolution does not take place for  $B$  in  $a.B$ . In the parallel composition, however, the next-step-underspecification is resolved by choosing an arbitrary scheduler. The parallel composition operator stores the original processes such that it can efficiently undo the concretization. In the renaming operator, the next-step-underspecification is resolved by either favoring the action corresponding to the source label ( $\mathbf{s}$ ) or favoring the action corresponding to the destination label ( $\mathbf{d}$ ) of the renaming. We proceed with some comments on  $\mapsto$ : The rules for  $[a.B]$  and  $P_1 + P_2$  are standard. The left side of the parallel composition  $P_1 \parallel_{B_3, A, B_4} P_2$  can execute  $a$  if (i) the left side is favored for  $a$  by the scheduler ( $a \notin A$ ) or (ii) the right side does not provide  $a$  ( $P_2 \not\rightarrow^a$ ). Symmetric constraints hold for the execution of  $a$  on the right side of  $P_1 \parallel_{B_3, A, B_4} P_2$ . As already mentioned before, the next-step-underspecification reso-

<sup>6</sup> The approach where the partial scheduler is kept makes only sense if each parallel component has its own scheduler and there is an additional global scheduler which decides which of the parallel components is favored.



$\overline{0 \Rightarrow [0]}$	$\overline{a.B \Rightarrow [a.B]}$	$\frac{B_1 \Rightarrow P_1 \quad B_2 \Rightarrow P_2}{B_1 + B_2 \Rightarrow P_1 + P_2}$
$\frac{B_1 \Rightarrow P_1 \quad B_2 \Rightarrow P_2 \quad A \subseteq \mathcal{Act}}{B_1 \  B_2 \Rightarrow P_1 \ _{B_1, A, B_2} P_2}$	$\frac{B \Rightarrow P \quad v \in \{\mathbf{d}, \mathbf{s}\}}{B \langle a/b \rangle \Rightarrow P \langle a/b, v \rangle}$	
<hr/>		
$\overline{[a.B] \xrightarrow{a} B}$	$\frac{i \in \{1, 2\} \quad P_i \xrightarrow{a} B}{P_1 + P_2 \xrightarrow{a} B}$	
$\frac{P_1 \xrightarrow{a} B_1 \quad a \in A \Rightarrow P_2 \not\xrightarrow{a}}{P_1 \ _{B_3, A, B_4} P_2 \xrightarrow{a} B_1 \  B_4}$	$\frac{P \xrightarrow{c} B \quad c \notin \{a, b\}}{P \langle a/b, v \rangle \xrightarrow{c} B \langle a/b \rangle}$	
$\frac{P_2 \ _{B_4, \mathcal{Act} \setminus A, B_3} P_1 \xrightarrow{a} B_4 \  B_1}$		
$\frac{P \xrightarrow{b} B \quad v = \mathbf{s} \Rightarrow P \not\xrightarrow{a}}{P \langle a/b, v \rangle \xrightarrow{b} B \langle a/b \rangle}$	$\frac{P \xrightarrow{a} B \quad v = \mathbf{d} \Rightarrow P \not\xrightarrow{b}}{P \langle a/b, v \rangle \xrightarrow{b} B \langle a/b \rangle}$	

Table 1

Upper section: Resolving of the next-step-underspecification via relation  $\Rightarrow \subseteq \text{PA} \times \text{PA}^{\text{con}}$ . Lower section: Action executions via  $\xrightarrow{\quad} \subseteq \text{PA}^{\text{con}} \times \mathcal{Act} \times \text{PA}$ .

lution has to be undone for the parallel component that did not make the execution. Therefore, process  $B_3$ , resp.  $B_4$  replaces the non-executed side. In  $P \langle a/b, v \rangle$  an action  $c$  different from  $a$  and  $b$  can be executed leading to  $B \langle a/b \rangle$ , whenever  $P$  can execute  $c$  leading to  $B$ . This is stated in the first rule. Furthermore,  $P \langle a/b, v \rangle$  can execute  $b$ , leading to a process  $B \langle a/b \rangle$ , where  $B$  can be obtained after executing  $b$  in  $P$ , whenever  $v$  favors the destination label ( $v = \mathbf{d}$ ) or no action  $a$  is provided by  $P$ . Similarly,  $P \langle a/b, v \rangle$  can execute  $a$ , leading to a process  $B \langle a/b \rangle$ , where  $B$  can be obtained after executing  $a$  in  $P$ , whenever  $v$  favors the source label ( $v = \mathbf{s}$ ) or no action  $b$  is provided by  $P$ . In all these three cases, the scheduler is removed after the execution. Note that by definition, the target processes of  $\xrightarrow{\quad}$  do not contain any scheduling information.

**Definition 3.3** [Operational semantics] The operational semantics of a process algebra term  $B \in \text{PA}$  is the  $\mu$ -automaton  $(\text{PA}, \text{PA}^{\text{con}}, B, \Rightarrow, \xrightarrow{\quad})$ , where  $\Rightarrow$  and  $\xrightarrow{\quad}$  are given in Table 1. We say that a process algebra term  $B$  from  $\text{PA}$  *refines* another one  $B'$ , written  $B \leq B'$ , if the operational semantics of  $B$   $\mu$ -refines the operational semantics of  $B'$ . Furthermore,  $B$  is *refinement equivalent* to  $B'$ , written  $B \equiv B'$ , if  $B$  refines  $B'$  and  $B'$  refines  $B$ .

**Example 3.4** The operational semantics for  $(a + a.b) \| a$  is illustrated in Figure 2.

**Example 3.5** We illustrate that associativity of the parallel composition does not hold, if the resolution of underspecification in the parallel composition rule of Table 1 is not undone, i.e., if the original process does not replace the current process in case of non-execution. Under this assumption,  $\tilde{B}_1 = a.b \| (a \| a.c)$  does not refine  $\tilde{B}_2 = (a.b \| a) \| a.c$ : By definition  $\tilde{B}_1 \Rightarrow [a.b] \|_{\mathcal{Act}} ([a] \|_{\mathcal{Act}} [a.c])$ . Since action  $c$  has to be possible afterwards, this process can only be adequately matched by  $\tilde{B}_2$  via a process that is refinement equivalent to  $([a.b] \|_{\{a\}} [a]) \|_{\{a\}} [a.c]$  or to  $([a.b] \|_{\emptyset} [a]) \|_{\{a\}} [a.c]$ .



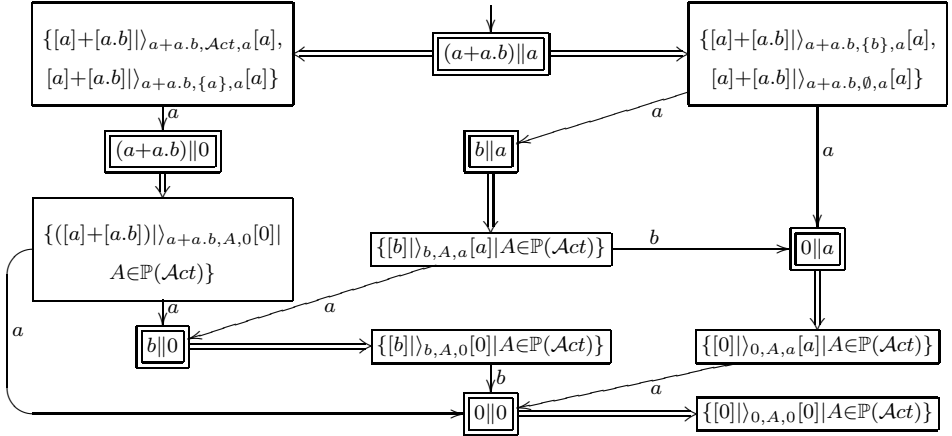


Fig. 2. The operational semantics for  $(a + a.b) \parallel a$ , where  $\text{Act} = \{a, b\}$ . OR-states of the  $\mu$ -automaton have double-lined frames, whereas BRANCH-states have single-lined frames. OR-transitions are drawn as double-line arrows, whereas BRANCH-transitions are drawn as labeled single-line arrows. A state described by a set stands for a set of states, described by the elements of the set and having the same incoming and outgoing transitions as the state labeled with the set.

Thus after the execution of  $a$  we would need that  $a.b \parallel (a \parallel c)$  refines  $([a.b] \parallel_{\{a\}} [a]) \parallel [c]$  or refines  $([a.b] \parallel_{\emptyset} [a]) \parallel [c]$ . Furthermore,  $a.b \parallel (a \parallel c)$  can be concretized such that either  $b$  is possible after the execution of  $a$  or no  $b$  is possible after the execution of  $a$ . But only one of these concretizations is possible in  $([a.b] \parallel_{\{a\}} [a]) \parallel [c]$  and in  $([a.b] \parallel_{\emptyset} [a]) \parallel [c]$ . Hence,  $\tilde{B}_1$  does not refine  $\tilde{B}_2$ .

**Example 3.6** Process  $a.(B_1 \parallel (a.B_2))$  refines  $(a.B_1) \parallel (a.B_2)$ , but not vice versa. This illustrates that refinement over PA does not yield an equivalence relation.

**Theorem 3.7** *Refinement is preserved under all process algebra operators, i.e., if  $B_1 \leq B'_1 \wedge B_2 \leq B'_2$  then  $a.B_1 \leq a.B'_1 \wedge B_1 + B_2 \leq B'_1 + B'_2 \wedge B_1 \parallel B_2 \leq B'_1 \parallel B'_2 \wedge B_1 \langle a/b \rangle \leq B'_1 \langle a/b \rangle$ .*

Note that the straightforward extension of  $\leq$  to  $\text{PA}^{\text{con}}$  is also preserved under all process algebra operators for  $\text{PA}^{\text{con}}$ .

## 4 Axiomatic Semantics

In this section, we present a sound and complete axiom system for the refinement over PA.

### 4.1 Process algebra extension

In order to define the axioms, further terms are introduced: The most interesting one is the resolvable nondeterminism operator  $\oplus$ , which is also of interest for modeling by itself. The intuition of resolvable nondeterminism  $B_1 \oplus B_2$  is that either  $B_1$  or  $B_2$  is implemented, but not both. Thus,  $B_1 + B_2$  is in general not an allowed implementation.

Further added terms are: (i) A next step restriction process  $B \setminus a$ , where  $B$  may not execute action  $a$  as its next step. Note that action  $a$  may be executed

$$\begin{array}{c}
\frac{i \in \{1, 2\} \quad B_i \Rightarrow P}{B_1 \oplus B_2 \Rightarrow P} \qquad \frac{B_2 \Rightarrow P_2}{c.B_1 >_a B_2 \Rightarrow c.B_1 >_a P_2} \\
\\
\frac{B \Rightarrow P}{B \setminus a \Rightarrow P \setminus a} \quad \frac{B_1 \Rightarrow P_1 \quad B_2 \Rightarrow P_2}{B_1 | \rangle_{B_3, A, B_4} B_2 \Rightarrow P_1 | \rangle_{B_3, A, B_4} P_2} \quad \frac{B \Rightarrow P}{B \langle a/b, v \rangle \Rightarrow P \langle a/b, v \rangle} \\
\\
\hline
\frac{P_2 \not\vdash^a}{c.B_1 >_a P_2 \mapsto^c B_1} \qquad \frac{P \mapsto^a B \quad b \neq a}{P \setminus b \mapsto^a B}
\end{array}$$

Table 2  
Additional transition rules for the extended process algebra.

if an action different from  $a$  is executed before. (ii) A conditional prefix term  $c.B_1 >_a B_2$ , which is equivalent to  $c.B_1$  whenever  $B_2$  cannot execute action  $a$  in its next step and it is equivalent to 0 if  $B_2$  can execute action  $a$  in its next step. (iii) A parallel composition  $B_1 | \rangle_{B_3, A, B_4} B_2$  where the scheduler information for the next execution and the replacing processes for non-execution are already given. (iv) A renaming operator  $B \langle a/b, v \rangle$ , where the scheduler information for the next execution is already present. Also some counterparts of these new expressions are added to the process algebra terms where the next-step-underspecification is resolved. Formally, we define  $\widetilde{\text{PA}}$  to be the set of all process algebra terms generated by

$$B ::= 0 \mid a.B \mid B + B \mid B \parallel B \mid B \langle a/b \rangle \mid B \oplus B \mid B \setminus a \mid c.B >_a B \mid B | \rangle_{B, A, B} B \mid B \langle a/b, v \rangle$$

and we define  $\widetilde{\text{PA}}^{con}$  to be the set of all process algebra terms generated by

$$P ::= [0] \mid [a.\widehat{B}] \mid P + P \mid P | \rangle_{\widehat{B}, A, \widetilde{B}} P \mid P \langle a/b, v \rangle \mid P \setminus a \mid c.\widehat{B} >_a P$$

where  $a, b, c \in \mathcal{Act}$ ,  $A \subseteq \mathcal{Act}$ ,  $\widehat{B}, \widetilde{B} \in \widetilde{\text{PA}}$  and  $v \in \{\mathbf{d}, \mathbf{s}\}$ . The previous transition rules (Table 1) are extended by those from Table 2. We give some comments on  $\Rightarrow$ : the resolvable nondeterminism  $\oplus$  is resolved by choosing either the right or the left side and resolving this term. In  $B_1 | \rangle_{B_3, A, B_4} B_2$  only  $B_1$  and  $B_2$  have to be resolved, since no next step analysis takes place in term  $B_3$  and  $B_4$ . By the same argument, only  $B_2$  has to be resolved in  $c.B_1 >_a B_2$ . Now some comments on  $\mapsto$ : In  $c.B_1 >_a P_2$  a  $c$ -step to  $B_1$  is possible iff the right hand side cannot execute  $a$ . In  $P \setminus b$  all executions of  $P$  that differ from  $b$  can take place as next step, in which case  $\setminus b$  is removed, since this restriction only holds for the next step execution.

**Remark 4.1** For terms of PA, after every  $\Rightarrow$ -step the same set of provided actions is obtained. This does not hold for terms of  $\widetilde{\text{PA}}$ , as, e.g., illustrated by  $a \oplus 0$ .

The operational semantics of terms in  $\widetilde{\text{PA}}$  and refinement (equivalence) over  $\widetilde{\text{PA}}$  are defined as in Definition 3.3 except that the extended concretization and step transition relations are used.

## 4.2 Equations

Before we present the axiom system, we discuss some of the (standard) axioms that do not hold in general, i.e., where refinement equivalence cannot be guaranteed,

$\overline{\text{nuf}(0)}$	$\overline{\text{nuf}(a.B_1)}$	$\frac{\text{nuf}(B_1) \quad \text{nuf}(B_2)}{\text{nuf}(B_1 + B_2)}$	$\frac{\text{nuf}(B)}{\text{nuf}(B \setminus a)}$
$\frac{\text{nuf}(B_2)}{\text{nuf}(c.B_1 >_a B_2)}$	$\frac{\text{nuf}(B_1) \quad \text{nuf}(B_2)}{\text{nuf}(B_1  _{B_3, A, B_4} B_2)}$	$\frac{\text{nuf}(B)}{\text{nuf}(B \langle a/b, v \rangle)}$	

Table 3  
Definition of predicate  $\text{nuf}$ .

which is denoted by  $\not\sim$ :

$B + B \not\sim B$ , since the left hand side allows more kinds of resolution of underspecification. For example,  $(a \oplus b) + (a \oplus b)$  can be resolved to  $[a] + [b]$ , which provides  $a$  as well as  $b$ . On the other hand,  $a \oplus b$  cannot be resolved such that both actions are provided. By similar arguments,

$$(5) \quad (B_1 \parallel B_2) + (B_1 \parallel B_3) \not\sim B_1 \parallel (B_2 + B_3),$$

since the underspecification in  $B_1$  can possibly be resolved in two different ways such that it cannot be matched by a single resolution of  $B_1$ . More precisely, after  $\Rightarrow$ , it is possible that the left hand side of (5) can provide more actions than the right hand side. In case  $B_1$  is next-step-underspecification-free, e.g., if  $B_1$  is of form  $\sum_{i \in I} a_i.B_i$  for some  $I$ ,  $a_i$ , and  $B_i$ , we do not have this problem. Predicate  $\text{nuf}$  on PA, which is formally defined in Table 3, collects those **next step underspecification free** processes. Resolution of next-step-underspecification yields a unique term if  $\text{nuf}$  holds:

**Lemma 4.2** *Suppose  $B \in \widetilde{\text{PA}}$  and  $\text{Act} \neq \emptyset$ . If  $\text{nuf}(B)$ , then  $B$  has a unique target w.r.t.  $\Rightarrow$ , i.e.,  $\text{nuf}(B) \Rightarrow |\{B\} \circ \Rightarrow| = 1$ .*

Nevertheless, the processes of (5) are not equivalent even if  $\text{nuf}(B_1)$  holds, since the resolvable nondeterminism of the parallel composition can be resolved in different ways. For example, after applying  $\Rightarrow$  on  $(a.b) \parallel (a.c + 0)$  there is exactly one transition  $t$  labeled with  $a$  and either  $b$  or  $c$  is possible afterwards. On the other hand a  $\Rightarrow$  step from  $((a.b) \parallel (a.c)) + ((a.b) \parallel 0)$  exists where two transitions  $t_1, t_2$  labeled with  $a$  are possible such that  $b$  is possible after  $t_1$  and  $c$  is possible after  $t_2$ . We remedy this problem by resolving the resolvable nondeterminism of the parallel operator before the parallel composition is expanded. This motivates why we introduced the term  $B_1 |_{B_3, A, B_4} B_2$  already in  $\widetilde{\text{PA}}$ .

In Table 4 axioms for the refinement relation  $\preceq$  over  $\widetilde{\text{PA}}$  are presented, where we assume for simplicity that  $\text{Act}$  is finite.

**Theorem 4.3** *The axioms from Table 4 are sound and complete, i.e.,  $\forall B_1, B_2 \in \widetilde{\text{PA}} : B_1 \preceq B_2 \iff B_1 \leq B_2$ .*

**Example 4.4** By using Axiom A32, we get  $a.(b \oplus c) \preceq (a.(b \oplus c)) \oplus (a.b)$ . Furthermore, by using Axiom A32 and then Axiom A3 we get  $(a.(b \oplus c)) \oplus (a.b) \preceq (a.(b \oplus c)) \oplus (a.(b \oplus c)) \simeq a.(b \oplus c)$ . Thus we have shown that  $a.(b \oplus c) \simeq (a.(b \oplus c)) \oplus (a.b)$ . Note that this cannot be shown by using only Axioms A1 – A31, since the necessary removal of  $\oplus$  becomes impossible. This illustrates that Axiom A32 is also essential for the completeness of our Axiom system w.r.t.  $\equiv$ .

- (A1)  $B_1 \oplus B_2 \simeq B_2 \oplus B_1$   
(A2)  $B_1 \oplus (B_2 \oplus B_3) \simeq (B_1 \oplus B_2) \oplus B_3$   
(A3)  $B \oplus B \simeq B$   
(A4)  $B_1 + B_2 \simeq B_2 + B_1$   
(A5)  $B_1 + (B_2 + B_3) \simeq (B_1 + B_2) + B_3$   
(A6)  $a.B + a.B \simeq a.B$   
(A7)  $B + 0 \simeq B$   
(A8)  $B_1 + (B_2 \oplus B_3) \simeq (B_1 + B_2) \oplus (B_1 + B_3)$   
(A9)  $B_1 \| B_2 \simeq \bigoplus_{A \subseteq \mathcal{Act}} (B_1 |_{B_1, A, B_2} B_2)$   
(A10)  $B_1 |_{B_4, A, B_5} B_2 \simeq B_2 |_{B_5, \mathcal{Act} \setminus A, B_4} B_1$   
(A11)  $B_1 |_{B_4, A, B_5} (B_2 \oplus B_3) \simeq (B_1 |_{B_4, A, B_5} B_2) \oplus (B_1 |_{B_4, A, B_5} B_3)$   
(A12)  $B_1 |_{B_4, A, B_5} (B_2 + a.B_3) \simeq ((B_1 \setminus a) |_{B_4, A, B_5} B_2) + a.(B_4 \| B_3) \quad \text{if } a \in A$   
(A13)  $B_1 |_{B_4, A, B_5} (B_2 + a.B_3) \simeq (B_1 |_{B_4, A, B_5} B_2) + (a.(B_4 \| B_3) >_a B_1)$   
if  $a \notin A \wedge \text{nuf}(B_1)$   
(A14)  $0 |_{B_4, A, B_5} 0 \simeq 0$   
(A15)  $B \langle a/b \rangle \simeq B \langle a/b, \mathbf{d} \rangle \oplus B \langle a/b, \mathbf{s} \rangle$   
(A16)  $(B_1 \oplus B_2) \langle a/b, v \rangle \simeq B_1 \langle a/b, v \rangle \oplus B_2 \langle a/b, v \rangle$   
(A17)  $(B_1 + c.B_2) \langle a/b, v \rangle \simeq B_1 \langle a/b, v \rangle + c.(B_2 \langle a/b \rangle) \quad \text{if } c \notin \{a, b\}$   
(A18)  $(B_1 + a.B_2) \langle a/b, \mathbf{d} \rangle \simeq B_1 \langle a/b, \mathbf{d} \rangle + (b.(B_2 \langle a/b \rangle) >_b B_1) \quad \text{if } \text{nuf}(B_1)$   
(A19)  $(B_1 + b.B_2) \langle a/b, \mathbf{d} \rangle \simeq (B_1 \setminus a) \langle a/b, \mathbf{d} \rangle + b.(B_2 \langle a/b \rangle)$   
(A20)  $(B_1 + a.B_2) \langle a/b, \mathbf{s} \rangle \simeq (B_1 \setminus b) \langle a/b, \mathbf{s} \rangle + b.(B_2 \langle a/b \rangle)$   
(A21)  $(B_1 + b.B_2) \langle a/b, \mathbf{s} \rangle \simeq B_1 \langle a/b, \mathbf{s} \rangle + (b.(B_2 \langle a/b \rangle) >_a B_1) \quad \text{if } \text{nuf}(B_1)$   
(A22)  $0 \langle a/b, v \rangle \simeq 0$   
(A23)  $(B_1 \oplus B_2) \setminus a \simeq (B_1 \setminus a) \oplus (B_2 \setminus a)$   
(A24)  $(B_1 + B_2) \setminus a \simeq (B_1 \setminus a) + (B_2 \setminus a)$   
(A25)  $0 \setminus a \simeq 0$   
(A26)  $(a.B) \setminus a \simeq 0$   
(A27)  $(b.B) \setminus a \simeq b.B \quad \text{if } b \neq a$   
(A28)  $c.B_1 >_a (B_2 \oplus B_3) \simeq (c.B_1 >_a B_2) \oplus (c.B_1 >_a B_3)$   
(A29)  $c.B_1 >_a (B_2 + a.B_3) \simeq 0$   
(A30)  $c.B_1 >_a (B_2 + b.B_3) \simeq c.B_1 >_a B_2 \quad \text{if } b \neq a$   
(A31)  $c.B >_a 0 \simeq c.B$   
(A32)  $B_1 \preceq B_1 \oplus B_2$

Table 4

Axioms for  $\preceq$ , where  $\simeq = \preceq \cap \succeq$ . Here,  $\bigoplus_{i \in \{j\}} B_i$  is defined to be  $B_j$  and for  $I$  finite,  $\bigoplus_{i \in I} B_i$  is defined to be  $B_{i'} \oplus (\bigoplus_{i \in I \setminus \{i'\}} B_i)$  where  $i' \in I$  (by the associativity and commutativity of  $\oplus$  the definition is independent of the chosen  $i'$ ). Note that we assume that  $\mathcal{Act}$  is finite. Otherwise, more complex formulas have to be used in order to remain in  $\widetilde{\text{F}\mathbf{A}}$ .

How the parallel composition can be expressed in terms of nondeterminism, known as the expansion theorem [23,4], is one of the most important rules. In our setting the expansion theorem is more complicated than usually, since resolvable as well as inherent nondeterminism has to be handled. It is directly derived from our axiom system and illustrated in the following, where  $\sum_{j=0}^{-1} B_j$  yields 0:

**Theorem 4.5 (Expansion theorem)**

$$B \parallel B' \equiv \bigoplus_{A \subseteq \text{Act}} \bigoplus_{i=0}^n \bigoplus_{i'=0}^{n'} \left( \sum_{j \in J_{(A,i,i')}} a_{i,j} \cdot (B_{i,j} \parallel B') + \sum_{j' \in J'_{(A,i,i')}} a'_{i',j'} \cdot (B \parallel B'_{i',j'}) \right)$$

where  $B = \bigoplus_{i=0}^n \sum_{j=0}^{m_{(i)}-1} a_{i,j} \cdot B_{i,j}$ ,  $B' = \bigoplus_{i=0}^{n'} \sum_{j=0}^{m'_{(i)}-1} a'_{i,j} \cdot B'_{i,j}$ ,

$$J_{(A,i,i')} = \{j < m_{(i)} \mid a_{i,j} \in A \Rightarrow \forall j' < m'_{(i')} : a_{i,j} \neq a'_{i',j'}\},$$

$$J'_{(A,i,i')} = \{j' < m'_{(i')} \mid a'_{i',j'} \notin A \Rightarrow \forall j < m_{(i)} : a_{i,j} \neq a'_{i',j'}\}.$$

Here, (i) the scheduler ( $A \subseteq \text{Act}$ ) is determined by a resolution of resolvable nondeterminism, (ii) any combination of resolutions of both sides is considered, and (iii) the complete inherent nondeterminism of a component for  $a$  is taken if this component is favored by the scheduler or if the current resolution of the other component cannot provide  $a$ .

## 5 Related Work

An overview on algebraic approaches to nondeterminism is given in [27]. Nondeterminism is often interpreted as angelic (chosen positively w.r.t. to a desired property) or demonic (chosen by an adversary). In [22], both nondeterminism interpretations are modeled in a single setting. The angelic/demonic view is orthogonal to our view, leading to the following four interpretations: (i) The task of resolving resolvable, angelic nondeterminism is a *satisfiability* check, i.e., an abstraction has an implementation satisfying the desired property if there is an angelic resolution. (ii) On the other hand, the task of resolving resolvable, demonic nondeterminism is a *satisfaction* check on the abstract level: the property holds if a demonic resolution satisfies it. (iii) Inherent, angelic nondeterminism ensures the existence of a step such that the desired property holds, whereas (iv) inherent, demonic nondeterminism ensures that all possible next steps satisfy the property. In alternating-time temporal logic [1], interpretations (iii) and (iv) are generalized to multiple actors.

In [3], both our choice operators (inherent and resolvable) are used in a process algebra and an operational semantics in terms of  $\mu$ -automata as well as an axiomatic semantics is given. The difference to our work consists in the semantics of the parallel operator, since the semantics of the parallel operator in [3] is based on inherent instead of resolvable scheduling choice, i.e., schedulers behave “randomly” also at the concrete level, which is in most applications, like schedulers in operating systems, not the case. If the scheduler can prefer different parallel components per action, we cannot apply the usual approach to split the parallel composition by using the *left merge* operator, as it is also made in [3]. Therefore our axiom system

becomes more complicated by using additional operators and by using predicate nuf. A further choice operator is presented in [3]. The interpretation of this choice operator  $\oplus'$  is similar to our resolvable choice operator  $\oplus$ , except that the resolving can be moved outwards, e.g.,  $a.(b \oplus' c)$  is equivalent to  $a.b \oplus' a.c$ , which is not the case for  $\oplus$ . The choice operator  $\oplus'$  has the same expressiveness as  $\oplus$  w.r.t. the describable sets of concrete systems, i.e., replacing  $\oplus'$  by  $\oplus$ , or vice versa, does not change the set of concrete systems that refine the corresponding expressions. A difference arises in the refinement relation between different abstract levels, i.e., the refinement relation between non-concrete systems is different.

In [26] a process algebra having our resolvable choice operator together with a choice operator that is only resolvable w.r.t. the same action, which harmonizes with ready simulation, is presented. Thus full inherent nondeterminism is not handled there. The semantics is given as possible worlds, i.e., sets of concrete systems. No parallel operator is considered at all. This process algebra is extended by recursion in [21], but still no parallel composition is considered. Different kinds of choice operators are considered in hybrid systems, see, e.g., [7], but those choice operators concentrate on underspecification (i.e., resolvable nondeterminism) resulting from time aspects.

Other abstract models that can express inherent as well as resolvable nondeterminism are, e.g., (disjunctive) modal transition systems [18,19], hypermixed Kripke structures [11], generalized Kripke modal transition systems [24], modal automata [9] and  $\nu$ -automata [12]. These models are often used as abstract models for transition systems in order to improve verification of full branching time properties, as in [13,10].

In some settings the scheduler can be restricted by further constraints, like priority or fairness assumptions. They can be interpreted as a refinement of an abstract level where no constraint on the scheduler is enforced. This allowance to describe a more detailed scheduler is orthogonal to the problem of handling the interaction of inherent nondeterminism with the resolvable nondeterminism of an underspecified scheduler.

In probabilistic process algebras, see [20] for an overview, choice operators are extended with a distribution determining the way the different sides are favored. Randomized choice operators are special kinds of inherent nondeterminism. Nevertheless, it is important to examine inherent choice operators, which do not contain a distribution, for their own, since sometimes the distribution is not known and sometimes it does not exist at all (cf. Examples 1.2 and 1.3). Note that approaches trying to embed pure inherent nondeterminism into probabilistic settings lead to unnecessarily complex models and thus unnecessarily increase the cost of verification.

## 6 Conclusion

We have presented the structural operational semantics of a process algebra that handles choice operators corresponding to inherent nondeterminism as well as re-

solvable nondeterminism obtained by abstraction from the scheduling of the parallel composition or the renaming/hiding operator. In particular,  $\mu$ -automata, which have two kinds of transition relations (one for action execution and one for resolution of resolvable nondeterminism), are used as underlying model for the structural operational semantics. In order to avoid any restriction on schedulers, the resolution of the resolvable nondeterminism has to be made undone if it was not affected by the previous execution step. A sound and complete axiom system has been presented, where in particular a choice operator representing resolvable nondeterminism is used. Note that the increased complexity of our semantics, compared to the standard semantics, is unavoidable if  $\mu$ -calculus formulas should be preserved under refinement.

Our operational semantics can be straightforwardly adapted to process algebra having parallel composition with a CSP-based synchronization [15], sequential composition, or recursion. The only problem arises for unguarded recursion, i.e., if there exists a variable that is bound without being behind an action prefix: There the definition of the concretization relation  $\Rightarrow$  yields problems, since infinite derivation trees can be generated. This is not very critical, because usually process algebras without unguarded recursion are sufficient for applications.

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