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[](http://crossmark.crossref.org/dialog/?doi=10.1016/j.jestch.2023.101454&domain=pdf)Hamiltonian path, routing, broadcasting algorithms for connected square network graphs

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a b s t r a c t

Connected Square Network Graphs (*CSNG*) in the study of Selcuk (2022) and Selcuk and Tankul (2022) is reconsidered in this paper. Although (*CSNG*) is a 2-dimensional mesh structure, the most important fea- ture of this graph is that it is a hypercube variant. For this reason, this study focuses on development algo- rithms that find solutions to various problems for (*CSNG*) with the help of hypercube. Firstly, an efficient algorithm that finds the Hamiltonian path is given. Further, two different algorithms that perform the mapping of labels in graph and the unicast routing are given. Furthermore, the parallel process for map- ping and unicast routing is discussed. Finally, guidelines are given for broadcasting algorithms.

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1. Introduction

Graphs appear in computer science both as a mathematical model and as a data type. Hypercube graphs and their variants are frequently used in computer architecture. Hayes et al. used hypercube architecture to implement supercomputers [[1]](#_bookmark18), Niemi- nen et al. described pseudocubes using hypercubes, while pse- docubes have similar properties with hypercubes [[2]](#_bookmark19), Chae et al. studied on general cubic graphs [[3]](#_bookmark20), Harary et al. presented com- prehensive analysis of hypercube graphs [[4]](#_bookmark21) and Mao and Nicol studied on graphs named *k*-ary *n*-cubes [[5]](#_bookmark22), proposed a technique to define and analyze a variation of this topology. Many research- ers have studied hypercube and its variants and obtained their topological properties. For example Folded Hypercube is studied by El-Amawy and Latifi [[6]](#_bookmark23) and properties of hypercube is investi- gated, Cahng and Chen [[7]](#_bookmark24) proposed the incrementally extensible folded hypercube as a new class and explained its properties while giving a simple routing algorithm. The topological structure of Crossed Cubes, which is another hypercube variant, is studied by Dong et al. [[8]](#_bookmark25) and a 3D mesh family is embedded in it, Efe [[9]](#_bookmark25) gave an alternative way for parallel architectures using Crossed Cubes, and Lai et al. proposed a new dynamic programming algorithm to apply multidimensional torus in it [[10]](#_bookmark25). Besides, there is also a lot of work on hierarchical networks. Abd-El-Barr and Al-Somani

[[11]](#_bookmark25) reviewed and compared different type of hierarchical inter-

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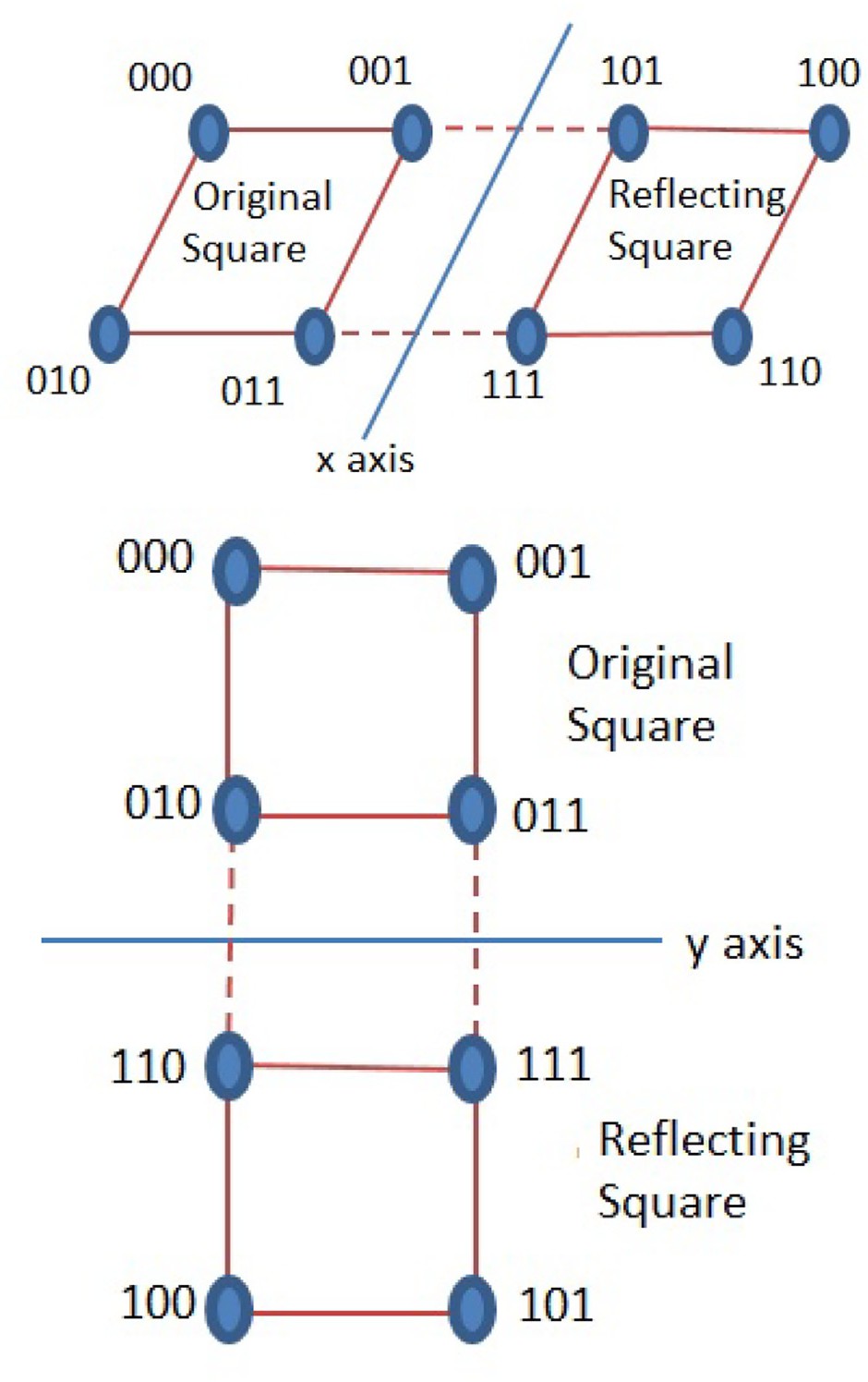
connection networks and their topological attributes, on the other hand Ghose and Desai [[12]](#_bookmark25) introduce the hierarchical cubic net- work (HCN), a novel interconnection system for massive dis- tributed memory multiprocessors. Hierarchical extended Fibonacci cubes (HEFC) is a new interconnection network structure proposed by Karci [[13]](#_bookmark25) and its properties are explained. Karci also used Fibonacci series to suggest new graphs which are hierarchi- cally definable and sub-graphs of Hierarchic Cubic graphs [[14]](#_bookmark25). Karci and Selcuk introduced new hypercube variants. These; Frac- tal Cubic Network Graph (FCNG) [[15]](#_bookmark25) using the fractal structures and Connected Cubic Network Graph (CCNG) [[16]](#_bookmark25) using hypercube shown in Fig 1.2, Fig 1.3. and Fig 1.4., and explained their proper- ties while giving algorithms for these graphs. Connected Square Network Graphs introduced by Selcuk can be obtained using two different definitions, and this variant of hypercube is a Hamiltonian graph [[17]](#_bookmark25). A new Hypercube-like Graph is introduced by Selcuk and Tankul which is another type of hypercube and is labeled with using gray code and the connectivity indexes are calculated [[18]](#_bookmark25).

Motivated by [[15,16]](#_bookmark25), this paper proposes new algorithms for *CSNG*. Section [2](#_bookmark0) gives information about *CSNG*, divide and conquer method, dynamic programming, the travelling salesman problem, the routing problem and the broadcasting problem. Section [3](#_bookmark7) intro- duces a new Hamiltonian path algorithm employing dynamic pro- gramming techniques. Additionally, algorithms are provided for mapping and routing, along with a discussion on the parallel pro- cess for these algorithms in Section [4](#_bookmark9). Finally, broadcasting algo- rithms are given for *CSNG* with the help of broadcasting algorithms for hypercube.

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1. Preliminaries

For the rest of the study, let *u* and *v* be vertices in a graph *G*. *G* is considered to be *CSNG* with vertex set *V* (*G*) and edge set *E*(*G*); *G* = (*V* ; *E*). ”||” indicates the concatenation of two strings.

The Hamming distance is P*n*—1(*ai* ⊕ *bi*) since ⊕ is bit-wise XOR

operation. *S* 2

*i*=0

*D* space and the 2*D* coordi-

is denoted a square in 2 nate system is given in [Fig. 1](#_bookmark6).

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Definition 1. *CSNG*: Let *CSNG* 0; 0 *S* 2 . *CSNG k*; *m* can be defined in two steps.

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*Case I. Construction in one direction*

1. Suppose P*m* 2*i* squares with common two nodes (an edge)

*i*=0

are connected along the *y*-axis. This graph will be called a *CSNG* 0; *m* . For example, the mesh structures given in [Fig. 2](#_bookmark3) (a) and [Fig. 3](#_bookmark4) are *CSNG* 0; 1 and *CSNG* 0; 2 , respectively.

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1. Suppose P*k*

*i*=0

2*i* squares with common two nodes (an edge)

are connected along the *x*-axis. This graph will be called a

*CSNG*(*k*; 0). For example, the mesh structure given in [Fig. 2](#_bookmark3)-

(b) is *CSNG*(1; 0).

*Case II. Construction in two directions*

1. Suppose P*m* 2*i CSNG k* 0 s with a common edge (right and

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*i*=0

left edges) are connected along the *y*-axis. This graph will be called a *CSNG*(*k*; *m*).

1. Suppose P*k*

*i*=0

2*i CSNG*(0; *m*)s with a common edge (lower

and upper edges) are connected along the *x*-axis. This graph will be called a *CSNG*(*k*; *m*). For example, the mesh structure given in [Fig. 4](#_bookmark5) is *CSNG*(1; 2).

* 1. *Divide and Conquer*

There are different ways of designing algorithms. One approach is the divide-and-conquer algorithm, which, as the name suggests, divides the problem into sub-problems to make it easier to solve the original problem. It solves sub-problems that are like a smaller version of the original problem and combines the solutions of the sub-problems to form the solution of the original problem [[19]](#_bookmark25). The divide and conquer paradigm increases program modularity, often leading to simple and efficient algorithms. It has therefore proven to be a powerful tool for sequential algorithm designers. The divide and conquer algorithms exhibit recursive structure, where the sub-problems are typically smaller in size compared to the original problem. Consequently, the original problem calls itself recursively to solve these sub-problems. At each recursive level, the divide and conquer method involves three steps.

Divide the problem into similar sub-problems,

●

Conquer these sub-problems with using recursive method to solve,

●

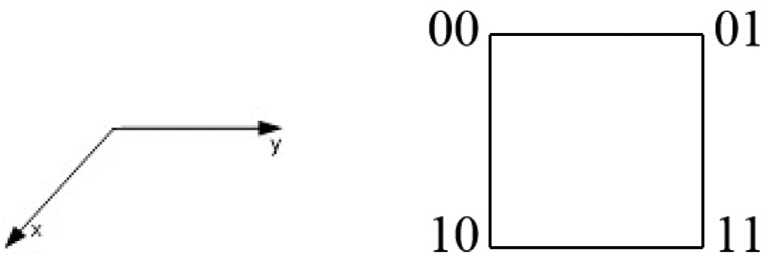


Fig. 1. a. 2*D* coordinate space, b. *S*(2), respectively.

Fig. 2. a. *CCNG*(0; 1), b. *CCNG*(1; 0), respectively.

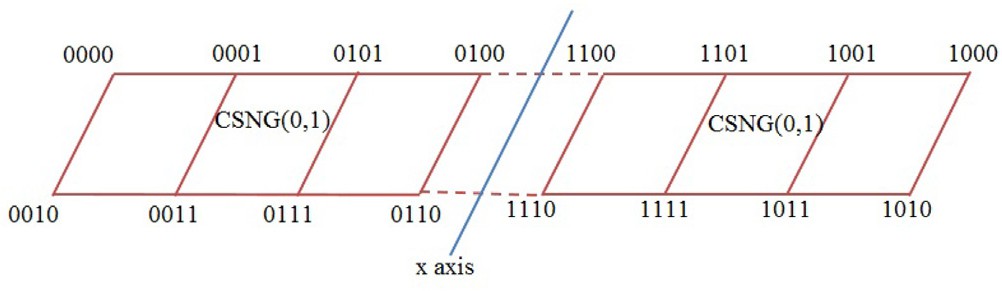


Fig. 3. *CCNG*(0; 2): Case 1-(i) of Def. 1.

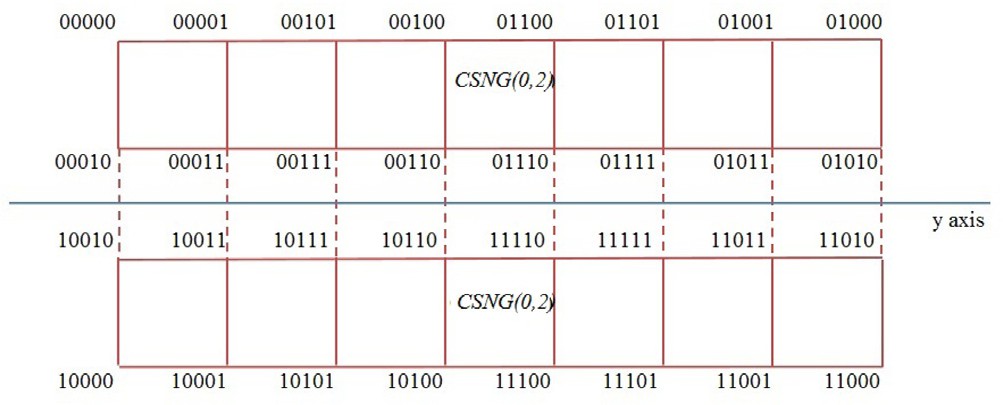


Fig. 4. *CCNG*(1; 2): Case 2-(ii) of Def. 1.

Combine the solutions by solving the sub-problems into the solution that will be the original problem solution.

●

Parallel divide and conquer algorithm design plays an even more important role. Since the sub-problems created in the first step

are usually independent, they can be solved in parallel. Usually the sub-problems are solved recursively, resulting in more sub- problems to be solved in parallel in the next dividing step. However, it should be noted that to obtain a highly parallel algorithm, the division and merge steps of divide and conquer must be paral- lelized. It is quite common in parallel algorithms to solve the orig- inal problem in parallel by dividing it into as many sub-problems as possible. For the Parallel Divide and Conquer example, consider the sequential Merge Sort algorithm. Merge sort takes *n* elements and returns them in order. This algorithm works by dividing an array of *n* elements into two parts, each dividing array recursively sorted, and combined the sorted sub-arrays.

Divide and conquer algorithm is implemented on various graph structures. On hypercube interconnection graph, a divide and con- quer method to solve tridiagonal systems is implemented by [[20,21]](#_bookmark26) gives a generic divide and conquer implementation of the algorithms on hypercube like routing. On the other hand, in [[22]](#_bookmark27) divide and conquer mapping is proposed for efficient execution of parallel computers with hypercube structure. In [[23]](#_bookmark33), a binomial tree is embedded onto 3*D* mesh and torus interconnection network, and divide and conquer routing algorithm is proposed. A simple and optimal approach for mapping in 2*D* meshes is given in [[24]](#_bookmark35) and a partially adaptive and deterministic routing algorithm for 2*D* mesh networks on chip is given in [[25]](#_bookmark36). Other mapping algorithm HyPar is proposed in [[26]](#_bookmark38) for hybrid GPU-CPU networks.

* 1. *Dynamic Programming*

Dynamic programming is generally applied to most of the opti- mization problems where a series of choices must be made to reach an optimal solution. Same sub-problems often arise while making choices. Dynamic programming is an algorithm design methodology much the same as divide and conquer, splitting prob- lem into similar sub-problems and solving them recursively ([[19]](#_bookmark25)). Dynamic programming is used when the partitioned sub-problems of the original problem are repeated and not independent. Each sub-problem is solved just once by the algorithm and then the solution of the problem is stored in a table. By storing, it reduces the cost of code by eliminating the need to recalculate the same operations. The structure of dynamic programming is also recur- sive, a method calls itself for sub-problems of the main problem, and solves it if the sub-problem is encountered for the first time. Dynamic programming has four steps in development process [[19]](#_bookmark25);

Characterization of the optimal solution structure, Iterative definition of the value for an optimal solution,

●

●

Calculation of the value for an optimal solution from the bottom up,

●

* + - Building an optimal solution from the calculated information.

The first three steps are the basic steps of a dynamic programming. If the goal is solely to obtain the value of the optimal solution, it is sufficient to use only the first three steps and last step can be skipped.

A dynamic programming approach is used for layout optimiza- tion problem on interconnection networks [[27]](#_bookmark39), link scheduling

[[27]](#_bookmark39) and routing [[28]](#_bookmark40) on mesh network, simulation of multi dimen- sional torus in crossed cube [[10]](#_bookmark25), optimal processor mapping for linear-complement communication [[29]](#_bookmark43), matrix chain product

[[30]](#_bookmark44) and knapsack problem [[31]](#_bookmark45) on hypercube.

* 1. *Travelling Salesman Problem (Hamiltonian path)*

The Traveling Salesman Problem is one of the NP-hard opti- mization problem that means it has a large search space for the

solution, so running time is not polynomial [[32]](#_bookmark47). In the Traveling Salesman Problem (TSP), the goal is to find the shortest tour a seller takes starting from the city he is in and returning to the city where he started after stopping by each city only once. It is assumed that there is a road between any two cities and that the length of that road is known. It is easy to understand but difficult to solve. TSP, in the language of graph theory, is to find the shortest Hamiltonian cycle on a (plain) graph where cities are represented by the vertex and roads by the edges between the vertices.

The Hamiltonian path is named after 19th century mathemati- cian William Hamiltonian which passes through each node on a graph only once. If a Hamiltonian path ends where it started, com- pleting a full cycle (if it is possible to go from the last node on the path to the first node), these paths is called the Hamiltonian cycle. A graph containing a Hamiltonian cycle also contains a Hamilto- nian path, but the reverse is not always true. The graphs that have a Hamiltonian cycle are called Hamiltonian graphs.

TSP is studied on interconnection topologies. Ant colony opti- mization algorithm applied on ring and hypercube in parallel for TSP [[33]](#_bookmark28), chemical reaction optimization [[34]](#_bookmark28) and grey wolf algo- rithm [[35]](#_bookmark28) is implemented for TSP over hypercube. [[36]](#_bookmark28) shows NISQ circuit compilation problem is same as TSP on torus network. TSP is also studied for mesh networks for channel allocation scheme for reducing interference [[37]](#_bookmark28).

* 1. *Routing*

Routing is the process of finding the shortest and potential paths between nodes in a network topology. There are two main types of routing problems, depending on their purpose and com- plexity [[38]](#_bookmark28). The shortest path is the main focus of the path finding problems. Compared to other routing problems, these problems are relatively simple. Tour construction problems are the second group. It is more complex than the first category problems of build- ing an entire tour within a given network. For instance, real distance and geographical information system (GIS) are used to solve real world problems represented as a network.

Path Finding Problems primarily encompass Shortest Path Prob- lems (SPP), which involve determining the minimum total cost (time, distance, expense) between two nodes. Single Source Short- est Path Problem is for finding a path between source and destina- tion vertices. There are various algorithms for these problems. All Pair Shortest Path Problem to find paths between all vertices pairs in the network. Tour construction problems can be divided into three classes: Travel Salesman Problem (TSP) is the first one and the most well known, Vehicle Routing Problem (VRP) is the gener- alized version of TSP with many salesmans, and lastly Bus Routing Problem (BRP) is another version of TSP with route balancing, may have time window constraints and busses can have same or differ- ent capacities.

SPP is implemented on many different types of interconnection networks. In [[39]](#_bookmark28) shortest path routing is applied for all to all com- munication on hypercube. In [[40]](#_bookmark28) average shortest distance is found with new model for irregular ring and hub network. One to one shortest path problem is studied for on-chip board large scale mesh network in [[41]](#_bookmark28), while one to all shortest path problem is studied for torus network [[42]](#_bookmark29).

* 1. *Broadcasting*

In computer networks, casting refers to sending data over the network used for communication between hosts. Types of casting used in networking are as follows [[43]](#_bookmark30):

Unicast transmission is one-to-one transmission used to send data from one sender to one receiver.

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Broadcast transmission is one-to-all transmission that sends data from one or more hosts to all hosts in the network.

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Multicast transmission is one-to-many transmission where there is a single source that sends data to the specific group in the network.

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The broadcasting process occurs with sequential data transmissions between pairs of nodes. Broadcast communication is often required in image processing, parallel implementation of algorithms or sci- entific computations, spread large data arrays among system nodes and to perform various data processing activities. There are two way for broadcasting the message over the network [[44]](#_bookmark31). First one is one-to-all broadcasting, transmission of the identical mes- sage from originating source to all nodes over the network. The *m*-size data that needs to be published is initially only available in the source node. At the end of the procedure there are n (number of nodes in the network) copies of the initial data, one at each node. All-to-all is the other type of broadcasting also called total exchange or gossip. In this broadcasting, each node sends a message to every other node on the network. All nodes start a broadcast simultane- ously in an all-to-all broadcast, which is a generalization of the one-to-all broadcast. A node sends the same message to all nodes, however different nodes may broadcast different messages. Broad- casting algorithms are existed for sparse networks like torus and grid networks [[45]](#_bookmark32), mesh networks [[46]](#_bookmark34), star networks [[47]](#_bookmark37), and variant of hypercube networks [[15,47–49]](#_bookmark25). The most known and used broadcast algorithms are Recursive Doubling, Network Parti- tioning and Extending Dominating Node algorithms [[50]](#_bookmark41).

1. Hamiltonian Path Algorithm

This section introduces a new Hamiltonian path algorithm for *CSNG*. The dynamic programming method is used in recursive structures like the divide and conquer method. Since it calculates the sub-problems once and stores this value in the table, it can offer better performance than the divide and conquer method. Thus, the dynamic programming method will be used in the fol- lowing [Algorithm 1](#_bookmark8). One bit change between labels of neighboring nodes is provided with the help of gray code in this algorithm.

Example 3.1. Firstly, it is assumed to be a Hamiltonian path for a square in 2D space;

00 → 01 → 11 → 10.

Assume that initial node is 01001 on *CSNG* 3 0 (or

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The middle node is 1 on 010, and similarly the 2nd iteration can be obtained as follows.

# *H*(2)=

=1001 → 1011 → 1010 → 1000 → 1100 → 1110

→ 1111 → 1101 → 0101 → 0111 → 0110 → 0100

→ 0000 → 0010 → 0011 → 0001,

and

# *I*(2)= 0||*H*(1)→ 1||*I*(1)

= 0001 → 0011 → 0010 → 0000 →

0100 → 0110 → 0111 → 0101 → 1101

→ 1111 → 1110 → 1100 →

1000 → 1010 → 1011 → 1001.

Start from node 0 on 010, and then the result of the last iteration;

*H*(3) =0||*H*(2) → 1||*I*(2) = 01001 → 01011 → 01010 → 01000 → 01100 → 01110

→ 01111 → 01101 → 00101 → 00111 → 00110 → 00100 →

00000 → 00010 → 00011 → 00001 → 10001 → 10011 → 10010

→ 10000 → 10100 → 10110 → 10111 → 10101 → 11101 → 11111

→ 11110 → 11100 → 11000 → 11010 → 11011 → 11001.

Remark 3.1. The [Algorithm 1](#_bookmark8) finds Hamiltonian path for *CSNG k*, *m* using dynamic programming. If this algorithm were designed with divide and conquer approach, two nested recursive structures would have to be encountered. By using dynamic

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programming instead of divide and conquer method, Hamilton path can be found with a simpler strategy. Step 1 does not include loop, recursion or other functions, only includes constant number of operations so its time complexity is O 1 . For step 2, because it includes loop and the loop variable *j* starts from 1 and increments by one at each iteration until *k m* and includes ” ” operations. This operation essentially contains a hidden for loop. For each *j*, the sum of the sizes of the arrays to be used in the ”||” operation will

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+ ||

give the complexity of the [Algorithm 1](#_bookmark8). For *j* = 1, size of array: 22,

for *j* = 2, size of array: 23,.. .,for *j* = *k* + *m*, size of array: 2*k*+*m*+1. Total running numbers; 22 23 ... 2*k*+*m*+1 2*k*+*m*+2 4 using

+ + + = —

geometric series expansion. Thus, the complexity of the Algo- rithm 1 is O(2*k*+*m*+2).

*CSNG*(*k*, *m*) has 2*k*+*m*+2 nodes. *CSNG*(0, 0) is equivalent to the square and has 22 nodes. Although *C*(2, 0), *C*(0, 2) and *C*(1, 1) have

,

*CSNG* 2, 1 , *CSNG* 1, 2 , C*SNG* 0, 3 ). In fact, 010 for the outer recur- sive structure and 01 for the inner recursive structure are chosen as the initial node. To obtain the Hamiltonian path for the inner recursive structure, the Hamiltonian path considered above must be shifted so that the starting node is 01 (See step 1 in [Algorithm 1](#_bookmark8)). A Hamiltonian path for the square will be named *H*(0) and *I*(0) (reverse sorted of *H*(0)) are obtained as follows:

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# *H*(0)= 01 → 11 → 10 → 00, *I*(0)= 00 → 10 → 11 → 01.

For the outer recursive structure (See step 2 in [Algorithm 1](#_bookmark8)), the last node is 0 on 010, and then the result of the first iteration;

# *H*(1)= 0||*H*(0)→ 1||*I*(0)

= 001 → 011 → 010 → 000 → 100 → 110 → 111 → 101,

and

# *I*(1)= 1||*H*(0)→ 0||*I*(0)

= 101 → 111 → 110 → 100 → 000 → 010 → 011 → 001.

the same number of nodes, they are not isomorphic graphs. How- ever, a Hamiltonian path for these graphs are not isomorphic due to the construction of the *CSNG k*, *m* graph can be obtained in a similar way using the Algorithm 1.

In *S* 2 , there are 8 Hamilton paths according to the starting address and node order, and there are 2 different Hamilton paths according to node order. These paths are specified as follows;

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00 → 01 → 11 → 10

00 → 10 → 11 → 01

01 → 11 → 10 → 00

01 → 00 → 10 → 11

11 → 10 → 00 → 01

11 → 01 → 00 → 10

10 → 00 → 01 → 11

10 → 11 → 01 → 00

A general formula for these paths is given below using step 1 in [Algorithm 1](#_bookmark8);

Case 1(*H*(0)) : *N* → *N* ⊕ 20 → *N* ⊕ 20 ⊕ 21 → *N* ⊕ 21

: *N* ⊕ 00 → *N* ⊕ 01 → *N* ⊕ 11 → *N* ⊕ 10 Case 2(*H*(0)) : *N* → *N* ⊕ 21 → *N* ⊕ 21 ⊕ 20 → *N* ⊕ 20

: *N* ⊕ 00 → *N* ⊕ 10 → *N* ⊕ 11 → *N* ⊕ 01

where *N* initialnode : 00, 01, 11, 10. In order to derive new graphs by analogy with the hypercube strategy, a reverse order of the found paths is also needed. For this, the reverse paths are indicated as follows in step 1 in [Algorithm 1](#_bookmark8);

( )

Case 1 (*I*(0)) : *N* ⊕ 21 → *N* ⊕ 21 ⊕ 20 → *N* ⊕ 20 → *N*

: *N* ⊕ 10 → *N* ⊕ 11 → *N* ⊕ 01 → *N* ⊕ 00 Case 2 (*I*(0)) : *N* ⊕ 20 → *N* ⊕ 20 ⊕ 21 → *N* ⊕ 21 → *N*

: *N* ⊕ 01 → *N* ⊕ 11 → *N* ⊕ 10 → *N* ⊕ 00

In step 2 of the [Algorithm 1](#_bookmark8), the path obtained in the previous sub- problem and the inverse of this path are combined using [Definition](#_bookmark2) [1](#_bookmark2).

*CSNG k*, *m* network structure is similar to 2D mesh network

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structure. In the literature, algorithms for Hamiltonian path in 2D mesh networks (*M*(*p*, *r*)) [[51–53]](#_bookmark42) with the size of *pr* have generally

the time complexity O(*pr*). Here, *p* = 2*k*+1 and *r* = 2*m*+1. Thus, it is obtained that the proposed algorithm in *CSNG*(*k*, *m*) and the Hamil- tonian algorithms for *M*(*p*, *r*) have the same complexity.

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Algorithm 1: This algorithm calculates an alternative Hamiltonian path with *CSNG k*, *m* using dynamic programming process.

* 1. *Unicast Routing Algorithms*

The nodes on the networks decide which will receive the mes- sage sent by various methods while communicating with other nodes. Some messages are directly sent to a node, some are intended to reach a specific set of nodes, and some reach all nodes in the same network.

If the message sent to the network targets directly a single node which address is known, the message is sent using an algorithm designed as unicast. Thus, the network elements used in between perform the task of transmitting the message only to the specified node. Broadcast algorithms deliver the message to all nodes while multicast algorithms are used for communication within the group.

Unicast routing algorithm for *CSNG k*, *m* has a different strat-

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egy from e-cube routing algorithm for the hypercube. The classical method determines firstly how many bits are changed from source

(*S*) to destination (*D*). In this study, a different strategy consisting of the following steps will be followed;

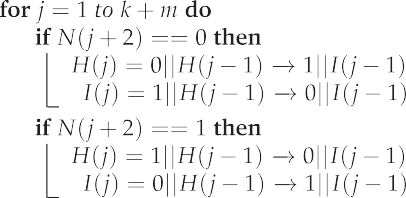
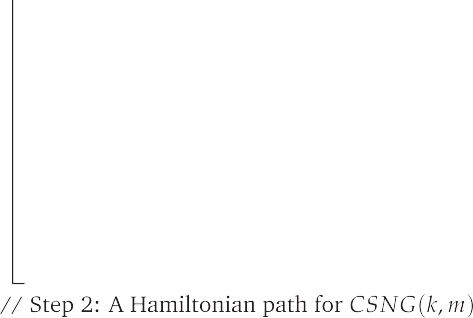
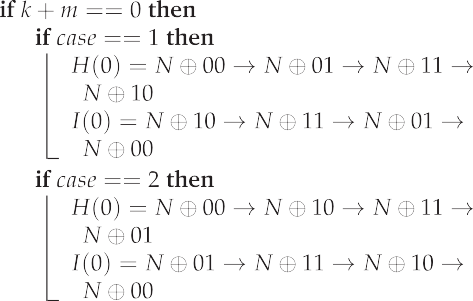
*Step 1.* The labels of all the squares that construct *CSNG k*, *m* need to be calculated. The new labeling template for this graph is obtained by transforming [Fig. 5](#_bookmark10) into [Fig. 6](#_bookmark13) using [Algorithm 2](#_bookmark11) (mapping algorithm),

* ( )

*Step 2.* The unicast routing path between source *S* and destina- tion *D* nodes is obtained by traversing first rows (columns) and then columns (rows) from the corresponding squares with the help of the new labels of these nodes using [Algorithm 3](#_bookmark12) (path-finding algorithm).

●

Example 4.1. Finding the map for the graph *CSNG* 2, 2 is a recur- sive process. There are 4 different base cases for [Algorithm 2](#_bookmark11) according to the values of *i* and *j*. These cases are 0, 0 , 1, 0 , 0, 1 and 1, 1 for values of *i*, *j* that determines base case return map *M* and takes



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[0001; 1011], [0100; 1110], [1011; 0001], [1110; 0100] values respec-

tively for *CSNG k*, *m* graphs, so for the graph *CSNG* 2, 2 when *k* and *m* values are equal to 2. *i* is used for calculating the mirror inverse of the graph with respect to the *y*-axis, while *j* is used for calculating the mirror inverse of the graph with respect to the *x*- axis. Then, each time the function is called recursively, the size of the map is doubled. First, the map size is increased horizontally

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according to *m* values by merging *CSNG* 0, *m* 1 and reversed

( — )

order of the elements in each row of *CSNG* 0, *m* 1 after adding

( — )

0 and 1 respectively at the beginning of each label. Then, the map size is increased vertically according to *k* values by merging *CSNG*(*k* — 1, *m*) and reversed order of the elements in each column

of *CSNG k* 1, *m* after adding 0 and 1 respectively at the beginning

( — )

of each label. At the first call *i* and *j* must be 0. Step by step, the result of *Map* 2, 2, 0, 0 :

( )

M=[00 01;10 11]

→

M=[000 001 101 100;010 011 111 110]

→

M=[0000 0001 0101 0100 1100 1101 1001 1000; 0010 0011

0111 0110 1110 1111 1011 1010]

→

M=[00000 00001 00101 00100 01100 01101 01001 01000;

00010 00011 00111 00110 01110 01111 01011 01010; 10010

10011 10111 10110 11110 11111 11011 11010; 10000 10001

10101 10100 11100 11101 11001 11000]

→

M=[000000 000001 000101 000100 001100 001101 001001

1. Communication Algorithms

This section, introduces new unicast routing algorithms for *CSNG* and provides a recursive algorithm for mapping node labels of *CSNG*, as well as an iterative algorithm for finding path.

001000; 000010 000011 000111 000110 001110 001111 001011

001010; 010010 010011 010111 010110 011110 011111 011011

011010; 010000 010001 010101 010100 011100 011101 011001

011000; 100000 100001 100101 100100 101100 101101 101001

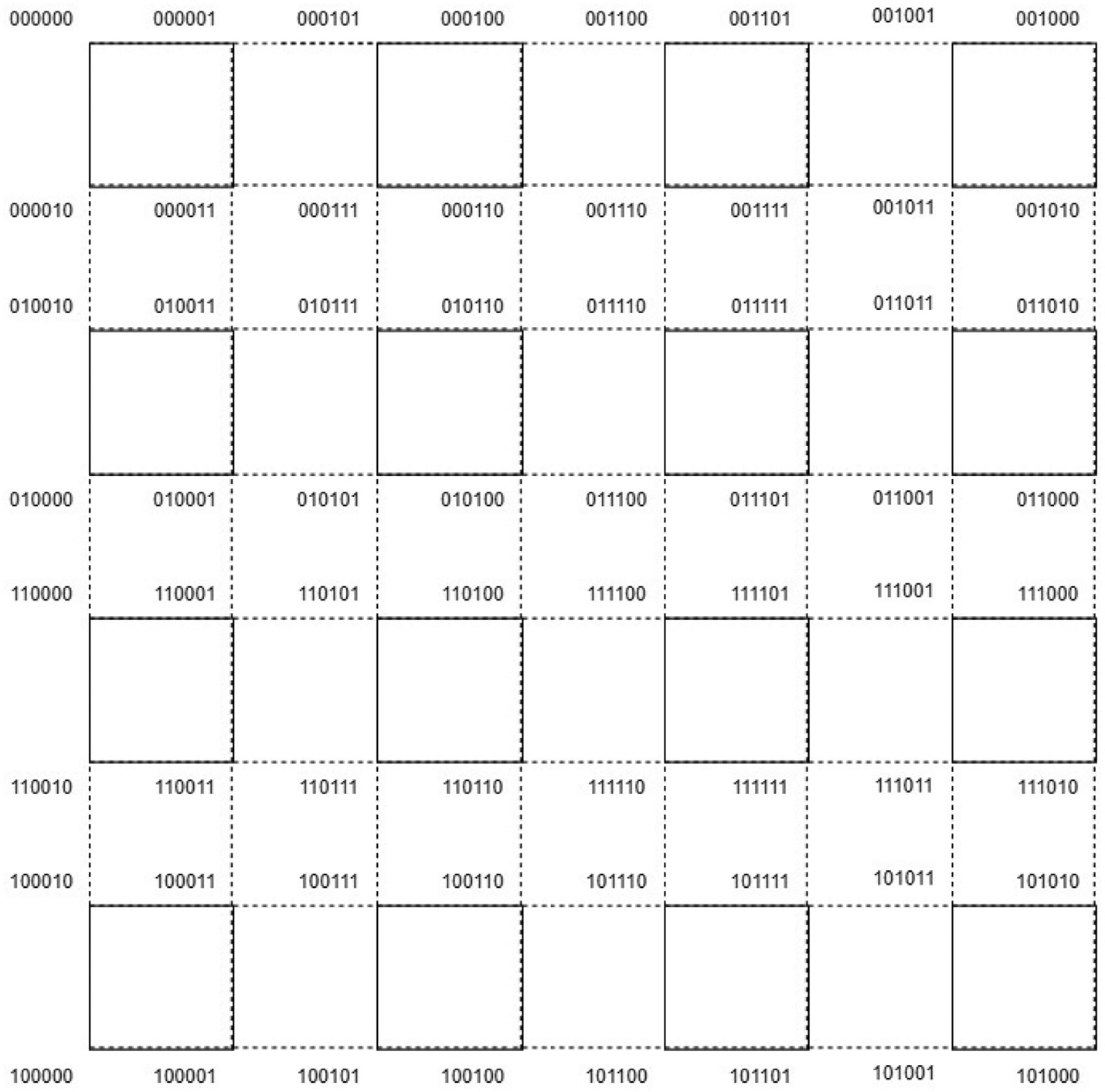


Fig. 5. Labelling of *CSNG*(2, 2) in [[17]](#_bookmark25).

101000; 100010 100011 100111 100110 101110 101111 101011

101010; 110010 110011 110111 110110 111110 111111 111011

111010; 110000 110001 110101 110100 111100 111101 111001

111000].

Example 4.2. Let *S* : (*Source*)= 101111 and *D* : *Destination* 010010 be the two nodes selected from the label set of *CSNG* 2, 2 . Mapping for this graph is obtained as in

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( )=

[Fig. 6](#_bookmark13) using [Algorithm 2](#_bookmark11). The method is to first determine the loca- tion of *S* and *D* on the map. The way to detect this is graph traver- sal. For this example, the location of *S* is *SI* = {7, 6} and the location

= { }

101111 → 111111 → 111101 → 011101 → 011111 → 011110

→ 010110 → 010111 → 010011 → 010010

Example 4.3. The Example 4.3 (map algorithm) maps the labels for the graph *CSNG k*, *m* in the 2*D* plane as [Fig. 6](#_bookmark13). The algorithm is recursive and works in the same way as the labeling algorithm

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given in [[17]](#_bookmark25). This algorithm returns an array of size such that the position of the labels in the 2*D* plane is equal to the number of vertices in the graph. Time complexity of the [Algorithm 2](#_bookmark11) run-

ning time is 2*k*+1 — 1 + 2*m*+1 — 1 as the calculation of the complex- ity of [Algorithm 1](#_bookmark8). When *k* > *m* the running time will be

of *D* is *DI* 3, 1 . It is understood from this locations to reach *D*

from *S* , path will go through 9 nodes that is equal to the difference

( — )

O(2*k*+1

— 1) and when *k* < *m* the running time will be

between the two locations as *SI* 1 *DI* 1 *SI* 2 *DI* 2

| ( )— ( )| + | ( )— ( )| =

4 5 9. After finding the locations of *S* and *D*, the path is formed by first moving vertically and then horizontally by [Algorithm 3](#_bookmark12). The locations of the nodes on this route are

+ =

{7, 6}→ {6, 6}→ {5, 6}→ {4, 6}→ {3, 6}→ {3, 5}→ {3, 4}

→ {3, 3}→ {3, 2}→ {3, 1}

where *SI* = {7, 6} and *DI* = {3, 1}. So, the route is

O 2*m*+1 1 . So, there are two cases of the running time of [Algo-](#_bookmark11) [rithm 2](#_bookmark11) as follows;

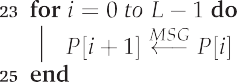
* *If k and m are different:* The running time of the [Algorithm 2](#_bookmark11) is

O(*z*) where *z* = *max*(2*k*+1 — 1, 2*m*+1 — 1),

* *If k and m are equal:* The running time of the [Algorithm 2](#_bookmark11) is O(*z*)

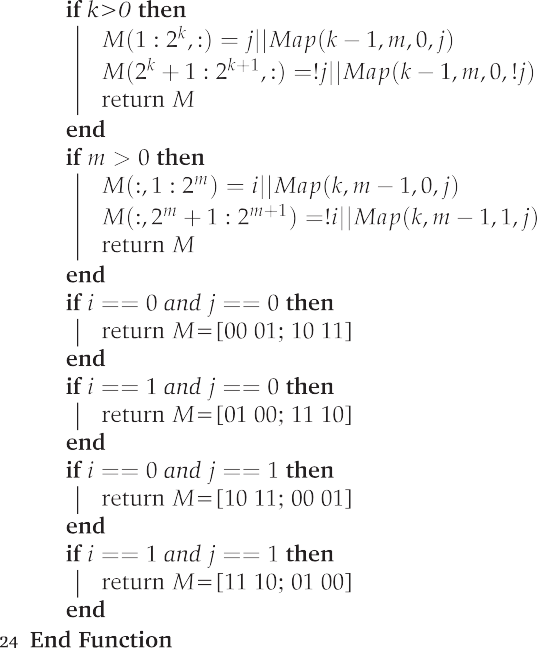
where *z* = 2*k*+2 — 2.

The [Algorithm 3](#_bookmark12) finds unicast routing for *CSNG*(*k*, *m*) with the help of [Algorithm 2](#_bookmark11). Time complexity of the [Algorithm 3](#_bookmark12) is O(2*k*+*m*+2).



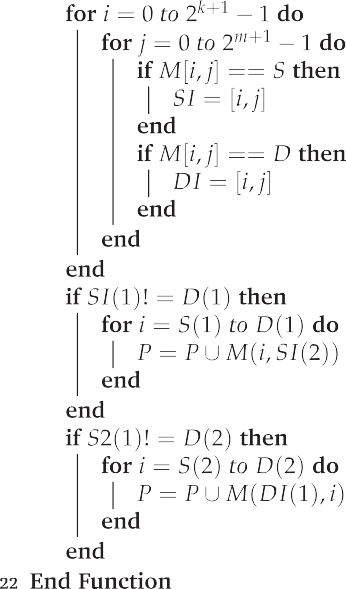
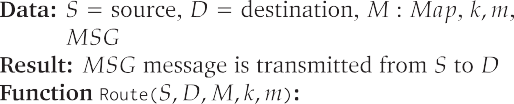
Algorithm 2: This algorithm is mapping labelling of nodes of

*CSNG*(*k*, *m*) using divide and conquer process.



Algorithm 3: This algorithm calculates unicast routing for

*CSNG*(*k*, *m*) (iterative process).



The *XY* routing algorithm is a distributed routing algorithm used on 2*D* mesh networks [[54]](#_bookmark46). This algorithm calculates the next node with using the current node address and the destination node address. Calculation of the next node takes constant time and it is done at each node on the path. So, the time complexity of the *XY* routing algorithm on *CSNG k*, *m* is H 2*k*+*m*+2 . Thus; [Algorithm 3](#_bookmark12) and the *XY* routing algorithm have the same time complexity For Example 4.2, the path created by Algorithm 3 is shown in [Fig. 7](#_bookmark14).

* + 1. *Parallelization*

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Through the utilization of parallel programming, it becomes possible to solve larger problems within a shorter time frame, thereby enhancing performance. Parallel programming method helps to provide the required acceleration and efficiency by divid- ing different parts of the problems into different processors for the solution of complex and large problems that have emerged with scientific developments. In parallel programming, the solving a problem is performed with the following steps [[55]](#_bookmark48);

Identifying the tasks that can be done simultaneously. Mapping the tasks that are concurrent to the parallel processes. Distribution of the program’s intermediate data input and out- put. Managing the use of data that is shared by processors.

●

●

●

Bringing the processors into sync at various points throughout the parallel program execution.

●

The most basic preference reason for parallel programming is to prevent slowdowns in the computer by making the most appropri- ate use of memory by using multiple processors instead of a single processor. In this way, computers can perform calculations quickly and this provides an increase in performance on the computer.

Mapping of labels for *CSNG k*, *m* can be perform in parallel to

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increase efficiency and decrease running time. At each iteration a map of subgraph *CSNG k*, *m* can be created while each label is computed on a different processor. When the function calls itself,

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it divides the problem in half and when the problem size is one, it calculates the map for *CSNG* 0, 0 . Then, the algorithm calculates a sub problem that is twice the size of the lower sub problem using the result of this lower sub problem.

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There are two different cases for running time of the [Algorithm](#_bookmark11) [2](#_bookmark11) in parallel architecture depends on the address length of nodes and the number of processors. Here, address length for

*CSNG k*, *m* is *k m* 2. For [Algorithm 3](#_bookmark12) there is only one case

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for the run-time complexity.

Case 1. *(If the number of processor* 2*n is greater than or equal to the number of nodes)* If [Algorithm 2](#_bookmark11) is executed on nodes in parallel, the running time will be *k* + *m* in this case. The reason for this is

that when computing the labels, 2*i* labels can be computed each time so that *i* can take values from 2 to *k* + *m*. For*CSNG*(*k*, *m*), *CSNG*(*k* — 1, *m*) or *CSNG*(*k*, *m* — 1) is used to com- pute the map and this step takes place in parallel in O(1) time on

different processors. When the map of the graph *CSNG*(*k*, *m*) is computed from the base case *CSNG*(0, 0), it takes O(*k* + *m*) time.

When 2*k*+*m* 6 2*n* and *n* ∈ *N*, running time of [Algorithm 2](#_bookmark11);

*T n k m*.

( )= +

For [Algorithm 3](#_bookmark12), the running time will be equal to the sending message through path. Finding the location of the source and des- tination nodes on the map takes O 1 time, at each node the source and destination node value is compared with the label value of that node and then the same ones are returned as row and column numbers of the graph, only the comparison time is used to calcu- late the running time of the parallel algorithm for location of

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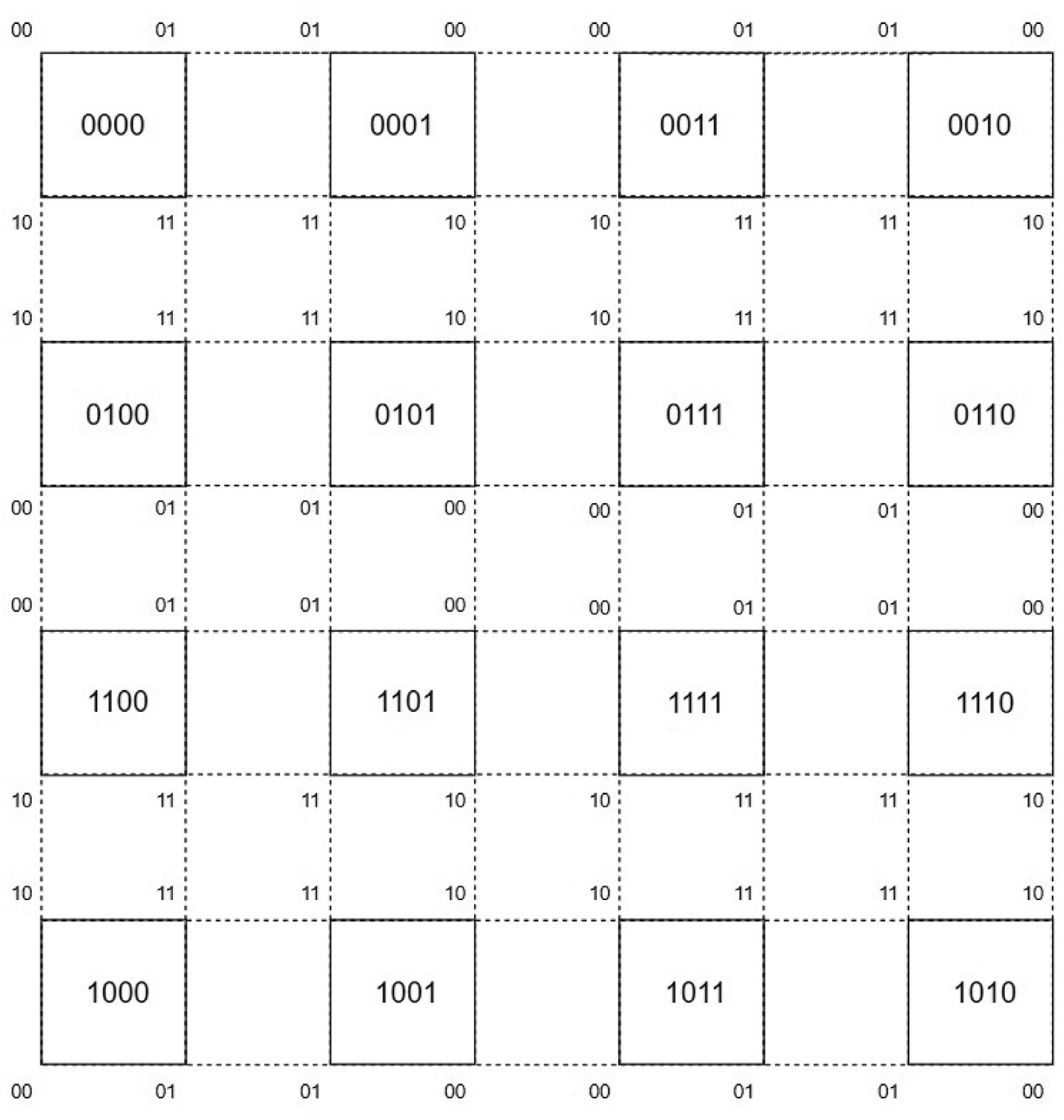


Fig. 6. Mapping process: labels of all the squares that make up *CSNG*(2, 2).

nodes. Once the location of the source and the destination node in the map is known, the number of vertices in a row and in a column of the map will be the running time of algorithm

2*k*+1 1 2*m*+1 1 that is O 2*k* 2*m* that is the message sending on 2*D* graph node by node in the worst case scenario. When 2*k*+*m* 6 2*n* and *n* ∈ *N*, running time of the [Algorithm 3](#_bookmark12);

— + — ( + )

*T*(*n*) = O(2*k* + 2*m*).

Case 2. *(If the number of processor is less than* 2*k*+*m*+2*)* The run- ning time of the [Algorithm 2](#_bookmark11) will be equal to *n* + 2*addresslength*—*n* where the *addresslength* is *m* + *k* + 2 and the number of processor

is 2*n*. It takes *n* times to compute the label of each node until the graph size is 2*n*, but once the size is larger than 2*n*, computation number increases proportionally to the power of 2. When

*k* + *m* > 2*n* and *n* ∈ *N*, running time of [Algorithm 2](#_bookmark11);

*T n* O *n* 2*addresslength*—*n* O *n* 2*k*+*m*+2—*n*

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The running time of [Algorithm 3](#_bookmark12) is same as Case 1, which is

O(2*k* + 2*m*) that is the sum of a number of vertices in a column and a number of vertices in a row in a *CSNG k*, *m* equals to mes- sage sending on 2*D* graph node by node in the worst case scenario. When *k* + *m* > 2*n* and *n* ∈ *N*, running time of [Algorithm 3](#_bookmark12);

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*T*(*n*) = O(2*k* + 2*m*)

* 1. *Broadcasting algorithms*

This subsection will focus on broadcasting algorithms, utilizing hypercube’s broadcasting algorithms.

Remark 4.1. Iterative version of one-to-all and all-to-all broad- casting algorithms for *CSNG*(*k*, *m*) can be obtained similarly by writing *k m* 2 instead of 2*k* 2 in Algorithms 2 and 3 in [[15]](#_bookmark25).

+ + +

[Algorithm 4](#_bookmark15) and [Algorithm 5](#_bookmark16) are the revised version of hypercube broadcast algorithms in [[44]](#_bookmark31) designed using divide and conquer approach. The costs for these two algorithms can be obtained by writing *k m* 2 instead of *logp* the complexity in [Algorithm 4](#_bookmark15).2 and [Algorithm 4](#_bookmark15).7 in [[44]](#_bookmark31).

+ +

According to cost analysis in [[44]](#_bookmark31), total time for one to all broad- casting has same equation and is equal to *logp* times time cost of point to point simple message transfer. Assume *ts* is the time required for start-up an *tw* is the time sending one word from one node to other node. To send a message with size *n* from one node to all nodes, time complexity will be *T ts twn k m* 2 . This algorithm has the same time com- plexity with the broadcast algorithm for mesh networks. For all- to-all broadcast algorithms, time complexity is similar with hyper-

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Fig. 7. Unicast routing for *CSNG*(2, 2).

cube and the equation for time complexity is

*T t k m* 2 *t n m k* 1 . Time for all-to-all broadcasting algorithm for mesh network is equal to *T* = 2*ts*(,ﬃ*p*ﬃﬃ-1)+*tw n*(*p* — 1)

,ﬃﬃﬃ

= *s*( + + )+ *w* ( + + )

where *p* is the number of node in the network. Since 2*ts* ( *p*-1)

>*tslogp* for large number of *p*, all-to-all broadcast algorithm for

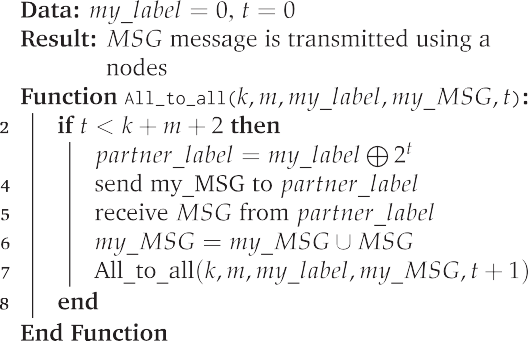
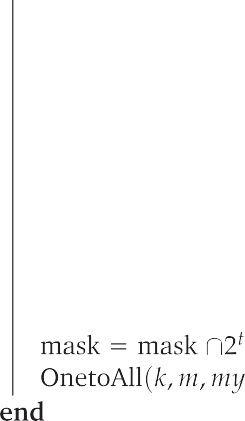
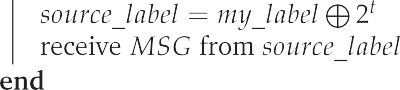
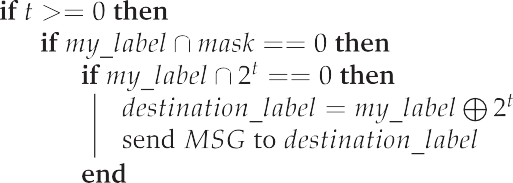
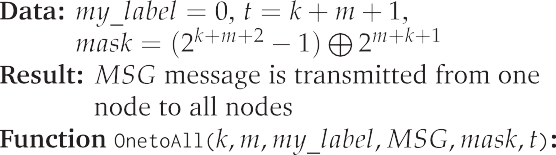
*CSNG*(*k*, *m*) has better performance than for mesh network.

Algorithm 4: This algorithm calculates one to all broadcasting

for *CSNG*(*k*, *m*) using divide and conquer approach.

Algorithm 5: This algorithm calculates all to all broadcasting

for *CSNG*(*k*, *m*) using divide and conquer approach.





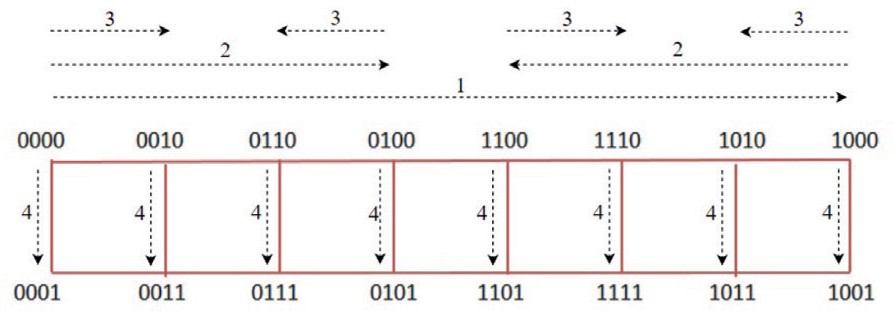


Fig. 8. All steps for one to all broadcasting for *CSNG*(*k*, *m*).

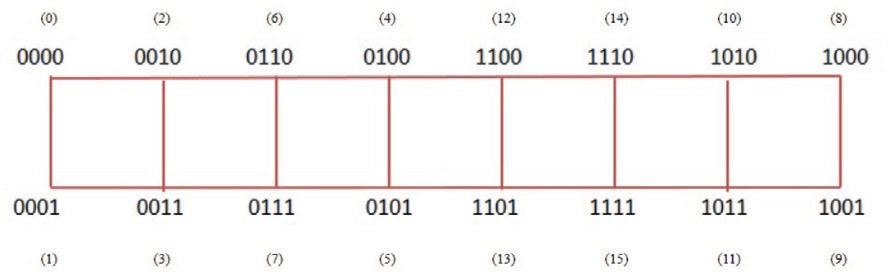


Fig. 9. Base case for all to all broadcasting for *CSNG*(*k*, *m*).

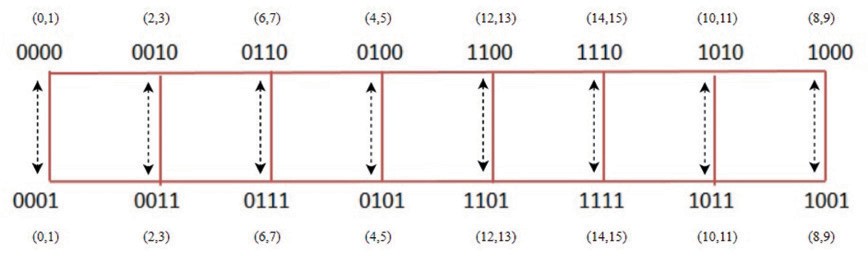


Fig. 10. First step for all to all broadcasting for *CSNG*(*k*, *m*).

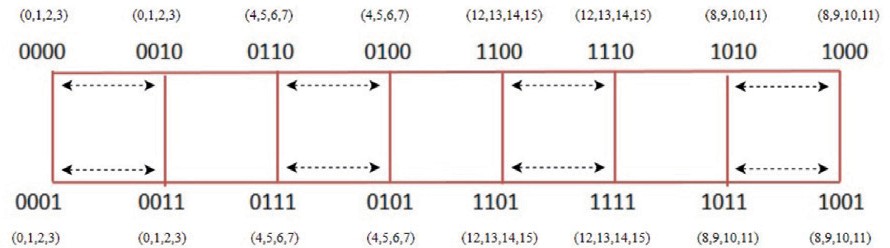


Fig. 11. Second step for all to all broadcasting for *CSNG*(*k*, *m*).

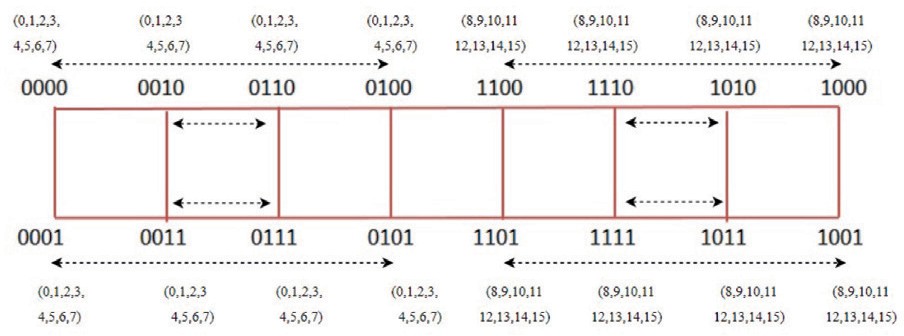
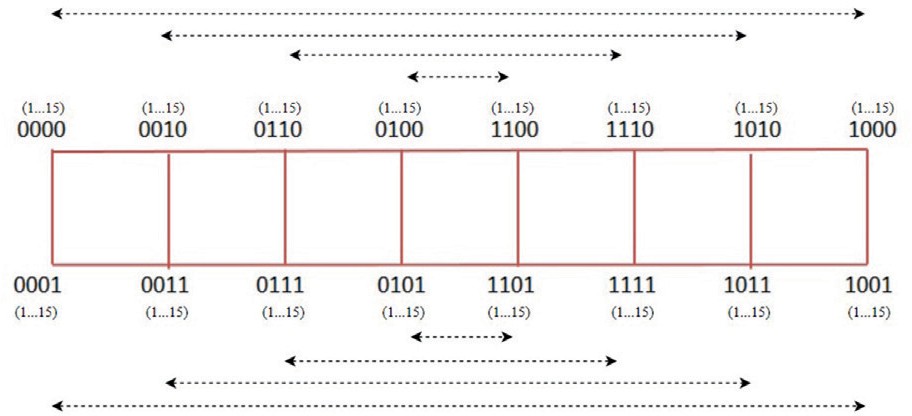


Fig. 12. Third step for all to all broadcasting for *CSNG*(*k*, *m*).

Actually, since this mesh structure *CSNG k*, *m* is constructed as a hypercube variant, the broadcasting algorithms for this mesh structure exhibit different behavior than the standard mesh algo- rithms (see [Fig. 8–13](#_bookmark17)).

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Fig. 13. Fourth step for all to all broadcasting for *CSNG*(*k*, *m*).

1. Conclusions

This study focuses on reevaluating Connected Square Network Graphs (*CSNG*(*k*, *m*)), which is a subgraph of 2*D*-meshes. The key feature of *CSNG*(*k*, *m*) is its resemblance to a hypercube variant. The primary objective of this study is to consider *CSNG k*, *m* as a

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hypercube variant and to develop algorithms that offer solutions to significant problems. As a result, these algorithms obtained for this graph are similar to hypercube algorithms rather than classical mesh algorithms. . The first algorithm aims to find an alternative Hamiltonian path for *CSNG* by using dynamic programming, with

a runtime of O(2*k*+*m*+2). The second algorithm calculates the map- ping of node labels for *CSNG k*, *m* and runtime of it is O 2*k*+*m*+2 . Additionally, [Algorithm 3](#_bookmark12) presents an unicast routing algorithm with time complexity of O *max* 2*k*+1 1, 2*m*+1 1 . The utilization of parallel and distributed architecture enhances performance at runtime, depending on the number of processors. The most general

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form of the runtime calculations for the parallel [Algorithm 2](#_bookmark11) equals to O(*n* + 2*k*+*m*+2—*n*) and running time of parallel [Algorithm 3](#_bookmark12) is

O 2*k* 2*m* where address length of *CSNG k*, *m* is *k m* 2. Broad-

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casting algorithms for *CSNG* can be easily derived using similar strategy as the broadcasting algorithms used for hypercube. For a specific case of 2*D* meshes, a different approach is presented using well-known hypercube algorithms.

Declaration of Competing Interest

The authors declare that they have no known competing finan- cial interests or personal relationships that could have appeared to influence the work reported in this paper.

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