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Modelling Dynamic Software Architectures using Typed Graph Grammars [1](#_bookmark3)

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Abstract

Several recent research efforts have focused on the dynamic aspects of software architectures providing suitable models and techniques for handling the run-time modification of the structure of a system. A large number of heterogeneous proposals for addressing dynamic architectures at many different levels of abstraction have been provided, such as programmable, ad-hoc, self-healing and self-repairing among others. It is then important to have a clear picture of the relations among these proposals by formulating them into a uniform framework and contrasting the different verification aspects that can be reasonably addressed by each proposal. Our work is a contribution in this line. In particular, we map several notions of dynamicity into the same formal framework in order to distill the similarities and differences among them. As a result we explain different styles of architectural dynamisms in term of graph grammars and get some better insights on the kinds of formal properties that can be naturally associated to such different specification styles. We take a simple automotive scenario as a running example to illustrate main ideas.

*Keywords:* Dynamic Software Architectures, Typed Graph Grammars and Modelling.

# Introduction

In the last decades, computer systems have changed from isolated static devices to highly interconnected machines that execute their tasks in a cooperative and coor- dinated manner. These modern, complex systems are known as *global computing systems* (GCS) or *network-aware computers*, and have to deal with frequent changes of the network environment. In a GCS, components are autonomous and dynamic, the network’s coverage is variable, and there is not a centralized authority.

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Software architectural models are intended to describe the structure of a sys- tem in terms of computational components, their interactions, and its composition patterns [[23](#_bookmark44)], so to reason about systems at a more abstract level, disregarding im- plementation details. Since GCS may change at design time, pre-execution time, or run-time [[20](#_bookmark41)], software architecture models for GCS should be able to describe the changes of the system structure and to enact the modifications during the system execution [[19](#_bookmark35)]. Such models are generally referred to as *Dynamic Software Archi- tectures (*dsa*s)*, to emphasize that the system architecture evolves during runtime.

A variety of definitions of dynamicity for software architecture have been pro- posed in the literature. Below we list some of the most prominent definitions to show the variability of connotations that the word *dynamic* acquires.

* **Programmed Dynamism** [[8](#_bookmark29)]. All admissible changes are defined prior to run- time and are triggered by the system itself.
* **Self-repairing** [[22](#_bookmark43)]. Changes are initiated and assessed internally, i.e., the run- time behavior of the system is monitored to determine whether a change is needed. In such case, a reconfiguration is automatically performed.
* **Self-adaptive** [[21](#_bookmark42)]. Systems can adapt to their environments by enacting run- time changes.
* **Ad-hoc dynamism** [[8](#_bookmark29)]. Changes are initiated by the user as part of a software maintenance task, they are defined at run-time and are not known at design-time.
* **Constructible dynamism** [[2](#_bookmark22)]. It is a kind of ad-hoc mechanism but all ar- chitectural changes must be described in a given modification language, whose primitives constrain the admissible changes.

The different proposals for dsa are bound to particular languages and models. In this paper we are aimed at understanding the main notions relying behind such proposals by abstracting away from particular languages and notations. We want to give a uniform formal presentation that is abstract enough to cover most of those features. In this sense, our work is in the line of other previous research efforts [[24](#_bookmark45),[7](#_bookmark28)]. In particular we select graph grammars as a formal framework for mapping the different notions of dynamicity because (i) they provide both a formal basis and a graphical representation that is in line with the usual way architectures are repre- sented, (ii) they allows for a natural way of describing styles and configurations, (iii) they have been largely used for specifying architectures. The use of graph gram- mars is instrumental in comparing different mechanisms and better understanding the kinds of properties that can be naturally associated to such specifications. We argue that the characterisation of dynamicity we present is to some extent orthog- onal to the particular kind of graph grammars we use, and therefore, extensible to different variants of graph rewriting systems.

**Related Work.** Several previous works have proposed alternative ways for de- scribing software architectures by using graph grammar. Our representation of dsa as graph grammars is borrowed from the Le M´etayer approach [[16](#_bookmark36)]. Actually the notion of programmed dsa corresponds to that proposal. A different way of repre- senting software architectures with graphs can be found in [[12](#_bookmark33)], where hyperedges

1 1

port1

component

r ,˛rr

*•*

2

*•* ,,

r,~¸connector~ ,

2 port2 3

Figure 1. A hypergraph describing a style.

are components and nodes are ports of communication, and the reconfiguration is given as context-free productions together with a constraint solving mechanism. Also Baresi et al. in [[3](#_bookmark23),[4](#_bookmark25)] use graph transformation systems to model programmed architectural styles at different levels of abstraction. Self-repairing mechanisms have been proposed in [[1](#_bookmark24),[9](#_bookmark30),[10](#_bookmark31),[22](#_bookmark43)]. Ad-hoc reconfiguration has been studied in [[8](#_bookmark29)] as a programming language that allows for the runtime modification of software architectures. Similarly, proposals for constructible languages can be found in [[20](#_bookmark41)]. These approaches are interested in providing executable frameworks for supporting dsa: The main difference w.r.t. our work is that they are aimed at providing real specification/ programming/ languages while we are aimed at giving an abstract characterization of such kind of mechanisms.

As far as the different flavours of dynamicity are concerned, the work of Wer- melinger in [[24](#_bookmark45)] explores the ability of the Chemical Abstract Machine (CHAM) [[5](#_bookmark26)] to express the dynamics of software architectures. His formalization proposes par- ticular CHAM (and commands) to tackle self-organized, ad-hoc, and programmed reconfiguration. Differently, we are interested in understanding how each particular style of dynamism is reflected into a graph grammar.

**Organization.** In Section [2](#_bookmark5) we describe the formal framework used in the rest of the paper, and the way in which software architectures are represented by using hypergraphs. Then we show how different forms of dynamism in software architec- ture can be expressed in terms of graph grammars (Section [3](#_bookmark9)) and apply them to a simple case study (Section [4](#_bookmark11)). Other orthogonal aspects of dynamism are discussed in Section [5](#_bookmark19). Some final remarks and future lines of research are in Section [6](#_bookmark20).

# Formalization of Dynamicity

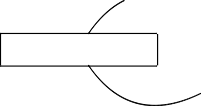
We model components and connectors as hyperedges and the ports to which they are attached as nodes. Figure [1](#_bookmark4) depicts an hypergraph containing two nodes port1 and port2, the hyperedge component (a component that exposes two different ports), and the hyperedge connector (a connector that has two tentacles to the port port1 and one to the port port2). Note that component edges are drawn as square boxes while connector edges as rounded boxes. Moreover, we show the ordering of tentacles by labeling the corresponding arrows with natural numbers (in some cases we shall use suitable names instead of numbers as labels, so to ease the reading).

**Definition 2.1** [Hypergraph] A *(hyper)graph* is a triple *H* = (*NH, EH, φH* ), where *NH* is the set of nodes, *EH* is the set of (hyper)edges, and *φH* : *EH → N* + describes the connections of the graph, where *N* + stands for the set of non-empty strings of

*H*

*H*

1 port1A



componentA

*•*

,1

3

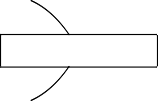
,, .connector1 ,,,

˛r

*•*

,

port1B 1

¸r

*•*

2 ˛,

componentB

* , ,

2 port2A port2B 2

Figure 2. A hypergraph describing a configuration for the style in Figure [1](#_bookmark4)

elements of *NH* . We call *|φH* (*e*)*|* the *rank* of *e*, with *|φH* (*e*)*| >* 0 for any *e ∈ EH* .

The connection function *φH* associates each hyperedge *e* to the ordered, non empty sequence of nodes *e* is attached to. An architectural style is just a hypergraph *T* that describes only the types of ports, connectors, components and the allowed connections. A configuration compliant to such style is then described by the notion of a *T* -typed hypergraph.

**Definition 2.2** [Typed Hypergraph] Given a hypergraph *T* (called the *style*), a *T-typed hypergraph* or *conﬁguration* is a pair *⟨|G|, τG⟩*, where *|G|* is the *underlying* graph and *τG* : *|G|→ T* is a total hypergraph morphism.

The graph *|G|* defines the configuration of the system, while *τG* defines the (static) *typing* of the resources. We recall that a total hypergraph morphism *f* : *G → G'* is a couple *f* = *⟨fN* : *N → N', fE* : *E → E'⟩* such that: *fN* (*φG*(*e*)) = *φG'* (*fE*(*e*)) (we overload *fN* to denote also the homomorphic extension of *fN* over strings).

Consider the style *T* in Figure [1](#_bookmark4): there is one unique type component of compo- nents exposing two ports of different types, and one connector attached to two ports of type port1 and one port of type port2. Then, a possible *T* -typed hypergraph (or a configuration of the style *T* ) is in Figure [2](#_bookmark6): it has two different components with their corresponding ports, and one connector. The typing morphism is implicitly defined by the name of the elements in the configuration, which consist of the type name plus a subindex identifying the particular instance (e.g., port port1A has type port1). We remark that the typing morphism requires components to have exactly one port of type port1 and one of type port2. Similarly, the only connections valid for a connector are those that attach its first two tentacles to ports of type port1 and the third one to a port of type port2. All such constraints are enforced by the existence of a typing morphism.

The reconfiguration of a software architecture is described by a set of rewriting productions. Roughly, a production *p* is a partial, injective morphism of *T* -typed graphs, i.e., it has the following shape: *p* : *L* > *R*, where *L* and *R* are *T* -typed hypergraphs, called the *left-hand* and the *right-hand side* of the production, re- spectively. Given a *T* -typed graph *G* and a production *p*, a rewriting of *G* using *p* can be informally described as follow: (1) find a (type preserving) match of the left-hand-side *L* in *G*, i.e., identify a subgraph of *G* that corresponds with *L*; (2) remove from the graph *G* all the items corresponding to the left-hand side that are not in the right-hand-side; (3) add all the items of the right-hand side that are not in the left-hand-side; (4) the elements that are both in *L* and *R* are preserved by the rewrite.

port1A



* ,1

r,~¸connector1~ ,

2

¸*•* ,

port2A



* ˛3r



port1B

port2B

port1A

>



* ,2

r,~¸connector2~ ,

1

¸*•* ,

*•*

port2A



port1B

 3

z*•* ¸

port2B

Figure 3. A rewriting production.

port1A

port1B

port1A

port1B

*•* , ˛*•* ,



~

1



* ,2

,r~¸co

˛*•* ,

J˜ 1 r~ ~ 2 ` \,

,¸ ,¸connector1 ,

>

nnector2 ,

* + ˛3r

port2A

*•*

port2B

*•*

port2A

3 z*•* ,

port2B

Figure 4. A rewriting production with negative application condition.

An example of a production is shown in Figure [3](#_bookmark7) (the morphism among the left and right-hand side of the rule is represented by using the same names for mapped elements, i.e., it is the partial inclusion). The production allows to remove an existing connector connector1 and to add a new connector connector2 that is attached to the original ports in a specular way with respect to the original connector.

Finally, an architecture is described by a *T* -typed graph grammar.

**Definition 2.3** [(*T* -typed) graph grammar] A *(T -typed) graph grammar G* is a tu- ple *⟨T, Gin,P⟩*, where *Gin* is the *initial (T -typed) graph* and *P* is a set of *productions*.

**Notation.** Let *G* = *⟨T, Gin,P⟩* be a (*T* -typed) graph grammar, and *G* and *H* (*T* -typed) hypergraphs. We write *G ⇒p H* to denote that *G* is rewritten in one step to *H* by using the production *p ∈ P* . We abbreviate the reduction sequence

*G*0 *⇒p*

*G*1 *⇒p*

*... ⇒p*

*Gn* with *G*0 *⇒p p ...p*

*Gn*. We write *G ⇒∗ G'* to denote

1 2 *n* 1 2 *n*

that there exists a possible empty sequence *s ∈ P∗* of derivation steps such that

*G ⇒s G'*.

For convenience when describing the examples, we will also consider productions with negative application conditions [[11](#_bookmark32)], i.e., productions that are equipped with a constraint about the context in which they can be applied. For instance, such conditions can state that the production is applicable only when certain nodes, edges, or subgraphs are not present in the graph. Such conditions are graphically shown in the left-hand-side of a production by grouping forbidden elements into a dotted lined area. Figure [4](#_bookmark8) shows a production with negative conditions stating that the new connector connector2 can be added to the configuration if and only if no other connector of type connector is already attached in a specular way to port1A and port1B. We refer the interested reader to the formal presentation of SPO graph grammars to [[17](#_bookmark37)] and to [[11](#_bookmark32)] for grammars with negative application conditions.

# Characterisation of Dynamism

This section characterizes different forms of dynamism in software architec- ture [[2](#_bookmark22),[8](#_bookmark29),[10](#_bookmark31),[20](#_bookmark41),[21](#_bookmark42),[22](#_bookmark43)] in terms of graph grammars. In particular we show that for verification aspects it make sense to focus only on two forms of dynamicity: Programmed and Repairing.

Given a grammar *G* = *⟨T, Gin,P⟩*, we will use the following notions:

* The set *R*(*G*) of *reachable conﬁgurations*, i.e., all configurations to which the initial configuration *Gin* can evolve. Formally, *R*(*G*) = *{G|Gin ⇒∗ G}*.
* The set *D*P(*G*) of *acceptable conﬁgurations* of an architecture are defined as the graphs that have type *T* and satisfies a auitable property P. Formally, *D*P(*G*) =

*{G | G is a T−typed graph ∧* P *holds in G}*.

* 1. *Programmed dynamism*

Programmed dynamism assumes that all architectural changes are identified at design time and triggered by the program itself [[8](#_bookmark29)]. Many proposals in the liter- ature [[16](#_bookmark36),[13](#_bookmark34),[4](#_bookmark25)] that use graph grammars for specifying dsa present this kind of dynamism. A programmed dsa *A* is associated with a grammar *GA* = *⟨T, Gin,P⟩*, where *T* stands for the style of the architecture, *Gin* is the initial configuration, and the set of productions *P* gives the evolution of the architecture. The grammar fixes the types of all elements in the architecture, and their possible connections, where the productions state the possible ways in which a configuration may change.

Programmed dynamism enables for the formulation of several verification ques- tions. Consider the set of desirable configurations *D*P(*G*), then it should be possible (at least) to know whether:

* + - the specification is correct, in the sense that any reachable configuration is desirable. This reduces to prove that *R*(*G*) *⊆ D*P(*G*), or equivalently that

*∀G ∈ R*(*G*) : P *holds in G*.

* + - the specification is complete, in the sense that any desirable configuration can be reached. This corresponds to prove *D*P(*G*) *⊆ R*(*G*), or equivalently that *if* P *holds in G then G ∈ R*(*G*).

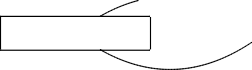
Hence, programmed dynamism provides an implicit definition of desirable config- urations. That is, the sets of desirable and reachable configurations should coincide, i.e., *Dp*(*G*) = *R*(*G*).

* 1. *Repairing (or healing) dynamism*

Self repairing systems are equipped with a mechanism that monitors the system be- haviour to determine whether it behaves within prefixed parameters. If a deviation exists, then the system itself is in charge of adapting the configuration [[9](#_bookmark30)].

We can think about a repairing architecture as an ordinary graph grammar *GA* = *⟨T, Gin,P⟩* in which the set of productions is partitioned into three different sets, i.e., *P* = *Ppgm ∪Penv ∪Prpr*. Rules in *Ppgm* describe the normal, ideal behaviour

1



1

port

.

r*•*

˛

component*i*

˛ r . r,~¸connector*j*~ ,

**,** .

*i j*

Figure 5. A general graph of types of a software architecture

of the architecture, i.e., *G'* = *⟨T, Gin, Ppgm⟩* is a programmed dsa. Rules in *Penv*

model the *environment* or, in other words, the ways in which the behaviour of the

*A*

architecture may deviate from the expected one. Rules in *Penv* may state that the communication among components may be lost or that a non authorised connector become attached to a particular component. Rules *Prpr* indicate the way in which an undesirable configuration can be repaired in order to become a valid one. That is, the left-hand side of any rule in *Prpr* identifies a composition pattern in the system that is undesirable. In this way a repairing architecture implicitly defines the desirable configurations of the system as those reachable configurations *G* that do not exhibit an undesirable composition pattern (i.e., a left-hand-side match for a repairing rule). Formally, the designer would expect that

*G ∈ D*P(*GA*) *iff G ∈ R*(*GA*) *∧*

*¬*(*∃q ∈ Prpr, ∃G' ∈ R*(*GA*) : *G ⇒q G'*)

As for the case of programmable dynamism, repairing dynamism allows for the formulation of the following two questions:

* the specification is complete. This reduces to prove that *G ∈ D*P(*GA*) implies

*G ∈ R*(*GA*) *∧ ¬*(*∃q ∈ Prpr, ∃G' ∈ R*(*GA*) : *G ⇒q G'*).

* the specification is correct. This corresponds to prove *G ∈ R*(*GA*) *∧ ¬*(*∃q ∈*

*Prpr, ∃G' ∈ R*(*GA*) : *G ⇒q G'*) implies *G ∈ D*P(*GA*).

In addition, this kind of dynamism naturally poses the question of whether reparing rules are adequate, i.e., whether the set of reparing rules assures that for any configuration that is reachable but not desirable there exists a sequence of repairing rules that moves the configuration to a desirable one. Formally,

* If *G ∈ R*(*GA*) *∧* (*∃q ∈ Prpr, ∃G' ∈ R*(*GA*) : *G ⇒q G'*) then *G ⇒q*

0

*... ⇒qn Gn* with *Gn ∈ D*P(*GA*) and *{q*0*,..., qn}∈ Prpr*.

*G*1 *⇒q*1

* 1. *Ad-hoc dynamism*

Roughly ad-hoc dynamism allows the architecture to evolve freely by adding and removing components and connectors without any restriction. The typed grammar corresponding to ad-hoc dsa should therefore exploit a fully general type graph that contains an infinite number of hyperarcs component*i* and connector*j* (see Figure [5](#_bookmark10)), one for every natural *i, j ∈* N. Any hyperarc component*i* (connector*j*) stands for the type of all connectors that expose exactly *i* ports (respectively, *j* roles). For simplicity, we define all ports as having the same type (otherwise the type graph should be extended, by adding an infinite number of nodes, to represent every

possible port type). Similarly, the set of production is infinite as it must allow for adding/ removing any kind of components and connectors. This leaves little space for verification issues, as the only guarantees given by ad-hoc dynamicity is that reached graphs are software configurations.

* 1. *Constructible dynamism*

Constructible dsas are similar to ad-hoc dsas but here rewriting productions are not the free combination of basic primitives: they are full-fledged programs written in some specific language. The main difference w.r.t. ad-hoc dsa is that a constructible dynamic architecture is mostly characterised by the specific programming language allowed for defining the reconfiguration programs that can manage the evolution. Generally speaking, constructible dynamism provides a very weak notion of de- sirable configurations, and hence verification aspects are almost meaningless when assuming autonomous reconfiguration (likewise ad-hoc dynamism). However, the situation is slightly different when considering reconfigurations controlled externally (see discussion in Section [5](#_bookmark19)).

# Automotive Software System

In order to illustrate the main forms of dynamism, we introduce the following sce- nario borrowed from the European Project sensoria.

* 1. *Automotive Case Study: Overview*

Much of the cost of research and development in vehicle production are associated to automotive software. Today vehicles are equipped with a multitude of sensors and actuators that provide different services, like abs and vehicle stabilization systems, that assist people to drive safer. Thanks to current mobile technology, vehicles have the possibility to connect to the telephone and internet infrastructures. This has given birth to a variety of new services into the automotive domain. Communication in AS systems may involve *communication* that takes place inside a vehicle (*intra- vehicle)*, connection to vehicles in the vicinity (*inter-vehicle)*, or interaction with the environment, for example through an Internet gateway (*vehicle-environment)*. Our scenario will focus only the last two kinds of communications.

* 1. *Car Assistance Scenario*

Consider a vehicle subscribed to an assistance service. Due to a collision, the airbag of the car is inflated, which causes the automatic generation of a message destined to the *accident assistant server*. The message can be transmitted through near vehicles until reaching the server (preferred method) or directly to the server. The message will be eventually delivered to the assistance server, which will coordinate the assistance.

pin

*in*  z*•* ¸ j *out*

V *out* z*•* ,r*in* r~V*/*V~ *s* pout

,¸ ,

r*•* , *i*r*n* r,~¸V*/*S~ , *out* z*•* ,r

S

ps pio *io*

Figure 6. Architectural Style of the AS System

* ,*in*r

z*•* ,*in*r,r¸~V*/*V1~ , *out* z*•* ,*in*r

z*•* ,*in*rr,~¸V*/*S1~ ,

*out*z*•* ,*io*rS

pin1

*•*,

pout1

pin2

*o*

*•*,

ps2

pios

ps1

|  |  |  |
| --- | --- | --- |
| V1 | | *out* |
|  |
| *s* |  | |

|  |  |  |
| --- | --- | --- |
| V2 | | *s* |
|  |
| *ut* |  | |

pout2

Figure 7. An instance of the AS style.

We will model the scenario by using two different kinds of components:

* **Vehicle (**V**)**: a component responsible for transmitting messages destined to the assistant server. A vehicle component has three associated ports: *pin* for receiving a message from another vehicle, *pout* for sending messages to the next vehicle, and *ps* for communicating directly with the server.
* **Accident Assistant Server (**S**)**: a component that handles help requests. Its unique port *pio* is used for sending/receiving information to/from vehicles.

Components are connected by using the following connector types:

* **Vehicle to Vehicle communication (**V*/*V**)**: a connector used for mediating the communication between two vehicles.
* **Vehicle to Accident Assistant Server communication (**V*/*S**)**: a connector used for supporting the interaction between a vehicle and a server.

Figure [6](#_bookmark12) shows the architectural style of the AS system, while Figure [7](#_bookmark13) depicts an instance consisting of two vehicles (V1 and V2) and one server (S).

* 1. *Programmed Dynamism*

We will use a programmable architecture for specifying the way in which the AS system keeps the communication structure among the components. We define the corresponding graph grammar *GA* = *⟨T, Gin,P⟩*, where the architectural style *T* is depicted in Figure [6](#_bookmark12), and the initial configuration *Gin* consists in the *T* -typed graph containing a unique component of type server S and its associated port (i.e., a node of type portio). The set *P* contains the productions that model the arrival/departure of a vehicle into/from the area covered by a server, a car that is getting close/far to/from another car, and a car that takes over another one. For space limitations, we describe just three productions. Figure [8](#_bookmark15) stands for the case of vehicle entering into the area covered by a server. This is modeled by creating a new component of type V and its associated ports, and a new connector between the car and the server.

pios

* ,*io*rS

pin1

> *•* ,*in*rV1 *s*

*out*

z*•* ,*in*r,r~¸V*/*S1~ , *out*

s

p 1

z*•*  ,*io*rS pios

*•*,

pout1

Figure 8. New Vehicle connected to the Server.

* ,*i*r*n*

*s* z*•* ,*in*rr,~¸V*/*S1~ ,*out* z*•* ,

* ,r

*in*

V3

pout3 *•* ¸

,r~¸V*/*V2~ ,*ou*

*in*

*s* z*•* , *•*

pin3

*out* ,

ps3

pios

pin3

*out* ,

ps3

pios

pout3 *• ss* >

V3

*ss*

*t* z*•* ,*in*rV2

pin2

*s*

,

*out*

z*•* ,

*•*

*in* r~ *s*~ *s in*

* + ,r,¸V*/s*V*ss* ,*out* z*•* ,r

V2

*out* z*•* ,

pout2

p 1 *sss* 1

p 2 *s* p 2

out *sss*

*sss*

in *•*, out

ps2

ps2

Figure 9. Vehicles approximation.

ps1

*•* ¸

V1

*out* ¸

ps1

*•* ¸

r~ ~

*s* pout1

pin1 *s*

r~ ~

* ,*in*r,¸V*/*V1 , *out* z*•* ,*in*rV1

*out* z*•* ,¸

* ,*in*r *out*z*•* ,*in*r,¸V*/*V6 , *out* z*•* ,

pout0

*in*

*•*

,

,r

*out*

V2

pin1

*in*

,r~¸V*/*V3~ ,

> ,r~¸V*/*V5~ ,

pout1

pin3

* *out*r,~¸V*/*V ~ , *in*

,r

3

z,rV2

* *out*

*in* ,*ou* *t*

*in •*

*out*r,~¸V*/*V ~ , *in •*

pin3

z*•* ,

z,r

z,

pout2

,*s*

ps2

*•*

4

pin2

pout2

,*s* pin2

ps2

*•*

pout0

Figure 10. One vehicle takes over another one.

Figure [9](#_bookmark16) describes the case of a car connected to the server (V3) that gets closer to another vehicle (V2) that is at the end of a queue (note V2 has no connectors attached to its input port). In this case the connection of V3 to the server is removed and a new connector between V3 and V2 is created. Finally, Figure [10](#_bookmark17) depicts a production that handles the case in which a vehicle takes over another one. (Note the rule assumes the vehicles V1 and V2 to be in the middle of a queue, since they are connected to other vehicles. The specification includes three additional rules to handle the cases in which the take over involves cars at the ends of the queue.)

The set of desirable configurations for the AS system consists of all the config- urations in which each vehicle has a unique, acyclic communication path with the unique server, and each vehicle port has attached at most one connector.

* 1. *Repairing dynamism*

The following example shows the use of a repairing architecture for modelling the fact that the communication between vehicles is not reliable and can be lost, but in such cases the architecture should repair itself in order to provide unconnected components with a link to a server. We defined a graph grammar *GA* = *⟨T, Gin,P⟩* in which the set of productions is divided into three different sets, i.e., *P* = *Ppgm ∪*

*•* ,*in*rr,~¸V*/*V1~ ,*out* z*•* , > *• •*

**(a)**

*•* ,

*in*

pin1

*s*

pout1

pin1

pout1

> *•* ,



*out*

pout1

r,r~¸V1~ ,

*in*

pin1

pin1

*s*

*•* ¸

z*•* ¸,*in*rr,~¸V*/*S2~ , *out* z*•* ,

**(b)**

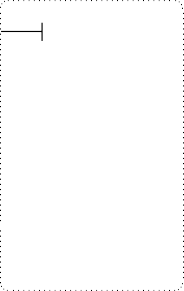
p

io1

ps1

pio1

Figure 11. (a)Lost of connectivity. (b) Repairing.



*•* ¸,r,¸V*/*V1 , *s*

*ss*

*in* r~ ~

*out* z

*s* ,

*s*

*s*

*s*

*•*

*ss* pin2

*sss*

*sss*

*ss*

*ss*

s1

z*•* ¸,*in*rr,~¸V*sss/*S1~ , *out* z,*•*

*sss*

p

*ss*

*out*

pout1

rr,~¸V1~ ,

*Penv ∪ Prpr*. In our scenario *Ppgm* contains the same productions as defined in Programmed Dynamism (see [4.3](#_bookmark14)), *Penv* contains the unique production shown in Figure [11](#_bookmark18)(a), which models the loss of connectivity between vehicles (i.e., the removal of a connector V*/*V1), and repairing production is in Figure [11](#_bookmark18)(b). Such rule states that whenever a vehicle without outcoming connections is found (note the left-hand-side of the rule requires the absence of connections on ports pout1 and ps1), then the vehicle should be connected directly to a server.

As for the case of programmable dsas, in desirable configurations all vehicles have a connection to the server. Nevertheless, the repairing grammar takes into account the cases in which the configuration may not satisfy this condition (some vehicles may not be connected to a server). However, these cases match with the left-hand-side of the repairing rule, and hence, they can be repaired by the system.

# Constrained and Self dynamism

Other aspects that are, to some extent, orthogonal to the approaches characterised in Section [3](#_bookmark9) are: (i) whether the application of a transformation rule can take place at any moment or not, and (ii) whether changes are fired internally by the system or activated externally. The first aspect is usually refered to as *constrained vs uncon- strained dynamism*, while the second is qualified as *self* vs *external* reconfiguration.

* 1. *Unconstrained vs Constrained dynamism*

Basically, constrained dynamism refers to the fact that a change may occur only after pre-defined constraints are satisfied. Such constraints may be (i) the con- figuration topology, e.g., when components are not connected in a specific, or (ii) the state of a component, e.g., when a component enters into the quiescent state. Topological constraints are naturally modelled by both positive and negative appli- cation conditions of graph productions. Hence, topological constrained dynamism may be characterised by a graph grammar whose productions have some contexts (either positive or negative). Differently, constraints related to particular states of

components have not an immediate counterpart in our proposal (since our frame- work does not describe component states). Nevertheless, they can be encoded by thinking about different states of components as different types of hyperedges. In this way, the change of a component state *s* into *s'* is represented as the rewrite that removes the hyperarc denoting the component in state *s* and adds a new hyperarc of type *s'* with attachments analogous to those of the removed arc. In this case, the fact that the grammar describes a dynamism constrained on the state of some com- ponents is hidden by the encoding. Another possibility is to use of attributed graph grammars [[18](#_bookmark38)] for equipping components with attributes describing their states.

Unconstrained dynamism refers to the fact that transformations can be applied at any moment. The graph grammar counterpart is the fact that productions have no associated constraints or application conditions, being, in some sense, context free, because they either produce or consume arcs but they do not read them.

* 1. *Self dynamism*

Usually, some kind of dynamisms (like programmed and repairing) are also qualified as “self”, meaning that the changes are initiated by the system itself and not by an external agent. We map the notion of self and external dynamism to particular features of the rewrite system. As a starting point we discuss some alternative ways for chosing a particular reconfiguration in a dsa, as proposed in [[7](#_bookmark28)].

* + - *External*: The reconfiguration rule is selected by an external source. This option resembles the external choice of process calculi, in which the branch of compu- tation to be selected is indicated by the context of process. In this sense, we can interpret a reduction of the form *G ⇒p G'* as the fact that the environment selects the application of the production *p*.
    - *Autonomous*: The system selects one of all the applicable transformations in a non-deterministic way. This corresponds to the notion of internal choices in process calculi. Accordingly, we may represent such reductions by hiding the actual name of the applied rule. That is, a rewriting step *G ⇒p G'* in which *p* is autonomous can be represented as *G ⇒τ G'*, where *τ* stands for a hidden change.
    - *Pre-deﬁned*: Pre-defined selection is a special case of autonomous choice, in which the system selects in a pre-defined way the appropriate transformation to apply from the set of available ones. In this case, the choice is completely deterministic (like a conditional choice if - then - else - of process calculi). This can be mapped into graph grammars as the definition of priorities in the selection of productions to be applied. As shown in [[11](#_bookmark32)], application conditions can be used as priorities for restricting the order in which rules are applied.

Let *G* = *⟨T, Gin, Pext ∪ Pself ⟩* be a grammar, where *Pext* stands for the set of all reconfigurations that are controlled by the environment, while *Pself* contains all the autonomous productions. We say *GA* has (i) self dynamism if *Pext* = *∅*, (ii) external dynamism if *Pself* = *∅*, or (iii) mixed dynamism otherwise. Assuming that all rewriting steps *G ⇒p G'* are written *G ⇒τ G'* when *p ∈ Pself* , we define the

following sets associated to the grammar *G* = *⟨T, Gin, Pext ∪ Pself ⟩*:

* + - * The set *S*(*G*) of autonomous or self reconfigurations, i.e., the set of all configura- tions reachable by applying autonomous changes is: *S*(*G*) = *{G | Gin ⇒τ∗ G}*.
      * The set *Ec*(*G*) of reconfigurations associated to an external sequence *c* = *p*1 *... pn* of commands: *Ec*(*G*) = *{G | Gin ⇒c' G ∧ c'* = *τ∗, p*1*,τ∗,...,τ∗, pn,τ∗}*. Note *Ec*(*G*) contains all the configurations reachable from the initial configuration by applying the sequence *c* of external chosen rules interleaved with the application of zero or more autonomous reconfigurations.

Clearly, *S*(*G*) and *Ec*(*G*) are subsets of *R*(*G*). Hence, we can proceed as in Sec- tion [3](#_bookmark9), and formulate some verification problems. In particular, we can specialise the problem *R*(*G*) *⊆ D*P(*G*) to either *S*(*G*) *⊆ D*P(*G*) or *Ec*(*G*) *⊆ D*P(*G*). The last re- lation is particular interesting when considering ad-hoc or constructible dynamism. In this case, it is possible to check whether a particular reconfiguration program may produce acceptable configurations.

# Final Remarks

In this work we have characterised different aspects of dynamic reconfiguration as particular features of graph rewriting systems. By taking advantage of this frame- work, we have distilled whether such kinds of dynamisms allow for posing typical questions about the completeness and correctness of the architectural specification. Figure [12](#_bookmark21) summarises the conclusions for the different types of dynamisms.

|  |  |  |  |  |
| --- | --- | --- | --- | --- |
| **Dynamicity** | **References** | **Correctness** | **Completeness** | **Adequacy** |
| Programmed | [[4](#_bookmark25),[8](#_bookmark29),[13](#_bookmark34),[16](#_bookmark36),[24](#_bookmark45)] | + | + | - |
| Repairing | [[1](#_bookmark24),[9](#_bookmark30),[10](#_bookmark31),[21](#_bookmark42),[22](#_bookmark43)] | + | + | + |
| Ad hoc | [[6](#_bookmark27),[8](#_bookmark29),[25](#_bookmark46)] | - | - | - |
| Constructible | [[2](#_bookmark22),[20](#_bookmark41)] | -/+ | -/+ | - |

Figure 12. Classification summary

As mentioned in Section [3](#_bookmark9), given a characterization of all desirable configurations of a programmable architecture, e.g., by defining a property P that should hold in every configuration, then it would be possible to prove whether the architectural specification is correct (by showing that P holds in every reachable configuration) and complete (by proving any configuration satisfying P is reachable). Correctness and completeness properties could be associated to repairing dynamism. But, dif- ferently from programmed dynamism, some reachable configurations of a repairing architecture may be non desirable. Instead, those configurations should be trans- formed into a desirable one by using repairing rules. The main idea is that unde- sirable configurations are characterized as those reachable configurations in which some repairing rule is applicable. Then, correctness and completeness properties involve those reachable configurations that are desirable. Such questions are mean- ingless for ad hoc dynamicity, where every configuration is potentially reachable. Analogously for constructible dynamism, even if some kind of weak analysis could

be performed in this case. For instance, to prove that particular configurations are not reachable when the reconfiguration language forbid some kind of programs.

Actually, the above characterization corresponds to the case in which transfor- mations are all autonomous, i.e., when we assume self dynamism. When external dynamism is considered, also correctness and completeness properties over ad hoc and constructible architectures can be formulated. For instance, given a particular (set of) desirable configuration(s) it can be proved whether a particular transforma- tion or configuration program selected by a programmer produces a desirable config- uration. Even more interesting is the case in which mixed dynamism is considered. Assume an ad hoc architecture where some productions are considered external and others autonomous or self. In this case, external transformations account for the reconfigurations activated by a user, while autonomous transformations model the actual program that performs the transformation (a kind of scripting). In this case, it would be possible to check whether a particular script produces a correct configuration when it is applied over a specific configuration.

In this paper we have identified classes of properties that can be naturally asso- ciated some kinds of dynamicities. Next work will approach the problem of verifying such properties over graph grammar specifications. In particular, we have in mind to use Alloy [[14](#_bookmark39),[15](#_bookmark40)] for attempting this task and we are going to concentrate our efforts on proving properties associated to each kind of dsa.

# References

1. R. Allen, R. Douence, and D. Garlan. Specifying and analyzing dynamic software architectures. *Proc.* *of FASE’98*, vol. 1382 of *LNCS*, pp. 21–37. Springer, 1998.
2. J. Andersson. Issues in dynamic software architectures. *Proc. of ISAW4*, pp. 111–114, June 2000.
3. L. Baresi, R. Heckel, S. Th¨one, and D. Varr´o. Modeling and validation of service-oriented architectures: application vs. style. *Proc. of ESEC/FSE’03*, pp. 68–77. ACM, 2003.
4. L. Baresi, R. Heckel, S. Th¨one, and D. Varr´o. Style-based refinement of dynamic software architectures.

*Proc. of WICSA’04*, pp. 155–166. IEEE, 2004.

1. G. Berry and G. Boudol. The chemical abstract machine. *Theoret. Comput. Sci.*, 96(1):217–248, 1992.
2. C. Bidan, V. Issarny, T. Saridakis, and A. Zarras. A dynamic reconfiguration service for corba. *Proc. of ICCDS’98*, pp. 35–42, 1998.
3. J. Bradbury, J. Cordy, J. Dingel, and M. Wermelinger. A survey of self-management in dynamic software architecture specifications. *Proc. of WOSS’04*, pp. 28–33. ACM, 2004.
4. M. Endler. A language for implementing generic dynamic reconfigurations of distributed programs.

*Proc. of BSCN’94*, pp. 175–187, 1994.

1. D. Garlan and B. Schmerl. Model-based adaptation for self-healing systems. *Proc. of WOSS’02*, pp. 27–32. ACM, 2002.
2. I. Georgiadis, J. Magee, and J. Kramer. Self-organising software architectures for distributed systems.

*Proc. of WOSS’02*, pp. 33–38. ACM, 2002.

1. A. Habel, R. Heckel, and G. Taentzer. Graph grammars with negative application conditions. *Fundam. Inform.*, 26(3/4):287–313, 1996.
2. D. Hirsch, P. Inverardi, and U. Montanari. Graph grammars and constraint solving for software architecture styles. *Proc. of ISAW’98*, pp. 69–72. ACM, 1998.
3. D. Hirsch, P. Inverardi, and U. Montanari. Reconfiguration of software architecture styles with name mobility. *Proc. of COORDINATION’00*, vol. 1906 of *LNCS*, pp. 148–163. Springer, 2000.
4. D. Jackson. Alloy: a lightweight object modelling notation. *ACM Trans. Softw. Eng. Methodol.*, 11(2):256–290, 2002.
5. D. Jackson. *Software Abstractions: Logic, Language, and Analysis*. The MIT Press, 2006.
6. D. Le M´etayer. Describing software architecture styles using graph grammars. *IEEE Trans. Software Eng.*, 24(7):521–533, 1998.
7. M. L¨owe. Algebraic approach to single-pushout graph transformation. *Theoret. Comput. Sci.*, 109(1- 2):181–224, 1993.
8. M. L¨owe, M. Korff, and A. Waner. An algebraic framework for the transformation of attributed graphs.

*Term Graph Rewriting: Theory and Practice*, pp. 185–199. John Wiley & Sons Ltd, 1993.

1. N. Medvidovic and R. Taylor. A classification and comparison framework for software architecture description languages. *IEEE Trans. Software Eng.*, 26(1):70–93, 2000.
2. P. Oreizy. Issues in the runtime modification of software architecture. Technical Report UCI-ICS-96-35, University of California, 1996.
3. P. Oreizy, M. Gorlick, R. Taylor, D. Heimhigner, G. Johnson, N. Medvidovic, A. Quilici, D. Rosenblum, and A. Wolf. An architecture-based approach to self-adaptive software. *IEEE Intelligent Systems*, 14(3):54–62, 1999.
4. B. Schmerl and D. Garlan. Exploiting architectural design knowledge to support self-repairing systems.

*Proc. of SEKE’02*, pp. 241–248. ACM, 2002.

1. M. Shaw and D. Garlan. *Software Architecture: Perspectives on An emerging Discipline*. Prentice Hall, 1996.
2. M. Wermelinger. Towards a chemical model for software architecture reconfiguration. *IEE Proc. - Software*, 145(5):130–136, 1998.
3. A. Young and J. Magee. A flexible approach to evolution of reconfigurable systems. *Proc. of IWCDS’92*, IEE, pp. 152–163, 1992.