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Verification of Scenario-based Specifications using Templates

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Abstract

Specifying dynamic behaviour of a system by listing scenarios of its interactions has become a pop- ular practice. Message sequence chart (MSC) is a rigorous and widely used notation for specifying such scenarios of system behaviour. High-level MSCs (HMSC) provide hierarchical and modular composition facilities for constructing complex scenarios from basic MSCs. Although the general problem of formal verification of properties of HMSCs is intractable, we propose a framework for restricted verification. We present simple templates for commonly used types of properties and discuss efficient algorithms for verifying them.

*Keywords:* Scenario-based Specifications, Message Sequence Charts, Formal Verification, Formal Methods.

# Introduction

It is important to clearly and precisely state the behavioural requirements when building practical business systems as well as safety-critical real-time, embedded systems (e.g., see our railway system [[23](#_bookmark33)]). It is not always easy to communicate dynamic behavioural requirements of a system to the end-users in an easy-to-understand non-mathematical manner, particularly in the early stages of requirements analysis, where the requirements need to be high-level and abstract (removed from design and implementation issues). A simple and intuitive way to describe a system is to list various examples or scenarios of its

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intended behaviour. At the highest level of abstraction, a scenario describes a set of interactions of the system with its environment and other external systems. An interaction includes entities (including system components) that participate in it and event occurrences. An interaction scenario often stands for a set of possible event sequences (episodes). Each interaction scenario is typically classified as either desirable (*sunny day*) or undesirable (*rainy day*). Ideally, an implementation should meet all sunny day interaction scenarios and none of the rainy day ones.

There is a need for simple, expressive, intuitive, graphical and standard- ised notations to specify interaction scenarios of systems. Message sequence chart (MSC) is just such a simple, visual and mathematically rigorous no- tation [[25](#_bookmark35),[13](#_bookmark23)]. MSCs have been used widely in the telecom domain and are also increasingly being used in many other applications [[23](#_bookmark33)]. The sequence diagram and use case notations in UML are semantically and visually close to the MSC notation [[12](#_bookmark17),[11](#_bookmark18),[20](#_bookmark30),[8](#_bookmark19)]. The ITU standard for MSC [[14](#_bookmark24)] includes a mathematical semantics for it. Other researchers have provided mathemati- cal semantics for MSCs using formalisms such as transition systems, process algebras etc. [[16](#_bookmark26),[19](#_bookmark29),[18](#_bookmark28),[9](#_bookmark20)]. In this paper, we use the partial order semantics given by Alur et al [[2](#_bookmark12)]. The main use of a formal semantics for a notation is that it can be used to design formal verification and analysis algorithms. For example, a specification written using MSCs can be analysed to detect various problems such as missing scenarios and race conditions. Formal veri- fication algorithms can be designed which check whether a given specification written using MSCs satisfies a given property written in a suitable formal no- tation [[2](#_bookmark12),[1](#_bookmark11),[24](#_bookmark34),[17](#_bookmark27),[4](#_bookmark14),[21](#_bookmark31),[22](#_bookmark32),[5](#_bookmark15)]. Model-checking tools have also been applied for formal verification of MSCs [[3](#_bookmark13),[10](#_bookmark21)].

This paper presents a set of restricted simple property templates and for- mal verification algorithms to check whether a given High-level MSC (HMSC) satisfies the given property. Since the general problem of formal verification of HMSC specification is intractable when the property is specified in tem- poral logic or equivalent notations, following [[7](#_bookmark22),[15](#_bookmark25),[5](#_bookmark15)], we restrict the kinds of properties that can be specified and give specialised algorithms for formal verification of properties specified using each template. We present a compro- mise wherein we sacrifice expressiveness for efficient verification. The MSC notation has been extended in several ways (e.g., [[26](#_bookmark36)]); here we focus on the core aspects of the MSC and HMSC notation only.

Section [2](#_bookmark1) presents an overview of HMSC notation and its formal semantics. Section [3](#_bookmark4) discusses our approach of property templates and algorithms to verify them. Section [4](#_bookmark7) provides conclusions and further work.

# HMSC Semantics

We assume familiarity with basic MSC notation and its partial order (linear time) semantics [[2](#_bookmark12)] (see Appendix [A](#_bookmark8) for an overview). Here we summarise the relevant definitions for MSC-graphs and HMSC.

* 1. *Linear Time Semantics of MSC Graphs*

An MSC graph is essentially a directed (not necessarily acyclic) graph in which each vertex refers to a basic MSC.

Definition 2.1 An *MSC graph G* is a tuple (*V,* →*, vI, vT , µ*) where *V* is a finite set of vertices, → is a binary relation over *V* (each element of → is a directed edge in *G*), *vI* is the initial vertex, *vT* is the terminal vertex and *µ* is a labelling function that maps each vertex to a basic MSC *m*.

When defining the semantics of MSC graphs, there are two ways of inter- preting the concatenation of basic MSCs *m*1 and *m*2. In *synchronous* con- catenation, all the events in *m*1 precede any event in *m*2. The *asynchronous* concatenation corresponds to concatenating two MSCs instance by instance.

Definition 2.2 The partial order associated with the *synchronous concate- nation* of two basic MSCs *m*1 and *m*2 having the same set of instances is the partial order ≤*m*1 *,m*2 on *locations*(*m*1) *locations*(*m*2) given by

≤*m*1 ≤*m*2 *locations*(*m*1) × *locations*(*m*2)

where *locations*(*m*) is the set of locations (events) in basic MSC *m* and is the operator for disjoint union of two sets. Each location is denoted by a tuple

*< i, l >* where *i* is the unique ID for an instance and *l* is the ID for the event within the visual order of the instance *i* (see Appendix [A](#_bookmark8)).

Definition 2.3 The partial order associated with the *asynchronous concate- nation* of two basic MSCs *m*1 and *m*2 having the same set of instances is the partial order ≤*m*1 *,m*2 on *locations*(*m*1) *locations*(*m*2) given by the transitive closure of the following relation:

≤*m*1 ≤*m*2 ∪ {(*< i, l >m*1 *,< i,l >m*2 ) | *< i, l >m*1 ∈ *locations*(*m*1)

∧ *< i, l >m*2 ∈ *locations*(*m*2)}

Figure [1](#_bookmark3) shows an MSC graph (initial vertex and terminal vertex are not shown, initial vertex connects to *v*0 and vertices *v*2, *v*3 connect to the terminal vertex). There are an infinite number of paths from the initial to the terminal vertex in this MSC graph (due to the loop in it); e.g., *< v*0*, v*1*, v*3 *>*, *< v*0*, v*1*, v*0*, v*1*, v*3 *>*, *< v*0*, v*1*, v*0*, v*1*, v*0*, v*1*, v*3 *>* etc. The idea in the semantics

v0 v2

s\_connect\_p1 r\_connect\_p2

P1 P2 P3

connect

r\_fail\_p1

v1

P1

v3

P2

P3

fail

report

s\_connect\_p1

s\_approve\_p2

r\_fail1\_p1

r\_connect\_p2

r\_approve\_p1

s\_fail\_p2 s\_req\_service\_p1

s\_report\_p2

r\_req\_service\_p3

r\_fail\_p1

r\_report\_p3

P1 P2 P3

req\_service

P1 P2 P3

approve

s\_fail\_p2 s\_report\_p2 r\_report\_p3

s\_connect1\_p1 r\_connect1\_p2

s\_fail1\_p2 s\_report1\_p2 r\_report1\_p3

s\_req\_service\_p1

r\_req\_service\_p3

Fig. 1. MSC Graph

of MSC graph with synchronous concatenation is to construct a partial order for each such finite path in the MSC graph, by synchronously concatenating the partial orders of the basic MSCs occurring in the path. Figure [1](#_bookmark3) also shows the precedence graph for walk *< v*0*, v*1*, v*0*, v*1*, v*3 *>* in the MSC graph; this graph is obtained by synchronously concatenating the basic MSCs for *v*0, *v*1, *v*0, *v*1 and *v*3. Dashed lines show the edges in the precedence graph added due to Definition [2.2](#_bookmark2); we omit the edges entailed by transitive closure. Note that the locations (events) are renamed in different instance of *v*0 (and *v*1) in the walk (due to the disjoint union).

Definition 2.4 The semantics of an MSC graph *G* under (a)synchronous con- catenation is the set of all finite and infinite runs obtained by (i) (a)synchronously concatenating each basic MSC along each walk in *G* and (ii) taking the dis- joint union over all walks, both finite and infinite, of the set of runs obtained from the partial orders in (i).

Since the set of walks in an MSC graph may be infinite (if it has loops), the set of runs is also infinite. The problem of deciding whether a given MSC-graph satisfies a given property *P* (where *P* is specified as an automaton) is coNP- complete for synchronous concatenation and is undecidable for asynchronous

concatenation [[3](#_bookmark13)].

* 1. *Linear Time Semantics of HMSC*

Essentially, an HMSC is a hierarchical (multi-level) graph whose nodes are either basic MSCs or another HMSC, thus allowing for nesting of graphs.

Definition 2.5 A *high-level message sequence chart (HMSC) H* is a tuple (*N, B, vI, vT , µ, E*) where *N* is a finite set of nodes, *B* is a finite set of boxes (or supernodes), *vI* ∈ *N* ∩ *B* is the initial node or box, *vT* ∈ *N* ∩ *B* is the terminal node or box, *µ* is a labelling function that maps each node in *N* to a basic MSC and each box in *B* to an already defined HMSC, and *E* is the set of edges that connect the nodes and boxes to each other.

We omit some HMSC features in the MSC standard such as conditions and inline expressions. What is the meaning of an HMSC *H*? First, an HMSC is *flattened* into an MSC-graph, obtained by recursively substituting a box by the corresponding HMSC. The meaning of an HMSC is then the set of all possible finite or infinite runs of this flattened MSC-graph [[3](#_bookmark13)]; here it is required that the nesting of HMSCs is not mutually recursive, i.e., if a node of an HMSC *H* is labelled with another HMSC *H*’, then a node of *H*’ cannot be labelled with *H* (or with any HMSC that refers to *H*).

# HMSC Verification

Given an HMSC *H* and a property *P* about the runs of *H*, the verification problem is to decide whether or not all runs of *H* satisfy *P* . *P* is typically stated using temporal logic like LTL or CTL, an automaton or a template MSC. A na¨ıve verification algorithm would examine some or all runs of *H* to decide whether or not *H* satisfies *P* . The general problem of deciding whether a given MSC-graph satisfies a given property *P* (where *P* is specified as an au- tomaton) is undecidable[[3](#_bookmark13)]. However, we consider some special classes of prop- erties below and present efficient verification algorithms for such properties. Following [[7](#_bookmark22),[15](#_bookmark25),[5](#_bookmark15)], we assume that the properties fall into various pre-defined templates, thus sacrificing generality for ease of use and efficiency of verifica- tion. The properties are stated in terms of the relative ordering of events in the runs of the input HMSC. Although the property templates cannot express all the properties that may be of interest in practice, they do cover broad classes of typical properties. Moreover, we present efficient graph-theoretic algorithms (based on linear time semantics of HMSCs) to verify properties stated using these templates.

Every internal event in an HMSC has a unique ID specified by the unique location in the basic MSC in which it occurs. A user-defined event *E* cor- responds in general to a non-empty finite set *γ*(*E*) of internal events in an HMSC. The properties are specified in terms of user-defined events and their negations. We say that a user-defined event occurs when any of the internal events corresponding to it occur. If a user-defined event *E* stands for a fi- nite set {*e*1*,... , ek*} of internal events, the negative event *not*(*E*) stands for *not*(*e*1) ∧ *...* ∧ *not*(*ek*). In Figure [1](#_bookmark3), a user event “*P* 2 sends message to *P* 1” (underscore stands for “don’t care”) corresponds to the two internal events: “*P* 2 sends *approve* to *P* 1” (in basic MSC *v*2) and “*P* 2 sends *f ail* to *P* 1” (in basic MSC *v*1).

Note

In this paper, all our verification algorithms assume *synchronous* concate- nation in the semantics of MSC graphs. This makes the algorithms modular in nature, in the sense that the verification task can be decomposed into smaller tasks that can be verified at each vertex. The verification algorithms for asyn- chronous concatenation are more complex, as they involve the construction of a global reachability graph from the input MSC graph.

* 1. *Tracing*

A tracing property asserts the occurrence of a sequence of events in the spec- ified order in all runs or in at least one run of the input HMSC *H*. The template for the property is shown below. The terms in bold are given by the user and the terms in bold separated by slashes are alternatives, one of which is chosen by the user. Each *ai* is a user-defined event or its negation.

The sub-sequence of events *X* = [a1*,* a2*,... ,* ak] occurs in some / all

runs of HMSC *H*

Definition 3.1 Let *H* be an MSC-graph and let *σ* = *u*0*, u*1*, u*2*,...* be a run of *H*, where each *ui* is an internal event in some MSC *M* in *H*. A positive user-defined event *a* = {*e*1*,... , en*} occurs in *σ* if there is some event *e* ∈ *a* and some *ui* (*i* ≥ 0) in *σ* such that *ui* = *e*. A negative user-defined event *not*(*a*), where *a* is a positive user-defined event *a* = {*e*1*,... , en*} occurs in *σ* if *a* does not occurs in *σ* i.e., if there is no *e* ∈ *a* and there is no *ui* (*i* ≥ 0) in *σ* such that *ui* = *e*.

The concept of a user-defined event occurring in a run can be generalised for a sequence *X* = [*a*1*, a*2*,... , an*] of (positive or negative) user-defined events by means of the inductive Definition [3.2](#_bookmark5).

Definition 3.2 Let *H* be an MSC-graph and *σ* = *u*0*, u*1*, u*2*,...* be a run of *H*, where each *ui* is an internal event in some MSC *M* in *H*. A trace *X* = [*a*1*, a*2*,... , an*] of (positive or negative) user-defined events occurs in *σ* if *x* is a prefix of *σ*, where *σ* = *x* • *y*, such that *a*1 occurs in *x* and the remaining trace [*a*2*,... , an*] occurs in the remaining run *y*. Here, • is the concatenation operation on sequences.

Suppose that all events in *X* are positive user-defined events. Then *X occurs* in a given finite or infinite run *σ* of an HMSC *H* if there is a sequence of internal events *B* =*< b*1*,... , bk >* such that *bi* ∈ *ai* and *B* occurs as a sub-sequence within *σ*. The sub-sequence *B* need not be contiguous in *σ* i.e., there may be other events between *bi* and *bi*+1 in *σ*. Now suppose one or more *ai*’s in *X* are negative user-defined events. Then *X occurs* in a given finite or infinite run *σ* of an HMSC *H* if there is a sequence of internal events *b*1*,... , bn* (where *n* = number of positive user-defined events in *X*) such that (i) each *bi* is in a positive user-defined event in *X* (in the order in which they occur in *X*) and (ii) *B* is a sub-sequence within *σ* and (iii) for every pair *bi* and *bi*+1 in *B* such that *bi* ∈ *ap* and *bi*+1 ∈ *aq*, no internal event from any negative user-defined events between *ap* and *aq* occurs between *bi* and *bi*+1 in *σ*.

For example, the following property checks if there is any run in the HMSC of Figure [1](#_bookmark3) where *P* 1 sends a *connect* message but *P* 1 does not receive an *approve* message after that. A chief difficulty in checking tracing properties is that the runs of *H* may be infinite.

The sub-sequence of events *X* = [“*P* 1 sends *connect* to ”,

not(“*P* 1 receives *approve* from )”] occurs in some runs of HMSC *H*.

The sequence *X* may include some *cycles* or repetitions, as shown below.

For simplicity, we assume that *X* does not include any negative user- defined events. Let *H* be an HMSC and let *S* be the set of basic MSCs that occur in the MSC-graph obtained by *flattening H*. For a given internal event *e*, let *φ*(*e*) denote the set of basic MSCs from *S* in which *e* occurs (clearly *φ*(*e*) /= ∅ and *φ*(*e*) ⊆ *S*); e.g., *φ*(“*P* 2 sends to *P* 1”) = {*v*1*, v*2}. Recall that a positive user-defined event *E* stands for a finite set *γ*(*E*) = {*e*1*,... , ep*} of

The sub-sequence of events *X* = [“*P* 1 sends *connect* to ”, “*P* 1 receives *f ail* from ”, “*P* 1 sends *connect* to ”)]

occurs in some runs of HMSC *H*.

internal events. Then the function *φ* can be extended to a user defined event

*E* as: *φ*(*E*)=

*e*∈*γ*(*E*)

*φ*(*e*) = *φ*(*e*1) ∪ *...* ∪ *φ*(*ep*). Here, *φ*(*E*) denotes the set

of basic MSCs in *H*, which contains at least one internal event corresponding

to the user-defined event *E*. We need some more definitions.

Definition 3.3 Let *H* be an MSC-graph over a set *S* of basic MSCs. Then a sequence *w* =*< M*1*, M*2*,... , Mm >* of basic MSCs in *S* is called feasible trace of *H* if (i) there is a directed path from initial node to *M*1 and (ii) from each *Mi* to *Mi*+1 (1 ≤ *i < m*) and (iii) from *Mm* to terminal node of *H*. Specifically,

(ii) needs to hold even if *Mi* = *Mi*+1 for any *i*, in which case there must be a non-empty directed path from *Mi* to itself (i.e., *Mi* should be reachable from itself via a non-empty directed path).

Definition 3.4 Let *β* =*< e*1*, e*2*,... , ek >* be a sequence of positive internal events in a set of basic MSCs *S* in an MSC-graph *H*. Let *w* =*< M*1*, M*2*,... , Mm >* be a feasible trace of *H*. Then a function *f* partitions *β* among *w* if (i)

*f* (*M* ) = *β* if *w* consists of a single MSC *M* and *M* ∈ *φ*(*β*); and (ii) if *w* =*< Mi >* • *< Mi*+1*,... , Mm >* then *f* (*Mi*) = a proper prefix *x* of *β* (where *β* = *x* • *y*), *Mi* ∈ *φ*(*x*) and *f* partitions *y* among *< Mi*+1*,... , Mm >*.

In Figure [1](#_bookmark3), *w* =*< v*1*, v*3 *>* is a feasible trace of *H*. Also, a function *f* = {*v*1 '→*< r f ail p*1*,r report p*3 *>, v*3 '→*< s req service p*1 *>*} partitions *β* =*< r f ail p*1*,r report p*3*,s req service p*1 *>* among *w*. Note that *f* asso- ciates a non-empty subsequence of *β* with every basic MSC in *w*. A simple algorithm can be designed to construct a function *f* that partitions given *β* among *w*; such an *f* is unique for given *w*, *β* because every event in *β* is an internal event, which belongs to a unique basic MSC in *S*.

Algorithm 1 (*hmsc tracing a*) to check properties stated using the tracing template is as follows (we assume the option some is chosen). We system- atically select a permutation *β* of internal events from *X*, form a candidate feasible trace *w* in *H* which partitions *β* through a function *f* and efficiently check whether *β* occurs in some linearization of the precedence relation corre- sponding to the synchronous concatenation of basic MSCs in *w*. The feasible traces have a length of at most *k* and hence are finite in number.

For the second property above, *X* = [*a*1*, a*2*, a*3] where *a*1 = “*P* 1 sends *connect* to ”, *a*2 = “*P* 1 receives *f ail* from ”, *a*3 = “*P* 1 sends *connect* to ”. *φ*(*a*1) = *φ*(*a*3) = {*v*0}, *φ*(*a*2) = {*v*1} and *γ*(*a*1) = *γ*(*a*3) = {*s connect p*1}, *γ*(*a*2)= {*r f ail p*1}. There is only one possible *β* =*< e*1*, e*2*, e*3 *>* where *e*1= *s connect p*1, *e*2 = *r f ail p*1, *e*3 = *s connect p*1. Thus *w* =*< M*1*, M*2*, M*3 *>* where *M*1 = *v*0, *M*2 = *v*1, *M*3 = *v*0 is a feasible trace of *H* and the function *f* = {*M*1 '→*< s connect p*1 *>, M*2 '→*< r f ail p*1 *>, M*3 '→*< s connect p*1 *>*} partitions *β* among *w*. This choice of *β*, *w* and *f* clearly satisfies the inner for loops; hence the property is satisfied. Clearly, the following property is not satisfied by the HMSC in Figure [1](#_bookmark3).

Algorithm 1 *hmsc tracing a*

input HMSC *H*; {actually *H* is the flattened MSC-graph for an HMSC} input *X* = [*a*1*,... , ak*] {finite sequence of positive user-defined events} output true if *X* occurs in some run of *H*; false otherwise

Let *S* = the set of all basic MSCs in *H*;

Let *φ*(*ai*) = the set of basic MSCs for user-defined events *ai*; Let *γ*(*ai*) = the set of internal events in user-defined event *ai*; for every sequence *β* =*< e*1*, e*2*,... , ek >* where *ei* ∈ *γ*(*ai*) do

for every feasible trace *w* =*< M*1*, M*2*,... , Ma >*, 1 ≤ *a* ≤ *k*,

of basic MSCs from *S* such that a function *f* partitions *β* among *w* do

*ok* = true;

{do for every MSC in w}

for (*x* = 1; *ok* == true && *x* ≤ *a*; *x*++) do

for (*j* = 2; *ok* == true && *j* ≤ |*f* (*Mx*)|; *j*++) do for (*i* = 1; *i < j*; *i*++) do

{*RM* = precedence order for MSC *M* }

if precedes(*RMx* ,*f* (*Mx*)*j*,*f* (*Mx*)*i*) then

ok = false; break;

end if end for

end for end for

if ok == true then

return(true); end if

end for end for return(false);

The sub-sequence of events *X* = [“*P* 1 sends *req service* to ”,

“*P* 1 sends *connect* to ”] occurs in some runs of HMSC *H*.

The complexity of the Algorithm 1 is easily seen to be *O*(*Ak* · *mk*) where *A* is the maximum number of internal events corresponding to any event *ai* in *X* (i.e., *A* = *max*{*γ*(*ai*)}), *m* is the number of basic MSCs in *H* and *k* is the number of events in *X*. To reduce the complexity, we enforce an upper bound of *k*=10, which means that one can use up to 10 user-defined events to state the tracing property, which is acceptable in practice. The earlier work reported in [[5](#_bookmark15)] presents a similar algorithm for basic MSCs and

its analysis. The algorithm is efficient and does not explicitly check all possible finite linearizations of *H*. Clearly, the approach works even when there are *repetitions* in *X*. The algorithm can be modified for the situations (a) when *X* contains negative user-defined events; and/or (b) the option all runs is chosen in the template. For better use in practice, we have extended this approach to provide additional options such as *packed* subsequences, position of the tracing (only at the beginning, only at the end or anywhere) within the linearization etc.

* 1. *Consequence*

Another useful kind of property is specified using the following template. Here *X* = {*x*1*,... , xm*}, *Y* = {*y*1*,... , yn*} are given sets of user-defined events or their negations.

Each / An / All events from *X* leads to an / all events from *Y*

in all runs of HMSC *H*

For example, in Figure [1](#_bookmark3), the following property states that each occurrence of *P* 1 sending a *connect* message is followed by *P* 1 receiving either *f ail* or *approve* message in every run.

As another example, in Figure [1](#_bookmark3), the following property states that each occurrence of *P* 1 sending a *connect* message or *P* 1 receiving a *f ail* mes- sage is followed by either *P* 1 receiving an *approve* message or *P* 1 sending a *req service* message in every run.

Each events from [“*P* 1 sends *connect* to ”, “*P* 1 receives *f ail* from ”] leads to an events from [“*P* 1 receives *approve* from ”,

“*P* 1 sends *req service* to ”] in all runs of HMSC *H*

An events from [“*P* 1 sends *connect* to ”] leads to an events from [“*P* 1 receives *f ail* from ”, “*P* 1 receives *approve* from ”]

in all runs of HMSC *H*

For simplicity, we again assume that *X*,*Y* do not include any negative user-defined events. Given a set of sets *X*, we use *X* to denote the union of the sets in *X*; e.g., {{*a, b*}*,* {*a, c*}} = {*a, b, c*}. The meaning of this property template is as follows:

* + - Property pattern: An event from *X* leads to an event from *Y* in all runs of HMSC *H*. Meaning: Is it true that for every run *σ* of *H*, if there exists some internal event *x* ∈ *X*, then there exists some internal event

*y* ∈ *Y* such that the sequence *x* • *y* (obtained by concatenating *x* and

*y*) is a sub-sequence (tracing) of *σ*?

* + - Property pattern: An event from *X* leads to all events from *Y* in all runs of HMSC *H*. Meaning: Is it true that for every run *σ* of *H*, if there exists some internal event *x* ∈ *X* then some ordered permutation *Y* 1 of internal events in *Y* exists such that the sequence *x* • *Y* 1 is a sub-sequence (tracing) of *σ*?

* + - Property pattern: Each event from *X* leads to an event from *Y* in all runs of HMSC *H*. Meaning: Is it true that for every run *σ* of *H*, and for every user event *a* ∈ *X*, if there exists some internal event *x* ∈ *a* then there exists some internal event *y* ∈ *Y* such that the sequence *x* • *y* is a sub-sequence (tracing) of *σ*?

* + - Property pattern: Each event from *X* leads to all events from *Y* in all runs of HMSC *H*. Meaning: Is it true that for every run *σ* of *H* and every user event *a* ∈ *X*, if there exists some internal event *x* ∈ *a* then there exists some ordered permutation *Y* 1 of internal events in *Y* such that the sequence *x* • *Y* 1 is a sub-sequence (tracing) of *σ*?

The meaning is defined similarly when the first “all events” option is cho- sen. We illustrate the approach by an algorithm to verify the second conse- quence property pattern listed above.

Definition 3.5 Let *H* be an MSC-graph. Let *A* be a positive user-defined event in *H* and let *B* be a collection of positive user-defined events in *H*. We say that *A must be followed by* some event in *B*, denoted *A* ► *B*, if for every finite run *σ* = *e*1*, e*2*,...* of the MSC graph *H*, if some internal event *a* ∈ *A*

occurs in *σ* (i.e., *ei* = *a* for some *i* ≥ 1) then (i) there is some event *b* ∈ *B*

such that *ej* = *b* for some *j > i* (i.e., *a* is followed by *b*); and (ii) *A* ► *B* is true in the remaining run *ei*+1*, ei*+2*,... , ej*−1*, ej*+1*,...* of *σ*.

Remark 3.6

* If no event from *A* occurs in some run of an HMSC *H* then *A* ► *B* is vacuously true for that run.
* If two internal events *e*1 and *e*2 belong to the same bMSC (i.e., *φ*(*e*1)= *φ*(*e*2)

= *M* ) then {*e*1} ► {{*e*2}} iff *e*1 *precedes e*2 in the precedence order of *M* .

For example, in Figure [1](#_bookmark3), it is easy to see that {“*P* 1 send *connect* to *P* 2”}

* + {“*P* 2 send *approve* to *P* 1”’, “*P* 2 send *f ail* to *P* 1”}.

Proposition 3.7 *Let H be an MSC-graph, let A be a positive user-deﬁned event and let B be a non-empty collection of positive user events over H. Then A* ► *B if and only if for every internal event e*1 *in A, there exists an*

*internal event e*2 *in* *B with φ*(*e*1) *= M*1*, φ*(*e*2) *= M*2*, such that the following holds:*

* *if M*1 *= M*2 *= some bMSC M then e*1 *precedes e*2 *in the partial order asso- ciated with M; or*
* *if M*1 /= *M*2 *then*

1. *M*1 *is reachable from the start vertex of H, and*
2. *M*2 *is reachable from M1, and*
3. *the end vertex of H is reachable from M*2*, and*
4. *every directed path from the start vertex of H to the end vertex of H that passes through M*1 *also passes through M*2 *after M*1*.*

The following Algorithm 2 implements Proposition [3.7](#_bookmark6) to check whether a given vertex *u* is always followed by another vertex *v* in a graph *G*, with respect to special given *start* and *end* vertices in *G*. The algorithm essentially performs a recursive depth-first traversal of the graph *G* starting at *u* and *backtracking* when reaching any vertex in *L*. The global array *visited* of Boolean flags marks vertices already visited.

The complexity of Algorithm 2 is clearly the same as depth-first graph traversal viz. *O*(|*V* | + |*E*|). The Algorithm 3 for the consequence property (where the second consequence property pattern listed above is chosen) is as follows.

If each user-defined event in the set *Y* contains at most *N* internal events then the algorithm explicitly checks *N* |*Y* | combinations. We manage this ex- ponential complexity by putting an upper limit on the number of events in *Y* (e.g., |*Y* | ≤ 10), which is satisfactory in practical situations. The earlier pa- per [[5](#_bookmark15)] presents similar algorithms for basic MSCs and their analysis. Again, the algorithm is efficient and does not explicitly check all possible finite lin- earizations of *H*. The algorithm can be modified when *X* or *Y* contain negative user-defined events. In addition to tracing and consequence, we have defined other templates to specify more varieties of properties, such as precedence.

# Conclusions and Further Work

The approach of specification of system behaviour by listing examples of its interactions is gaining widespread acceptance during the requirements anal- ysis phase. Consequently, it is important to analyse scenario-based system specifications to detect errors early and to gain confidence in the system re- quirements. Message sequence charts and high-level message sequence charts provide a rigorous yet intuitive formalism for specifying such scenarios of sys-

Algorithm 2 *must followed by*

global Boolean *visited*[|*V* |]; {initially each vertex in *V* is unvisited}

input Graph *G* = (*V, E*);

input start, end, *u* ∈ *V* ; {*u* /= *start*,*u* /= *end*}

input *L* ⊆ *V* \ {*start, end, u*};

output true if *u* is always followed by at least one vertex in *L* on any path from start to end through *u*; false otherwise

{assumed: *u* is reachable from start and end is reachable from *u*}

visited[*u*] = true; {mark *u* as visited}

for every vertex *w* adjacent to *u* do

if (visited[*w*] || *w* == start || *w* ∈ *L*) then continue

end if

if (*w* == end) then

return(false); {*u* not always followed by a vertex in *L*}

end if

visited[*w*] = true; {mark *w* as visited}

if (*must followed by*(*G, start, end, w, L*) == false) then return(false);

end if end for

return(true);

tem behaviour. HMSCs provide hierarchical and modular composition facili- ties for constructing complex scenarios from basic ones. Other notations like UML use cases and sequence diagrams have been given formal semantics in terms of HMSCs. Several techniques have been proposed for analysing scenar- ios specified by HMSC: race condition detection, missing scenario detection and formal verification. Although the general problem of formal verification of properties of HMSCs is intractable, in this paper, we proposed simple and intuitive templates for stating common types of properties and algorithms for verifying them.

For further work, we need to add more templates for expanding the kinds of properties that can be specified. We can look at using a restricted linear temporal logic (or regular expressions) for stating properties of HMSCs. The approach certainly restricts the expressive power for stating properties of HM- SCs but might gain in efficiency of verification algorithms. Verification of live sequence charts [[6](#_bookmark16)] or MSCs with timing properties [[26](#_bookmark36)] is also of considerable interest.

Algorithm 3 *hmsc consequence b*

input HMSC *H*; {actually *H* is the flattened MSC-graph for an HMSC} input finite non-empty sets *X*, *Y* of positive user-defined events in *H* output true if for all runs *σ* of *H*, there exists an internal event *x* in *X* and some permutation *Y* 1 of internal events in *Y* such that *σ* contains *x* • *Y* 1 as a sub-sequence (tracing); false otherwise

{*γ*(*a*) denotes the set of internal events in user-defined event *a*}

for (*i* = 1; *i* ≤ |*X*|; *i*++) do

{check every internal-event in *X*}

for every internal event *x* ∈ *γ*(*X*[*i*]) do

{Is *x* followed by all elements of *Y* in some order in all runs of *H*?}

for every combination *L* =*< e*1*, e*2*,... , e*|*Y* | *>* s.t. *ei* ∈ *γ*(*Y* [*i*]) do if (*φ*(*x*)==*φ*(*ei*) for every *ei* ∈ *L*) then

if (*x precedes ei* for every *ei* in *L*) then return(true);

end if else

if (*must followed by*(*H, startH, endH, x, L*)) then

return(true); {*x* is the one}

end if end if

end for

end for{*x* is not followed by all elements of *Y* }

end for return(false);

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# Linear Time Semantics of Basic MSC Notation

* 1. *The Basic MSC Notation*

We present here only some essential features in the MSC notation; we omit features like actions, inline expressions and gates etc. In an MSC, entities stand for instances (or processes) and events typically represent sending and receiving of messages by processes - there are also other kinds of events such

Fig. A.1. Basic MSC

**msc** Steam Boiler

valve

pressure gauge

status available

t1

status close

status high pressure

controller

t1

cmd open

as those related to timers. The meanings of an instance and a message depend on the system being described. An instance does not necessarily stand for a computer program; it refers to some active agent or entity. A message does not necessarily represent an actual data message; it may refer to some kind of exchange of information. A message has a name and no further structure or details (in the simplest case). An MSC is not concerned with the actual mechanism or channels of the message transmission, except to assume that the messages are always delivered in the order without any loss or corrup- tion. Further, the send is non-blocking i.e., the sender does not wait until the receiver receives the message.

Figure [A.1](#_bookmark9) shows an MSC that depicts a simple interaction among 3 in- stances named *valve*, *controller* and *pressure gauge*. Each vertical line for an instance denotes the events that happen within the instance; the topmost event is the earliest event (within the instance) and so on downwards in time. This temporal ordering of events for an instance is called its *local order*. The visual distance between the events within a local order is immaterial. In Fig- ure [A.1](#_bookmark9), the *valve* and *pressure gauge* processes send their respective status to the *controller* using messages named *status close* and *status high pressure* respectively. The *controller* then issues an open command to the *valve* using the message named *cmd open*. Note that the MSC depicts only one scenario; many other scenarios are possible e.g., one where the valve is open, pressure is low and the controller issues command to close the valve. The MSC is enclosed in a rectangular frame and has a name (*Steam Boiler* in this example).

The MSC notation allows one to avoid any particular ordering of a subset of events for a process, by using a construct called co-region. A *co-region* is indicated by a dotted line segment along the vertical line for the process and the events within this dotted line are unordered. Figure [A.1](#_bookmark9) shows a

co-region for *controller* in which the *controller* does not impose any order on the receipt of the *status close* message from *valve* and *status high pressure* message from *pressure gauge* (these messages may be received in any order).

Many scenarios relate message flows with timing constraints. It is possible to easily specify such scenarios in the MSC notation using three special events: timer set, timer reset and timeout. The *timer set* event is denoted by an hourglass symbol connected to the timeline of a single process. The *timer reset* event is denoted by a cross connected to the timeline of a process. The *timeout* event is denoted by connecting the hourglass symbol of the timer to the process timeline by a bent line. Each timer has a unique name. Naturally, for each timer, the timer reset and timeout events must be preceded by the timer set event. In Figure [A.1](#_bookmark9), the controller starts a timer *t*1 before waiting for the messages from *valve* and *pressure gauge* instances. In this particular scenario, the *controller* receives both the messages before the timeout and then resets the timer *t*1.

A *condition* is an informal descriptive mechanism in the MSC notation used to display a state or situation that must be reached by either a single instance or a group of instances. A condition is written as a text label within a hexagonal box, which is placed either on a single instance or across a group of instances. If a condition *C* is placed on a single instance *P* then the execution of *P* does not proceed to the next event (below the condition on the local order) until the condition is reached. That is, the condition *C* is a pre-requisite for the next event in the instance. If the condition *C* is placed across a group of instances *P*1*,... , Pk*, then all the *k* instances must achieve local states in which the condition *C* is satisfied; only after that state is reached, can any of the *k* instances proceed further in their respective local orders. In such a case, the condition *C* can be thought of a synchronisation mechanism useful to ensure that the instances *P*1*,... , Pk* reach the same state before proceeding further. In Figure [A.1](#_bookmark9), instances *valve* and *pressure gauge* share a condition called *status available*; only when both these instances reach a state that satisfies this condition, can they proceed further with their local orders.

What exactly is the scenario specified by the basic MSC in Figure [A.1](#_bookmark9)? It may appear that the MSC represents only one sequence of events; in fact it represents several actual event sequences each of which *captures* the intended behaviour. To understand this clearly, we formally define the linear time semantics of a basic MSC.

* 1. *Linear Time Semantics of Basic MSCs*

The MSC notation discussed so far is called a basic MSC to distinguish it from the HMSC defined later. We now formally define the linear time semantics of

a basic MSC in terms of the associated partial order and the set of runs [[2](#_bookmark12),[6](#_bookmark16),[5](#_bookmark15)]. With each instance *i* in a given basic MSC *m*, we associate an ordered sequence 0*..lmax*(*m, i*) of finite number of discrete locations, which are numbered from the top of the instance to the bottom. Each location on an instance *i* in basic MSC *m*, denoted *< i, l >m*, is associated with an event, which may be sending or receiving of a message, a condition, or timer events set, reset, timeout. We drop the subscript *m* when the basic MSC is clear from the context. The semantics of a basic MSC *m* is defined in terms of the partial order ≤*m* induced by *m* on the set of its locations *< i, l >*. The partial order is obtained from the following precedence relation *Rm*:

* + - visual order along an instance line: *< i, l > Rm < i, l* +1 *>* unless the two consecutive locations are in a co-region
    - send of a message precedes its receipt: if *< i, l >* is a send event and

*< i*'*, l*' *>* is the corresponding receive event for the message, then *< i, l > Rm < i*'*, l*' *>*

* + - shared condition induces synchronisation barrier: if locations *< i, l >*

and *< i*'*, l*' *>* refer to the same condition *c*, then *< i, l > Rm < i*'*, l*' +1 *>*

* + - events within a co-region have no order among them: suppose *< i, l >, < i, l* +1 *>,... ,< i,l* + *k >* are events in a co-region. If *l >* 0 (i.e., there is at least one event before the co-region) then *< i, l* − 1 *> Rm < i, l >*,

*< i, l* − 1 *> Rm < i, l* +1 *>*, ... ,*< i, l* − 1 *> Rm < i, l* + *k >*. Also, if *k < lmax*(*i, m*) (i.e., there is at least one event after the co-region) then *< i, l > Rm < i, l* + *k* +1 *>*,*< i, l* +1 *> Rm < i, l* + *k* +1 *>*,

... ,*< i, l* + *k > Rm < i, l* + *k* +1 *>*.

We assume that the basic MSC *m* is well-formed so that the relation *Rm* is acyclic. We call *Rm* the *precedence relation* of the basic MSC *m*. The partial order ≤*m* is the reflexive transitive closure of *Rm*.

Definition A.1 The semantics of a basic MSC *m* is the set of all runs (i.e., linearizations) of the partial order ≤*m*.

This implies that we have interleaving semantics for concurrency: any two events that are incomparable in the partial order can happen in any order. Note also that each run of a basic MSC is a finite sequence of events and each basic MSC has only a finite number of such finite runs. The precedence graph in Figure [A.2](#_bookmark10) depicts the precedence relation *Rm* among the locations (events) in the basic MSC of Figure [A.1](#_bookmark9). The vertices of this precedence graph stand for events; there is a directed edge from event *u* to event *v* if *u* directly precedes *v* i.e., *uRmv*. An event *v* cannot *occur* until all preceding events have occurred. If the MSC is well-formed then the corresponding precedence graph

e6



e5 e1 = valve sends status\_close to controller

e2 = controller receives status\_close from valve

e3 = pressure\_gauge sends status\_high\_pressure to controller

e4 = controller receives status\_high\_pressure from pressure\_gauge e5 = controller sends cmd\_open to valve

e8 e6 = valve receives cmd\_open from controller e7 = set timer t1

e8 = reset timer t1

c1 = condition correct\_status\_available

e4

e2

e1 e7 e3

c1

Fig. A.2. Precedence graph for the basic MSC in Figure [A.1](#_bookmark9)

is a directed acyclic graph (DAG).

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