1 Single bit-flip temporel

Table 1: Logical fault injection simulation campaigns results

	Crash	NSTR	Delay	Detection	Success	Total
Buffer Overflow	0	238633	1143	0	2159 (0.89%)	241935
WU-FTPd	0	429260	12192	0	10160 (2.25%)	451612
Compare/Compute	0	94488	381	0	1905 (1.97%)	96774

Table 2: Logical fault injection simulation campaigns results

	Crash	NSTR	Delay	Detection	Success	Total
Buffer Overflow	0	0	0	261360	0 (0.00%)	261360

Table 3: Logical fault injection simulation campaigns results

	Crash	NSTR	Delay	Detection	Success	Total
Buffer Overflow	0	346560	0	0	0 (0.00%)	346560
WU-FTPd	0	646912	0	0	0 (0.00%)	646912
Compare/Compute	0	138624	0	0	0 (0.00%)	138624