| | alu_operand_a_ex_o_tag | alu_operand_b_ex_o_tag | alu_operand_c_ex_o_tag | alu_operator_o_mode | check_d_o_tag | check_s1_o_tag | check_s2_o_tag | is_store_post_o_tag | memory_set_o_tag | pc_id_o_tag | pc_if_o_tag | regfile_alu_waddr_ex_o_tag | register_set_o_tag | rf_reg[0] | rf_reg[1] | store_dest_addr_ex_o_tag | store_source_ex_o_tag | use_store_ops_ex_o |
|---|---|---|---|---|---|---|---|---|---|---|---|---|---|---|---|---|---|---|
| alu_operand_a_ex_o_tag | | | | | | | | | | | 2 | | | | | | | |
| alu_operand_b_ex_o_tag | | | | | | | | | | | 2 | | | | | | | |
| alu_operand_c_ex_o_tag | | | | | | | | | | | 2 | | | | | | | |
| alu_operator_o_mode | | | | | | | | | | | 4 | | | | | | | |
| check_d_o_tag | | | | | | | | | | | 2 | | | | | | | |
| check_s1_o_tag | | | | | | | | | | | 2 | | | | | | | |
| check_s2_o_tag | | | | | | | | | | | 2 | | | | | | | |
| data_type_q_tag | | | | | | | | | 4 | | 4 | | | | 4 | | | |
| data_we_q_tag | | | | | | | | | 2 | | 2 | | | | 2 | | | |
| is_store_post_o_tag | | | | | | | | | | | 2 | | | | | | | |
| memory_set_o_tag | 1 | 2 | 2 | 4 | 2 | 2 | 2 | 2 | | 1 | 3 | | | | | | | |
| pc_if_o_tag | | | | | | | | | | 3 | | | | | | | | |
| rdata_offset_q_tag | | | | | | | | | 4 | | 4 | | | | 4 | | | |
| rdata_q_tag | | | | | | | | | 16 | | 16 | | | | 16 | | | |
| regfile_alu_waddr_ex_o_tag | | | | | | | | | 32 | | 32 | | | | | | | |
| register_set_o_tag | | | | | | | | | 2 | | 2 | | | | | | | |
| rf_reg[0] | | | | | | | | | 1 | | 1 | | | | | | | |
| rf_reg[10] | | | | | | | | | 2 | | 2 | | | | 2 | | | |
| rf_reg[11] | | | | | | | | | 2 | | 2 | | | | 2 | | | |
| rf_reg[12] | | | | | | | | | 2 | | 2 | | | | 2 | | | |
| rf_reg[13] | | | | | | | | | 2 | | 2 | | | | 2 | | | |
| rf_reg[14] | | | | | | | | | 2 | | 2 | | | | 2 | | | |
| rf_reg[15] | | | | | | | | | 2 | | 2 | | | | 2 | | | |
| rf_reg[16] | | | | | | | | | 2 | | 2 | | | | 2 | | | |
| rf_reg[17] | | | | | | | | | 2 | | 2 | | | | 2 | | | |
| rf_reg[18] | | | | | | | | | 2 | | 2 | | | | 2 | | | |
| rf_reg[19] | | | | | | | | | 2 | | 2 | | | | 2 | | | |
| rf_reg[1] | 1 | 1 | 2 | 4 | 2 | 2 | 2 | 2 | 4 | 1 | 4 | 32 | 1 | 1 | | 2 | 2 | 2 |
| rf_reg[20] | | | | | | | | | 2 | | 2 | | | | 2 | | | |
| rf_reg[21] | | | | | | | | | 2 | | 2 | | | | 2 | | | |
| rf_reg[22] | | | | | | | | | 2 | | 2 | | | | 2 | | | |
| rf_reg[23] | | | | | | | | | 2 | | 2 | | | | 2 | | | |
| rf_reg[24] | | | | | | | | | 2 | | 2 | | | | 2 | | | |
| rf_reg[25] | | | | | | | | | 2 | | 2 | | | | 2 | | | |
| rf_reg[26] | | | | | | | | | 2 | | 2 | | | | 2 | | | |
| rf_reg[27] | | | | | | | | | 2 | | 2 | | | | 2 | | | |
| rf_reg[28] | | | | | | | | | 2 | | 2 | | | | 2 | | | |
| rf_reg[29] | | | | | | | | | 2 | | 2 | | | | 2 | | | |
| rf_reg[2] | | | | | | | | | 1 | | 1 | | | | 2 | | | |
| rf_reg[30] | | | | | | | | | 2 | | 2 | | | | 2 | | | |
| rf_reg[31] | | | | | | | | | 2 | | 2 | | | | 2 | | | |
| rf_reg[3] | | | | | | | | | 2 | | 2 | | | | 2 | | | |
| rf_reg[4] | | | | | | | | | 2 | | 2 | | | | 2 | | | |
| rf_reg[5] | | | | | | | | | 2 | | 2 | | | | 2 | | | |
| rf_reg[6] | | | | | | | | | 2 | | 2 | | | | 2 | | | |
| rf_reg[7] | | | | | | | | | 2 | | 2 | | | | 2 | | | |
| rf_reg[8] | | | | | | | | | 2 | | 2 | | | | 2 | | | |
| rf_reg[9] | | | | | | | | | 2 | | 2 | | | | 2 | | | |
| rs1_o_tag | | | | | | | | | 2 | | 2 | | | | 1 | | | |
| store_dest_addr_ex_o_tag | | | | | | | | | 2 | | 2 | | | | | | | |
| store_source_ex_o_tag | | | | | | | | | 2 | | 2 | | | | | | | |
| use_store_ops_ex_o | | | | | | | | | 2 | | 2 | | | | | | | |