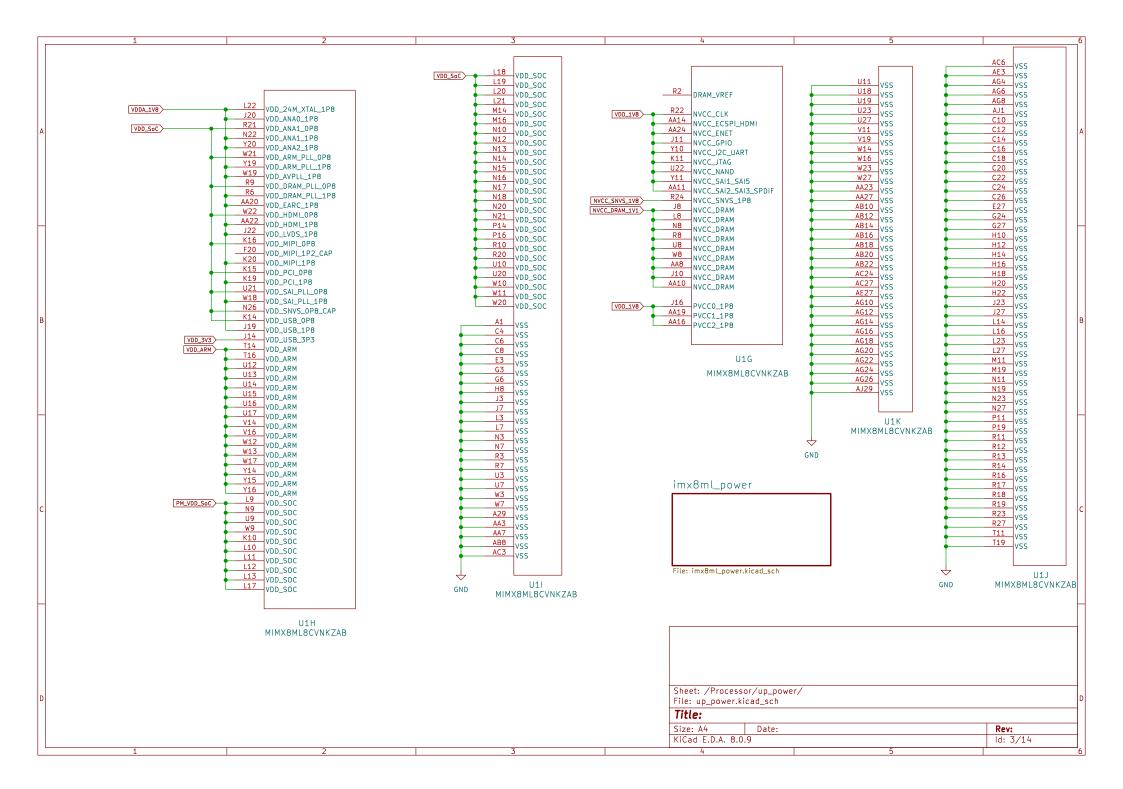
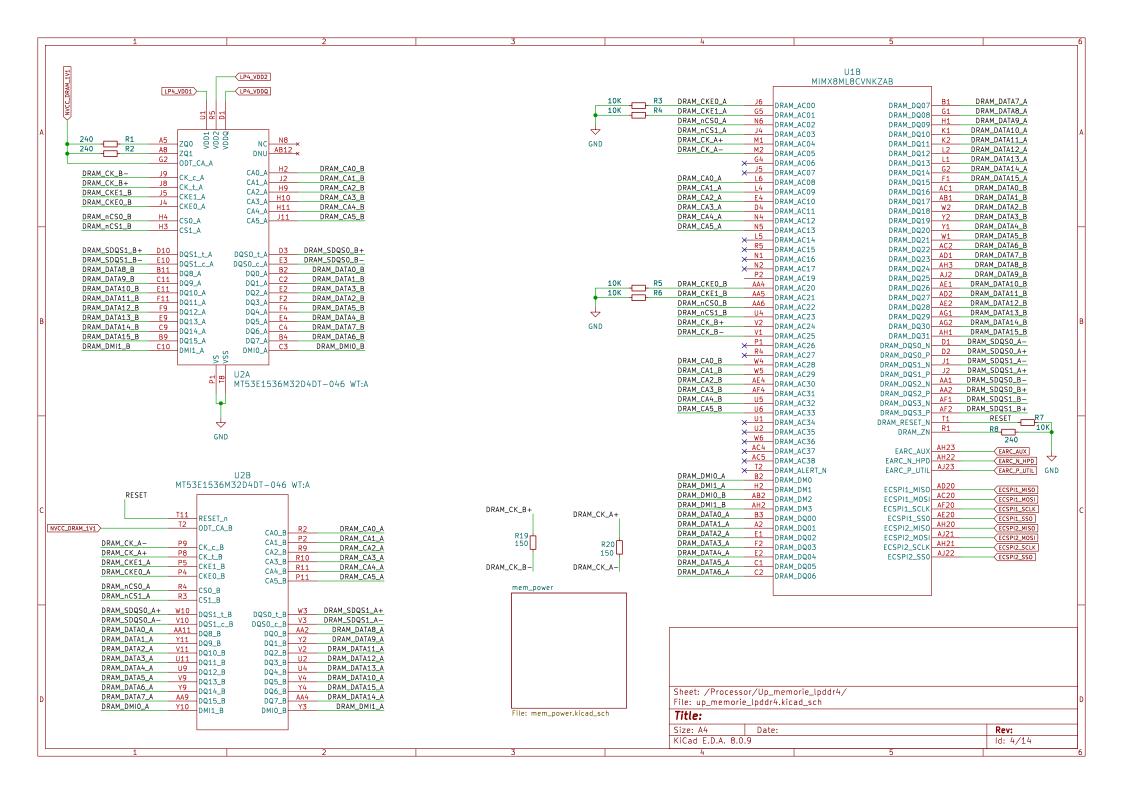
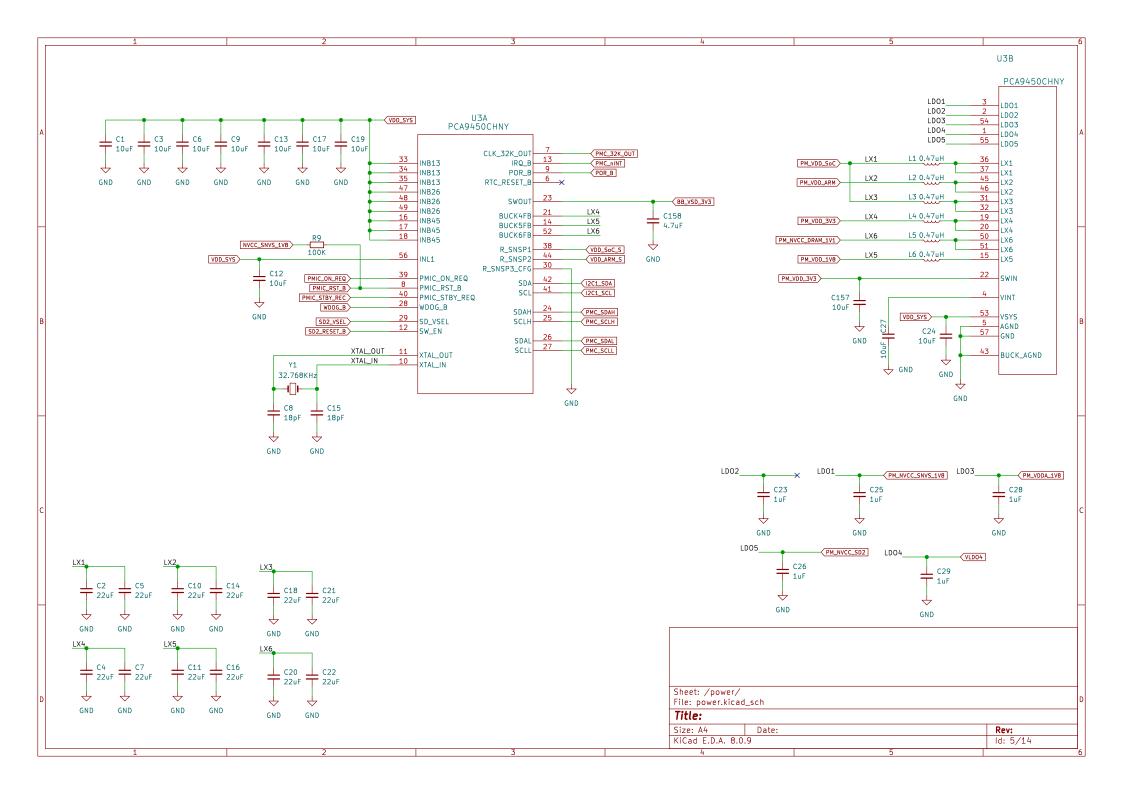
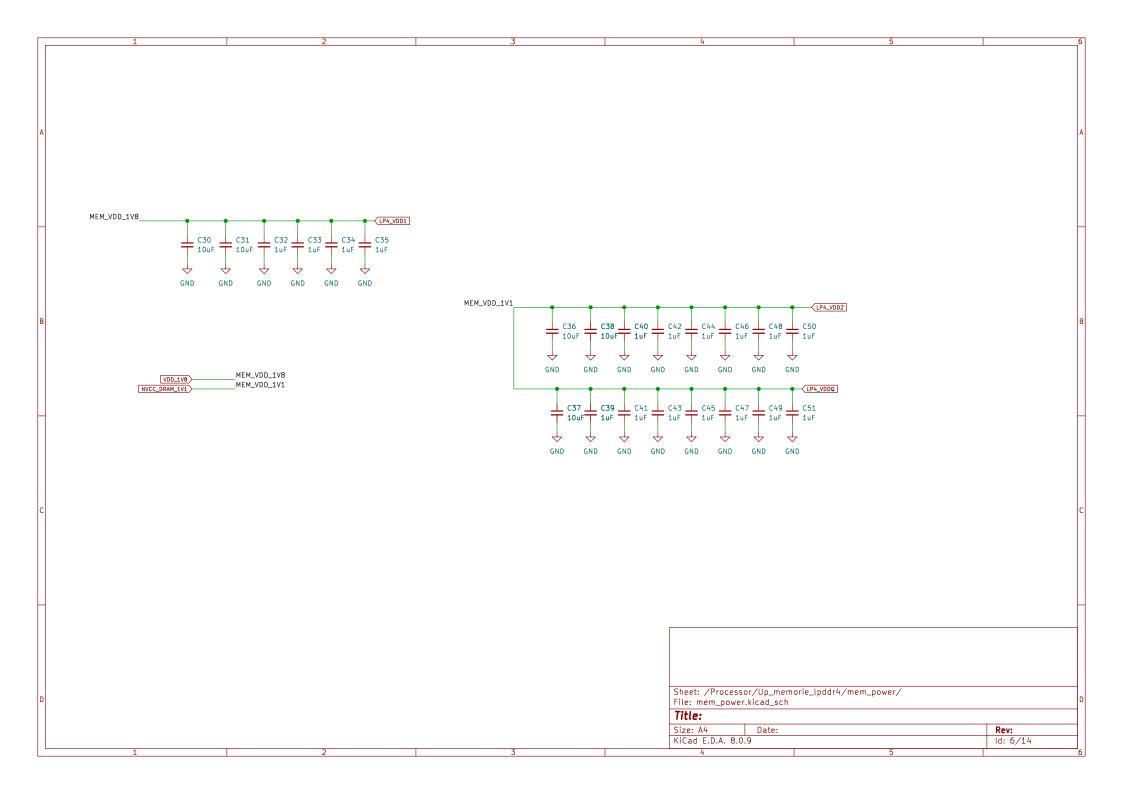
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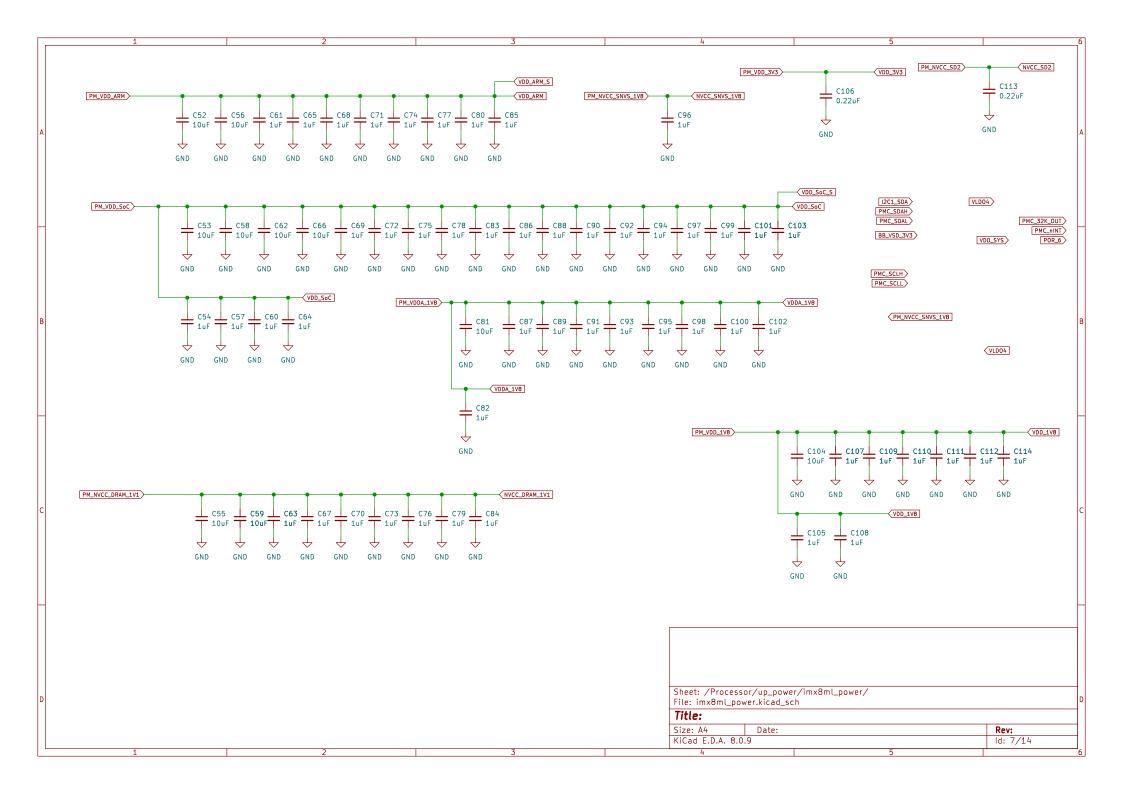
up_power	Up_memorie_lpddr4	wireless		
L File: up_power.kicad_sch	File: up_memorie_lpddr4.kicad_sch	File: wireless_module.kicad_sch		
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		et: /Processor/ : MIMX8ML8CVNKZAB.kicad_sch		

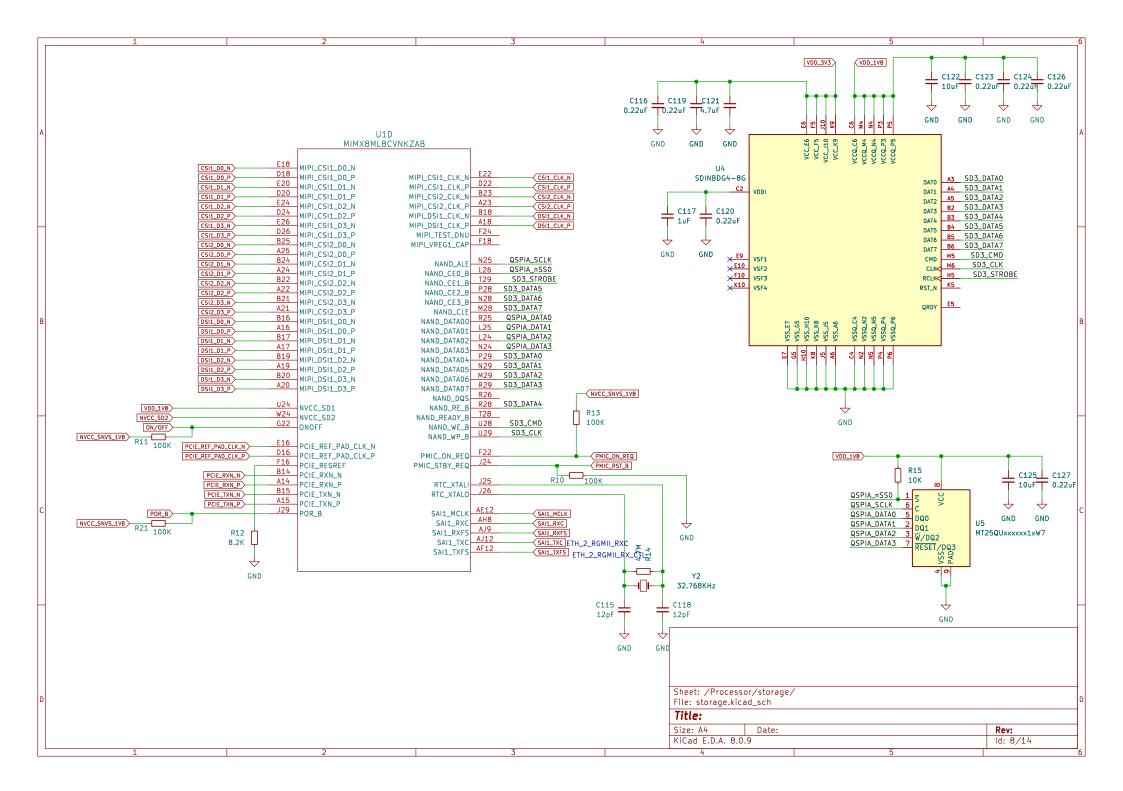












U1E MIMX8ML8CVNKZAB U1C AJ15 SAI2_MCLK AC10 CAN2_RX SAI2_MCLK SAIL RXDO 12S_1_D_IN MIMX8ML8CVNKZAB SAI1_RXD0 AJ16 SAI2_RXC SAI1_RXD1 AF10 CAN1_TX SD1_PWR_EN SAI2_RXC SAI1_RXD1 GPIO_7_CSI AH17 SAI2_RXFS AH9 SAI1_RXD2 ENET_MDC AH28 GPIO_9_DSI SAI2_RXFS AG29 SAI1_RXD2 ETH_2_RGMII_MDC ENET_RD0 AH15 SAI2_TXC ENET_RD0 SAI1_RXD3 AJ8 ENET_MDC CAN1_RX SAI2_TXC AG28 ENET_RD1 ENET_MDIO AH29 SAI1_RXD3 ETH_2_RGMII_MDIO ENET_RD1 ENET_MDIO SAI1_RXD4 AD10 AJ17 SAI2_TXFS ENET_RX_CTL AE28 UART_1_RTS SAI2_TXFS AF29 ENET_RD2 SAI1_RXD4 ETH_2_RGMII_RXD_0 ENET_RD2 ENET_RX_CTL SAI1_RXD5 AE10 AF28 ENET_RD3 ENET_RXC AE29 SAI1_RXD5 ETH_2_RGMII_RXD_1 ENET RD3 ENET_RXC AJ14 SAI2_RXD0 SAI1_RXD6 AH10 AC25 ENET_TD0 ENET_TX_CTL AF24 UART_1_CTS SAI2_RXD0 SAI1_RXD6 ETH_2_RGMII_RXD_2 ENET_TDO ENET_TX_CTL AH16 SAI2_TXD0 SAI1_RXD7 AH12 AE26 ENET_TD1 SAI1_RXD7 ETH_2_RGMII_RXD_3 ENET_TXC AE24 CAN2_TX SAI2_TXD0 ENET_TD1 ENET_TXC SAI1_TXD0 AJ11 AF26 ENET_TD2 SAI1_TXD0 ETH_2_RGMII_TXD_0

SAI1_TXD1 ETH_2_RGMII_TXD_1 SAI1_TXD1 AJ10 ENET_TD2 AJ20 SAI3_MCLK UsB_2_OC# SAI3_MCLK AD24 ENET_TD3 SAI1_TXD2 AH11 ENET_TD3 GPI01_I000 AJ18 SAI3_RXC GPI01_I000 CTRL_SLEEP_MOCI# SAI3_RXC GPI01_I001 E8 SAI1_TXD2 ETH_2_RGMII_TXD_2 AF18 SAI3_RXD SAI1_TXD3 AD12 GPI01_I001 I2S_2_D_IN SAI3_RXD AD22 HDMI_CEC SAI1_TXD3 ETH_2_RGMII_TXD_3 B6 GPI01 1002 HDMI_CEC GPIO_10_DSI SAI3_RXFS WDOG_B AJ19 SAI3_RXFS SAI1_TXD4 AH13 AC22 HDMI_DDC_SCL SAI1_TXD4 ETH_2_RGMII_TX_CTL AH19 SAI3_TXC SAI1_TXD5 AH14 GPI01_I003 HDMI_DDC_SCL PMC_nINT I2S_2_BCLK SAI3_TXC SAI1_TXD5 ETH_2_RGMII_TX_TXC AF22 HDMI_DDC_SDA GPI01_I004 E6 AH18 SAI3_TXD SAI1_TXD6 AC12 SD2_VSEL HDMI_DDC_SDA I2S_2_D_OUT SAI3_TXD SAI1_TXD6 ETH_2_RGMII_INT# AE22 HDMI_HPD GPI01_I005 B4 HDMI_HPD GPI01_I005 SAI1_TXD7 AJ13 AC16 SAI3_TXFS I2S_2_SYNC SAI3_TXFS SAI1_TXD7 PCIe_1_RESET# AJ28 HDMI_REXT GPI01_I006 A3 GPI01_I006 AJ25 HDMI_TXO_N F6 HDMI_TXO_N GPI01_I007 GPI01_I007 GPI0_5_CSI AE16 SAI5_RXD0 SAI5_MCLK AF14 SAI5_MCLK I2S_1_BCLK AH25 HDMI_TX0_P SAI5_RXD0 A8 HDMI_TX0_P GPI01_I008 GPI01_I008 GPI0_6_CSI AD16 SAI5_RXD1 SAI5_RXC AD14 AJ26 HDMI_TX1_N GPI01_I009 B8 I2S_1_SYNC SAI5_RXD1 SAI5_RXC PWM_3_DSI HDMI_TX1_N GPI01_I009 AF16 SAI5_RXD2 SAI5_RXFS AC14 AH26 HDMI_TX1_P GPI01_I010 B7 SAI5_RXD2 SAI5_RXFS I2S_1_D_OUT HDMI_TX1_P GPI01_I010 AE14 SAI5_RXD3 AJ27 HDMI_TX2_N GPI01_I011 D8 SAI5_RXD3 HDMI_TX2_N GPI01_I011 PWM_2 SD1_CLK W28 AH27 HDMI_TX2_P A5 GPI01_I012 AD29 SD2_CD_B SD1_CMD W29 HDMI_TX2_P GPI01_I012 USB_1_EN AJ24 HDMI_TXC_N SDC2_CD_B SDC1_CMD Α6 GPI01_I013 AB29 SD2_CLK HDMI_TXC_N GPI01_I013 USB_1_0C# SD1_DATA0 Y29 AH24 HDMI_TXC_P SDC2_CLK SDC1_DATA0 A4 AB28 SD2_CMD GPI01_I014 HDMI_TXC_P > GPI01_I014 USB_2_EN SD1_DATA1 GPI01_I015 B5 SDC2_CMD SDC1_DATA1 GPI01_I015 CSI_1_MCLK AC28 SD2_DATA0 V29 SD1_DATA2 SDC2_DATA0 SDC1_DATA2 I2C1_SCL AC29 SD2_DATA1 I2C1_SCL SD1_DATA3 V28 SDC2_DATA1 SDC1_DATA3 AH7 AH6 I2C1_SDA I2C1_SDA 1202 501 12C2_SCL AA26 SD2_DATA2 U26 SD1_DATA4 UART3_RTS | SD1_RESET_B I2C2_SDA AE8 SDC2_DATA2 SDC1_DATA4 [12C2_SDA] AA25 SD2_DATA3 SD1_DATA5 AA29 UART3_RXD SD1_DATA7 SDC2_DATA3 SDC1_DATA5 AD28 SD2_RESET_B SD1_DATA6 AA28 I2C3_SCL I2C3_SCL UART3_TXD SD1_DATA6 SDC2_RESET_B SDC1_DATA6 AJ6 12C3_SDA 12C4_SCL AC26 SD2_WP SD1_DATA7 U25 12C3_SDA 12C4_SDA I2C4_SDA AD8 SDC1_DATA7 UART3_CTS SD1_STROB SDC2_WP 12C4_SCL SD1_RESET_B W25 SDC1_RESET_B USB_1_ID G20 JTAG_MOD JTAG_MOD AC18 SPDIF_EXT_CLK SD1_STROBE W26 G18 JTAG_TCK SDC1_STROBE G28 JTAG_TCK LVDS0_CLK_N LVDS0_CLK_N AD18 SPDIF_RX G16 JTAG_TDI LVDS0_CLK_P F29 JTAG_TDI LVDS0_CLK_P AE18 | SPDIF_TX F14 LVDS1_CLK_N B28 UART1_RXD UART1_RXD JTAG_TDO UART1_TXD AJ3 JTAG_TDO LVDS1_CLK_N G14 JTAG_TMS UART1_TXD LVDS1_CLK_P A28 E10 USB1_D_N UART2_RXD AF6 JTAG TMS LVDS1_CLK_P USB1_D_N UART2_RXD D10 USB1_D_P AH4 UART2_TXD USB1_D_P UART2_TXD LVDS0_D0_N B11 USB1_DNU LVDS0_D0_N AE6 UART3_RXD LVDS0_D0_P D29 USB1_DNU UART3 RXD LVDS0_D0_P B9 USB1_RX_N UART3_TXD AJ4 USB1_RX_N UART3_TXD LVDS0_D1_N F28 LVDS0_D1_N MSB1_RX_P UART4_RXD AJ5 LVDS0_D1_P E29 USB1_RX_P UART4_RXD B10 USB1_TX_N UART4_TXD AH5 LVDS0_D1_P UART4_TXD LVDS0_D2_N H28 USB1_TX_N LVDS0_D2_N A10 USB1_TX_P LVDS0_D2_P G29 USB1_TX_P F10 USB1_TXRTUNE LVDS0_D2_P USB1_TXRTUNE LVDS0_D3_N J28 LVDS0_D3_N A11 USB1_VBUS LVDS0_D3_P H29 USB1_VBUS LVDS0_D3_P LVDS1_D0_N B26 LVDS1_D0_N LVDS1_D0_P A26 LVDS1_D0_P LVDS1_D1_N B27 LVDS1_D1_N LVDS1_D1_P A27 LVDS1_D1_P LVDS1_D2_N C28 LVDS1_D2_N LVDS1_D2_P B29 LVDS1_D2_P LVDS1_D3_N D28 LVDS1_D3_N LVDS1_D3_P C29 LVDS1_D3_P Sheet: /Processor/io-interface/ File: io-interface.kicad_sch Title: Size: A4 Date: Rev: KiCad E.D.A. 8.0.9 ld: 9/14

