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CS220: Computer Architecture Unit 2: Digital Logic Project Professor: Dr. Bill Gaught

Truth Table

Tutti lable						
Α	В	С	D	E = A AND B	F = C OR D	G = (F NOR E)
Т	Т	Т	Т	T	T	F
F	Т	Т	Т	F	T	F
Т	Т	Т	F	T	Τ	F
F	Т	Т	F	F	T	F
Т	Т	F	Т	Т	T	F
F	Т	F	Т	F	T	F
Т	Т	F	F	Т	F	F
F	Т	F	F	F	F	T
Т	F	Т	Т	F	T	F
F	F	Т	Т	F	T	F
Т	F	Т	F	F	T	F
F	F	Т	F	F	T	F
Т	F	F	Т	F	T	F
F	F	F	Т	F	Τ	F
Т	F	F	F	F	F	Т
F	F	F	F	F	F	Т

