**R3-instruction**
format
**ALU**

Reg_S1

Result

64

Reg_S2

**R4**

Opcode R3

64

64

4

2

**Load**
**Immediate**
**SHIFTER**

16

Immediate_16

Result

2

LI_Offset

64

4-instruction
format
ALU

**Result
Select
MUX**

Reg_S1

R3_Result

Result      64      R4_Result

Reg_S2

Reg_S3

LI_Result

Final Result      64

Opcode R4

Result_Select

2

64