#### Model:

LI-IMX219-MIPI-FF-NANO-H90 LI-IMX219-MIPI-FF-NANO-H136 LI-IMX219-MIPI-FF-NANO-H145

Rev 1.4 Leopard Imaging Inc.

# **Contents**

Version History	3
Key Information	4
Pin Assignment	5
Electrical Characteristics	6
1. Absolute Maximum Ratings	6
2. Recommended Operating Conditions	6
3. Spectral Sensitivity Characteristics	6
4. DC Characteristics	7
5. Electrical Characteristics	7
6. AC Characteristics	
7. Power On Sequence	9
8. Power Off Sequence	10
Mechanical Drawing	11



# **Version History**

Version	Description	Release Date
1.0	First Release	16. Mar. 2019
1.1	Add H90 and H135 version	22. Mar. 2019
1.2	Update the dimensions of H90 and H135 modules	14. Apr. 2019
1.3	Remove H135 module	26. Apr. 2019
1.4	Add H136 module	18. Oct. 2019



# **Key Information**

Module Pa	rt#		LI-IMX219-MIPI-FF-NANO				
Compatible	Platform		NVIDIA® Jetson Nano <sup>TM</sup> Developer Kit				
Module Size	e	H90	150.0 mm (L) x 25.0 mm (W) x 15.27 mm(H)				
		H136	150.0 mm (L) x 25.0 mm (W) x 15.3 mm(H)				
		H145	150.0 mm (L) x 25.0 mm (W) x 14.1 mm(H)				
Sensor Type	2	·	Sony IMX219 8.08MP Color sensor				
Active Pixe	ls		3280 (H) x 2464 (V)				
Image Size			Diagonal 4.60 mm (Type 1/4.0)				
F/No			2.6 (H90) / 2.0 (H136) / 2.5 (H145)				
Focal Lengt	th		2.3mm (H90) / 1.58mm (H136) / 1.55 mm (H145)				
FOV			90 °(H) / 136 °(H) /145 °(H)				
TV Distorti	on		< -10% (H90) / < -15% (H136) / < -15% (H145)				
Focusing Ra	ange		30cm - Infinity				
Pixel size			1.12 um x 1.12 um				
IR Cutter Fi	ilter		Yes				
Temperature	e Range	Operating	-20 ℃ to +60 ℃				
		Storage	-30 ℃ to +80 ℃				
Data Output	t Interface	)	CSI-2 MIPI 2-lane				
Maximum I	mage Tra	nsfer Rate	3280 x 2464 @ 21fps				
Mating	Part#		1-1734248-5				
Connector Number of Positions			15				
	Pitch		0.039" (1.00mm)				





# Pin Assignment

No.	Name	Pin type	Description
1	GND	Ground	
2	MDN0	О	MIPI data positive output
3	MDP0	0	MIPI data negative output
4	GND	Ground	
5	MDN1	0	MIPI data positive output
6	MDP1	0	MIPI data negative output
7	GND	Ground	
8	MCN	0	MIPI clock negative output
9	MCP	0	MIPI clock positive output
10	GND	Ground	
11	RESET	I	Reset
12	FSTROBE	0	Strobe output
13	SCL	I	
14	SDA	I/O	
15	VCC3.3V	Power	



# **Sensor Specifications**

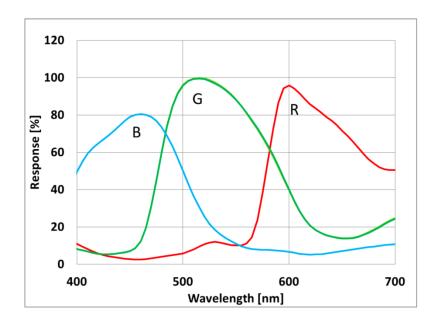
## 1. Absolute Maximum Ratings

Item	Symbol	Min.	Тур.	Max.	Unit	Remarks
Supply voltage (analogue)	V <sub>ANA</sub>	-0.3		3.3	٧	
Supply voltage (Core)	V <sub>DDL</sub>	-0.3		2.0	V	
Supply voltage (IF)	$V_{DIG}$	-0.3		3.3	V	
Input voltage	Vı	-0.3		3.3	V	
Output voltage	Vo	-0.3		3.3	V	
Operating temperature (function)	Topr	-20		60	°C	Junction temperature
Storage temperature	Tstg	-30		80	°C	Junction temperature
Performance guarantee temperature	Tspec	-20		60	°C	Junction temperature

## 2. Recommended Operating Conditions

Item	Symbol	Min.	Тур.	Max.	Unit	Remarks
Supply voltage (analogue)	V <sub>ANA</sub>	2.6	2.8	3.0	V	
Supply voltage (Core)	$V_{DDL}$	1.08	1.2	1.3	V	
Supply voltage (IF)	$V_{DIG}$	1.62	1.8	1.98	V	

## 3. Spectral Sensitivity Characteristics





## 4. DC Characteristics

Item	Pins	Symbol	Min.	Тур.	Max.	Unit	Comment
	VDDHFIL1,2						
	VDDHCM1,2						
	VDDHAN	V <sub>ANA</sub>	2.6	2.8	3.0	V	
	VDDHPL						
Supply voltage	VDDHSN1,2						
	VDDMCO	$V_{DIG}$	1.62	1.8	1.98	٧	
	VDDLSC1-8		1.08	1.20	1.30	V	
	VDDLCN1,2	$V_{DDL}$					
	VDDLIO1,2						
		VIL	-0.5		0.3V <sub>DIG</sub>	V	
Digital input/output	SCL, SDA, GPO	VIH	0.7V <sub>DIG</sub>		V <sub>DIG</sub> + 0.5	٧	
voltage		VOL			0.25V <sub>DIG</sub>	٧	
		VOH	0.75V <sub>DIG</sub>			V	
Digital output valtage	FSTROBE	VOL			0.45	٧	
Digital output voltage	FSIRUBE	VOH	V <sub>DIG</sub> - 0.45				
Digital input voltage	XCLR,INCK	VIL	-0.3		0.35V <sub>DIG</sub>	V	
Digital input voltage	AULN, INUK	VIH	0.65V <sub>DIG</sub>		V <sub>DIG</sub> + 0.3		

## 5. Electrical Characteristics

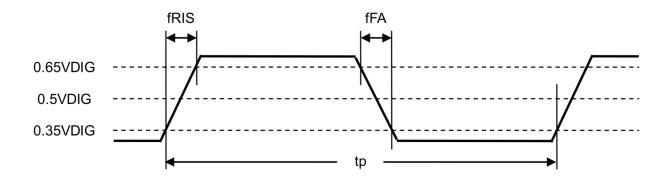
 $(V_{ANA} = 3.0 \text{ V}, V_{DDL} = 1.3 \text{ V}, V_{DIG} = 1.98 \text{ V}, Tj = 60 ^{\circ}\text{C})$ 

Item	Symbol	Min.	Тур.	Max.	Unit	Comment
Current consumption (Full,30 frame/s)	IVAVA_strm		33	38	mA	VTmax is max speed read out from pixel array CSI2 4 lanes, V <sub>ANA</sub> current
	IVDDL_strm		100	160	mA	VTmax is max speed read out from pixel array CSI2 4 lanes, V <sub>DDL</sub> current
	_					Defect Correction, L.S.C. function off
	ISTB_ana			50	μA	XCLR = Lo, V <sub>ANA</sub> current
HW-Standby current	ISTB_dig	·		10	μA	XCLR = Lo, V <sub>DIG</sub> current
	ISTB_lddl			50	μA	XCLR = Lo, V <sub>DDL</sub> current



## 6. AC Characteristics

Input specifications are shown below when square-wave inputs directly into the external pin INCK.

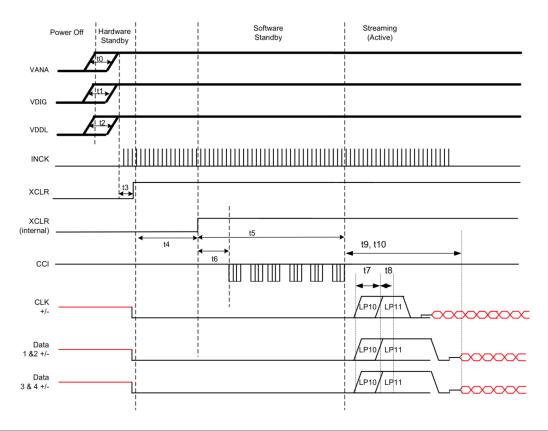


### Master Clock Square Waveform Input Characteristics

Item	Symbol	Min.	Тур.	Max.	Unit	Comment
Frequency	fSCK	6	18	27	MHz	
jitter (period, peak-to-peak)	Tjitter			600	ps	
Rise Time	fRISE	1		10	ns	
Fall Time	fFALL	1		10	ns	
Duty Cycle	fDUTY	40		60	%	
Input Leakage	fILEAK	-10		10	μΑ	



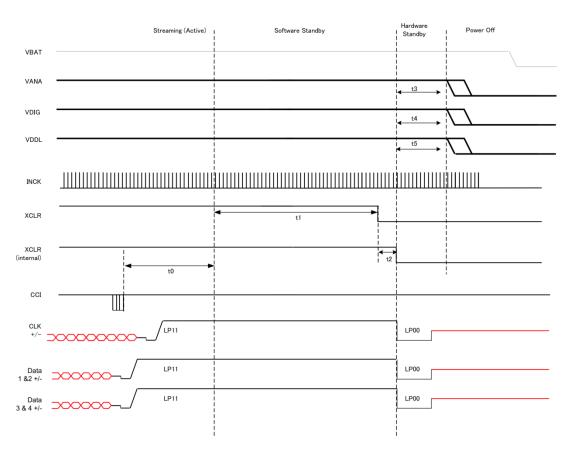
# 7. Power On Sequence



Constraint	Label	Min.	Max.	Units	Comment
Sequence free of VDDs rising	t0, t1, t2	VANA, VDIG, VDDL may rise in any order.		ns	
XCLR rising	t3	0.5 —		μs	
Internal XCLR is Low to High after VDDs & XCLR supplied	t4	200		μs	
releasing software standby after XCLR Low to High	t5	6	_	ms	charge up VRL
Initializing time of silicon	t6	_	— 32000		clock is INCK Case of INCK = 6[MHz], 5.3[msec]
D-PHY power-up	t7	1	1.1	ms	
D-PHY init	t8	100	110	μs	
After releasing software standby to data streaming time	t9	1.2 ms + exposure time	_		
Quick launch up time	t10	_	1	frame	stable time until optimal image quality



## 8. Power Off Sequence



Constraint	Label	Min.	Max.	Units	Comment
Communication end – Software standby	t0		One frame time (*1)	s	Until frame output
Software standby - XCLR $H \rightarrow L$	t1	0		ns	
Falling time of internal XCLR after XCLR H → L	t2		10	μs	
VANA falling - VDIG falling - VDDL falling	t3,t4,t5	VANA, VDIG and VDDL may fall in any order.		ns	



