

## < IGBT MODULES >

# CM400DY-34A

HIGH POWER SWITCHING USE INSULATED TYPE

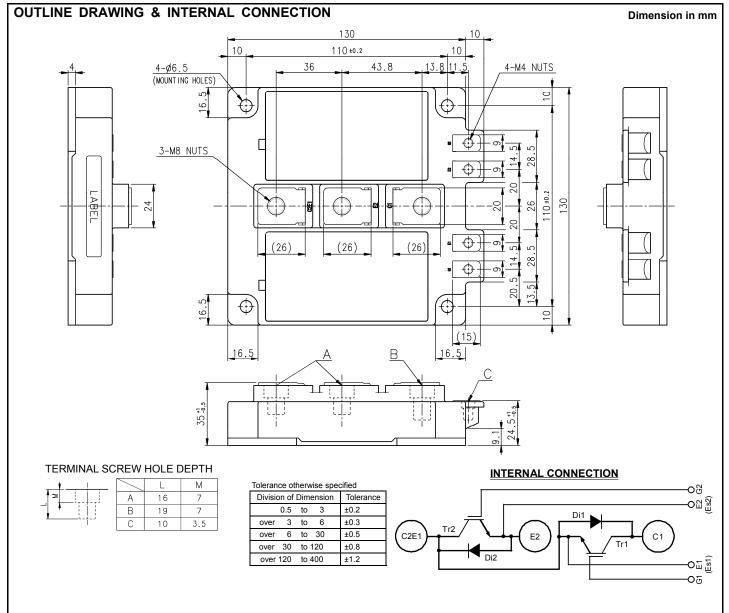


**Dual (Half-Bridge)** 

- Flat base Type
- Copper base plate (non-plating)
- •RoHS Directive compliant
- •UL Recognized under UL1557, File E323585

## **APPLICATION**

AC Motor Control, Motion/Servo Control, Power supply, etc.



# < IGBT MODULES > CM400DY-34A HIGH POWER SWITCHING USE INSULATED TYPE

| ABOULD IL MAXIMOM MATIMOU (11-20 0, unicos otnerwise specifica) | ABSOLUTE MAXIMUM RATINGS | (T | <sub>i</sub> =25 °C, | unless | otherwise s | pecified) |  |
|---|--------------------------|----|----------------------|--------|-------------|-----------|--|
|---|--------------------------|----|----------------------|--------|-------------|-----------|--|

| Symbol                    | Item                      | Conditions                                      | Rating     | Unit |
|---------------------------|---------------------------|---|------------|------|
| V <sub>CES</sub>          | Collector-emitter voltage | G-E short-circuited                             | 1700       | V    |
| $V_{\text{GES}}$          | Gate-emitter voltage      | C-E short-circuited                             | ±20        | V    |
| Ic                        | Collector current         | DC, T <sub>C</sub> =107 °C (Note.2, 4)          | 400        | ^    |
| I <sub>CRM</sub>          | Collector current         | Pulse, Repetitive (Note.3)                      | 800        | A    |
| P <sub>tot</sub>          | Total power dissipation   | T <sub>C</sub> =25 °C (Note.2, 4)               | 3780       | W    |
| I <sub>E</sub> (Note.1)   | Emitter current           | T <sub>C</sub> =25 °C (Note.2, 4)               | 400        | ^    |
| I <sub>ERM</sub> (Note.1) | - Emilier current         | Pulse, Repetitive (Note.3)                      | 800        | A    |
| Tj                        | Junction temperature      | -   | -40 ~ +150 | °C   |
| T <sub>stg</sub>          | Storage temperature       | -   | -40 ~ +125 | 7    |
| V <sub>isol</sub>         | Isolation voltage         | Terminals to base plate, RMS, f=60 Hz, AC 1 min | 3500       | V    |

ELECTRICAL CHARACTERISTICS (T<sub>i</sub>=25 °C, unless otherwise specified)

| Symbol                   | Item                                  | Conditions -  |   | Limits |       |      | Linit |
|--------------------------|---------------------------------------|---|---|--------|-------|------|-------|
| Symbol                   | item                                  |   |   | Min.   | Тур.  | Max. | Unit  |
| I <sub>CES</sub>         | Collector-emitter cut-off current     | V <sub>CE</sub> =V <sub>CES</sub> , G-E short-circuited   |   | -      | -     | 1.0  | mA    |
| I <sub>GES</sub>         | Gate-emitter leakage current          | V <sub>GE</sub> =V <sub>GES</sub> , C-E short-circuited   |   | -      | -     | 2.0  | μA    |
| $V_{GE(th)}$             | Gate-emitter threshold voltage        | I <sub>C</sub> =40 mA, V <sub>CE</sub> =10 V  |   | 5.5    | 7.0   | 8.5  | V     |
|                          | Collector-emitter saturation voltage  | I <sub>C</sub> =400 A (Note.5),   | $I_{C}$ =400 A (Note.5), $T_{j}$ =25 °C $V_{GE}$ =15 V $T_{j}$ =125 °C  |        | 2.2   | 2.8  | V     |
| V <sub>CEsat</sub>       | Collector-enlitter saturation voltage | V <sub>GE</sub> =15 V   |   |        | 2.45  | -    |       |
| Cies                     | Input capacitance                     |   | V <sub>CE</sub> =10 V, G-E short-circuited                              |        | -     | 98.8 |       |
| Coes                     | Output capacitance                    | V <sub>CE</sub> =10 V, G-E short-circuited  |   |        | -     | 11.2 | nF    |
| Cres                     | Reverse transfer capacitance          | ]   |   | -      | -     | 2.1  |       |
| $Q_G$                    | Gate charge                           | V <sub>CC</sub> =1000 V, I <sub>C</sub> =400 A, V <sub>GE</sub> =15 V                                 |   | -      | 2670  | -    | nC    |
| t <sub>d(on)</sub>       | Turn-on delay time                    | $V_{\rm CC}$ =1000 V, $I_{\rm C}$ =400 A, $V_{\rm GE}$ =±15 V, $R_{\rm G}$ =1.2 Ω, Inductive load     |   | -      | -     | 950  |       |
| tr                       | Rise time                             |   |   | -      | -     | 300  | ns    |
| t <sub>d(off)</sub>      | Turn-off delay time                   |   |   | -      | -     | 1000 |       |
| t <sub>f</sub>           | Fall time                             |   |   | -      | -     | 350  |       |
| V <sub>EC</sub> (Note.1) | Emitter-collector voltage             | I <sub>E</sub> =400 A (Note.5), G-E short-cir   | cuited  | -      | 2.3   | 3.0  | V     |
| t <sub>rr</sub> (Note.1) | Reverse recovery time                 | V <sub>CC</sub> =1000 V, I <sub>E</sub> =400 A, V <sub>GE</sub> =±1                                   | V <sub>CC</sub> =1000 V, I <sub>E</sub> =400 A, V <sub>GE</sub> =±15 V, |        | -     | 450  | ns    |
| Q <sub>rr</sub> (Note.1) | Reverse recovery charge               | R <sub>G</sub> =1.2 Ω, Inductive load   |   | -      | 40    | -    | μC    |
| Eon                      | Turn-on switching energy per pulse    | $V_{CC}$ =1000 V, $I_{C}$ = $I_{E}$ =400 A, $V_{GE}$ =±15 V, $R_{G}$ =1.2 $\Omega$ , $T_{J}$ =125 °C, |   | -      | 197.3 | -    | m l   |
| E <sub>off</sub>         | Turn-off switching energy per pulse   |   |   | -      | 117.9 | -    | mJ    |
| E <sub>rr</sub> (Note.1) | Reverse recovery energy per pulse     | Inductive load  | Inductive load  |        | 98.5  | -    | mJ    |
| r <sub>g</sub>           | Internal gate resistance              | Per switch, T <sub>c</sub> =25 °C   |   | -      | 3.7   | -    | Ω     |

#### THERMAL RESISTANCE CHARACTERISTICS

| Symbol Item          | Itom                                | Conditions                         | Limits |      |       | Unit  |
|----------------------|-------------------------------------|------------------------------------|--------|------|-------|-------|
|                      | Conditions                          | Min.                               | Тур.   | Max. | Offic |       |
| $R_{th(j-c)Q}$       | Thermal resistance (Note.2)         | Junction to case, per IGBT         | -      | -    | 33    | K/kW  |
| $R_{th(j-c)D}$       | mermanesistance                     | Junction to case, per FWDi         | -      | -    | 55    | K/kW  |
| В                    | Contact thermal resistance (Note.2) | Case to heat sink, per 1/2 module, |        | 19   |       | K/kW  |
| R <sub>th(c-s)</sub> | Contact thermal resistance          | Thermal grease applied (Note.6)    | -      | 19   | -     | K/KVV |

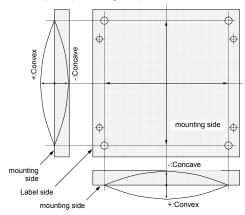
# **MECHANICAL CHARACTERISTICS**

| Symbol         | Item                   | Conditions                      |           |      | Unit |      |        |
|----------------|------------------------|---------------------------------|-----------|------|------|------|--------|
| Symbol         |                        | Conditions                      |           | Min. | Тур. | Max. | Offic  |
| M              |                        | Main terminals                  | M 8 screw | 8.8  | 9.7  | 10.8 | N·m    |
| M <sub>t</sub> | Mounting torque        | Auxiliary (G, E) terminals      | M 4 screw | 1.3  | 1.5  | 1.7  | INTIII |
| Ms             |                        | Mounting to heat sink           | M 6 screw | 3.5  | 4.0  | 4.5  | N·m    |
| m              | Weight                 | -                               |           | -    | 1200 | -    | g      |
| ec             | Flatness of base plate | On the centerline X, Y (Note.7) |           | -100 | -    | +100 | μm     |

# < IGBT MODULES > CM400DY-34A HIGH POWER SWITCHING USE INSULATED TYPE

Note1. Represent ratings and characteristics of the anti-parallel, emitter-collector free wheeling diode (FWDi).

- 2. Case temperature (T<sub>C</sub>) and heat sink temperature (T<sub>s</sub>) are defined on the each surface (mounting side) of base plate and heat sink just under the chips. Refer to the figure of chip location.
  - The heat sink thermal resistance should measure just under the chips.
- 3. Pulse width and repetition rate should be such that the device junction temperature (T<sub>i</sub>) dose not exceed T<sub>imax</sub> rating.
- 4. Junction temperature  $(T_j)$  should not increase beyond  $T_{jmax}$  rating.
- 5. Pulse width and repetition rate should be such as to cause negligible temperature rise. Refer to the figure of test circuit.
- 6. Typical value is measured by using thermally conductive grease of  $\lambda$ =0.9 W/(m·K).
- 7. Base plate (mounting side) flatness measurement points (X, Y) are as follows of the following figure.

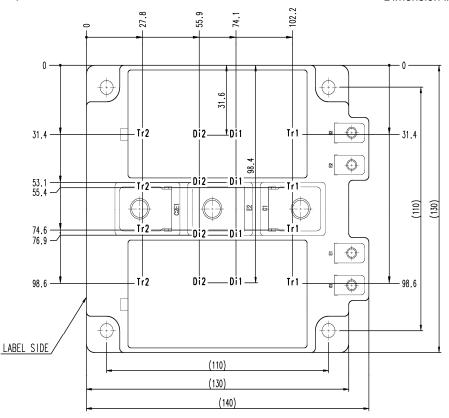


#### RECOMMENDED OPERATING CONDITIONS

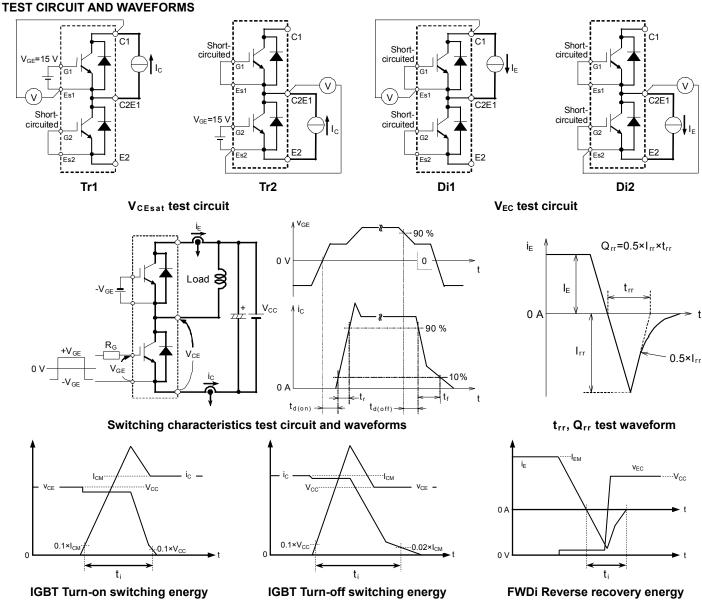
| Symbol Item    | Itom                          | Conditions                   | Limits |      |      | Unit |
|----------------|-------------------------------|------------------------------|--------|------|------|------|
|                | Conditions                    | Min.                         | Тур.   | Max. | Onit |      |
| Vcc            | (DC) Supply voltage           | Applied across C1-E2         | 1      | 1000 | 1100 | V    |
| $V_{GEon}$     | Gate (-emitter drive) voltage | Applied across G1-Es1/G2-Es2 | 13.5   | 15.0 | 16.5 | V    |
| R <sub>G</sub> | External gate resistance      | Per switch                   | 1.2    | 1    | 12   | Ω    |

#### **CHIP LOCATION (Top view)**

Dimension in mm, tolerance: ±1 mm



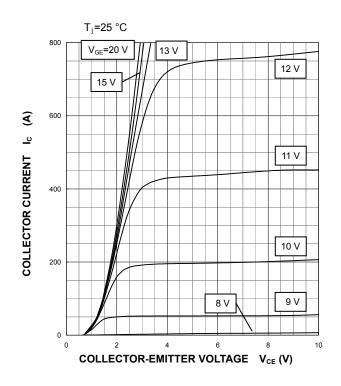
Tr1/Tr2: IGBT, Di1/Di2: FWDi



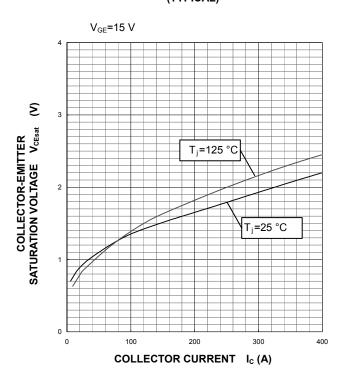
Turn-on / Turn-off switching energy and Reverse recovery energy test waveforms (Integral time instruction drawing)

#### **PERFORMANCE CURVES**

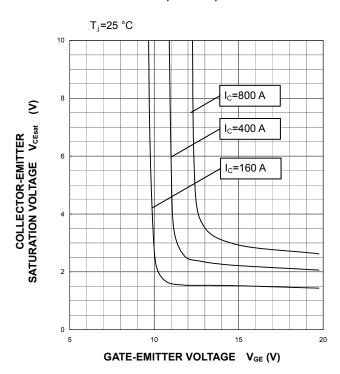
# OUTPUT CHARACTERISTICS (TYPICAL)



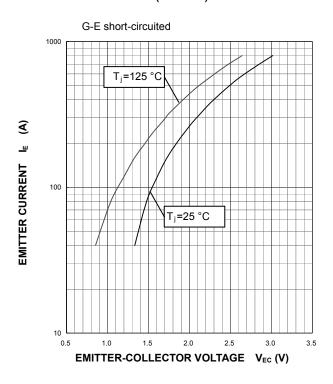
#### COLLECTOR-EMITTER SATURATION VOLTAGE CHARACTERISTICS (TYPICAL)



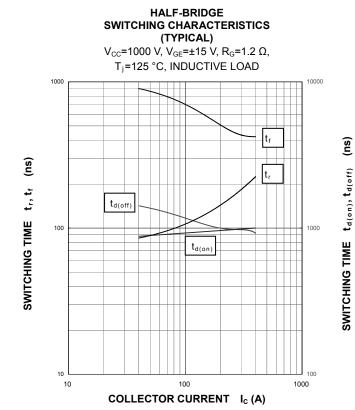
#### COLLECTOR-EMITTER SATURATION VOLTAGE CHARACTERISTICS (TYPICAL)



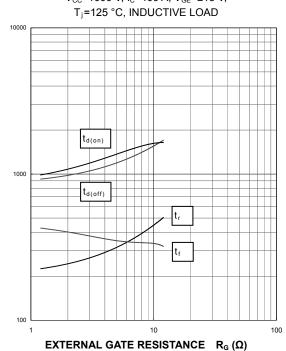
#### FREE WHEELING DIODE FORWARD CHARACTERISTICS (TYPICAL)



#### **PERFORMANCE CURVES**

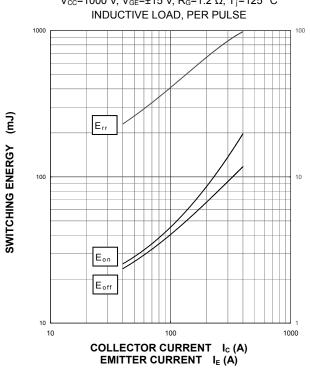


## **HALF-BRIDGE SWITCHING CHARACTERISTICS** (TYPICAL) $V_{CC}$ =1000 V, $I_{C}$ =400 A, $V_{GE}$ =±15 V,



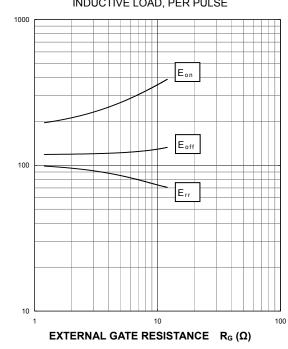
#### **HALF-BRIDGE SWITCHING CHARACTERISTICS** (TYPICAL)

 $V_{CC}$ =1000 V,  $V_{GE}$ =±15 V,  $R_{G}$ =1.2  $\Omega$ ,  $T_{j}$ =125 °C INDUCTIVE LOAD, PER PULSE



#### **HALF-BRIDGE SWITCHING CHARACTERISTICS** (TYPICAL)

 $V_{CC}$ =1000 V,  $I_C/I_E$ =400 A,  $V_{GE}$ =±15 V,  $T_j$ =125 °C INDUCTIVE LOAD, PER PULSE



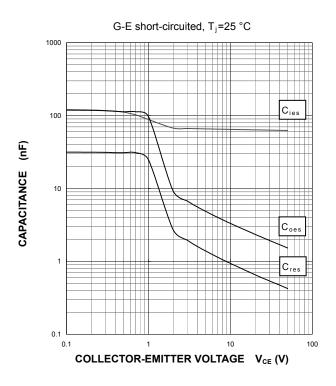
REVERSE RECOVERY ENERGY (mJ)

SWITCHING ENERGY (mJ)

SWITCHING TIME

#### **PERFORMANCE CURVES**

#### **CAPACITANCE CHARACTERISTICS** (TYPICAL)



# t<sub>rr</sub> (ns), I<sub>rr</sub>

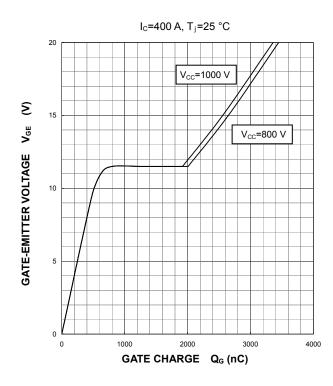
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**FREE WHEELING DIODE** REVERSE RECOVERY CHARACTERISTICS

(TYPICAL)  $V_{CC}$ =1000 V,  $V_{GE}$ =±15 V,  $R_{G}$ =1.2  $\Omega$ , T<sub>j</sub>=25 °C, INDUCTIVE LOAD

#### **GATE CHARGE CHARACTERISTICS** (TYPICAL)

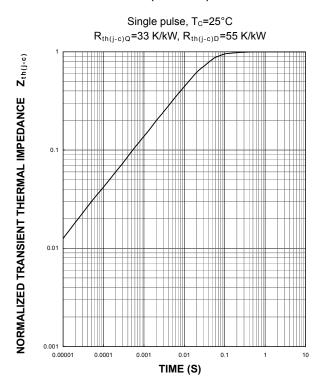


#### TRANSIENT THERMAL IMPEDANCE CHARACTERISTICS (MAXIMUM)

100

EMITTER CURRENT I<sub>E</sub> (A)

1000



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