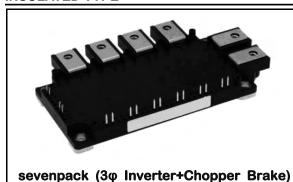


< IGBT MODULES >

CM100RX-24S

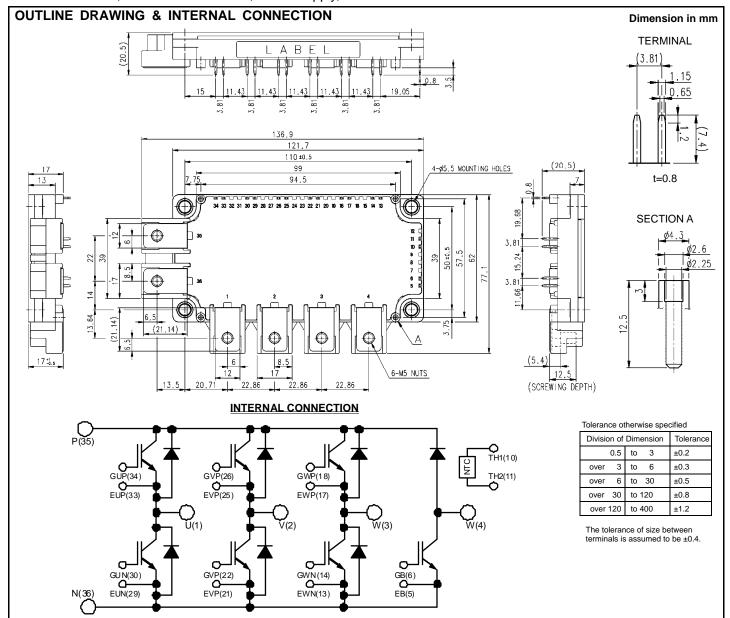
HIGH POWER SWITCHING USE INSULATED TYPE



- Flat base Type
- Copper base plate (non-plating)
- •Tin plating pin terminals
- •RoHS Directive compliant
- •Recognized under UL1557, File E323585

APPLICATION

AC Motor Control, Motion/Servo Control, Power supply, etc.



< IGBT MODULES > CM100RX-24S HIGH POWER SWITCHING USE INSULATED TYPE

ABSOLUTE MAXIMUM RATINGS (T $_{\rm j}$ =25 °C, unless otherwise specified) INVERTER PART IGBT/DIODE

Symbol	ool Item Conditions		Rating	Unit
V _{CES}	Collector-emitter voltage	G-E short-circuited	1200	V
V _{GES}	Gate-emitter voltage	C-E short-circuited	± 20	V
Ic	Callegtor current	DC, T _C =119 °C (Note2, 4)	100	
I _{CRM}	Collector current	Pulse, Repetitive (Note3)	200	_ A
P _{tot}	Total power dissipation	T _C =25 °C (Note2, 4)	750	W
I _E (Note1)	Fasittan assurant	(Note2)	100	^
I _{ERM} (Note1)	Emitter current	Pulse, Repetitive (Note3)	200	A

BRAKE PART IGBT/DIODE

Symbol	Item	Conditions	Rating	Unit
V _{CES}	Collector-emitter voltage	G-E short-circuited	1200	V
V_{GES}	Gate-emitter voltage	C-E short-circuited	± 20	V
Ic	Collector current	DC, T _C =125 °C (Note2, 4)	50	^
I _{CRM}	Collector current	Pulse, Repetitive (Note3)	100	A
P _{tot}	Total power dissipation	T _C =25 °C (Note2, 4)	425	W
V _{RRM}	Repetitive peak reverse voltage	G-E short-circuited	1200	V
I _F	Forward current	(Note2)	50	۸
I _{FRM}	Torward Current	Pulse, Repetitive (Note3)	100	A

MODULE

Symbol	Item	Conditions	Rating	Unit
V _{isol}	Isolation voltage	Terminals to base plate, RMS, f=60 Hz, AC 1 min	2500	V
T _{jmax}	Maximum junction temperature	Instantaneous event (overload)	175	°C
T _{Cmax}	Maximum case temperature	(Note4)	125	°C
T _{jop}	Operating junction temperature	Continuous operation (under switching)	-40 ~ +150	°C
T _{stg}	Storage temperature	-	-40 ~ +125	

ELECTRICAL CHARACTERISTICS (T $_{j}$ =25 °C, unless otherwise specified) INVERTER PART IGBT/DIODE

Cumbal	Itama	Conditions		Limits			Linit
Symbol	Item	Conditions	Conditions		Тур.	Max.	Unit
I _{CES}	Collector-emitter cut-off current	V _{CE} =V _{CES} , G-E short-circuited		-	-	1.0	mA
I _{GES}	Gate-emitter leakage current	V _{GE} =V _{GES} , C-E short-circuited		-	-	0.5	μΑ
$V_{GE(th)}$	Gate-emitter threshold voltage	I _C =10 mA, V _{CE} =10 V		5.4	6.0	6.6	V
		I _C =100 A (Note5),	T _j =25 °C	-	1.80	2.25	
		V _{GE} =15 V,	T _j =125 °C	-	2.00	-	V
V	Collector-emitter saturation voltage	(Terminal)	T _j =150 °C	-	2.05	-	
V _{CEsat}		I _C =100 A (Note5),	T _j =25 °C	-	1.70	2.15	
		V _{GE} =15 V,	T _j =125 °C	-	1.90	-	V
		(Chip)	T _j =150 °C	-	1.95	-	
Cies	Input capacitance			-	-	10	nF
Coes	Output capacitance	V _{CE} =10 V, G-E short-circuited		-	-	2.0	
Cres	Reverse transfer capacitance			-	-	0.17	
Q _G	Gate charge	V _{CC} =600 V, I _C =100 A, V _{GE} =15	V	-	233	-	nC
t _{d(on)}	Turn-on delay time	V 600 V I 100 A V -115	- \/	-	-	300	
tr	Rise time	V_{CC} =600 V, I_{C} =100 A, V_{GE} =±15 V,		-	-	200	
t _{d(off)}	Turn-off delay time	B. 6.2.O. Industive load		-	-	600	ns
t _f	Fall time	$R_G=6.2 \Omega$, Inductive load		-	-	300	1

< IGBT MODULES > CM100RX-24S HIGH POWER SWITCHING USE INSULATED TYPE

ELECTRICAL CHARACTERISTICS (cont; T $_{\rm j}$ =25 °C, unless otherwise specified) INVERTER PART IGBT/DIODE

Symbol	Itom	em Conditions —			Limits		Unit
Symbol	item			Min.	Тур.	Max.	Offic
		I _E =100 A (Note5),	T _j =25 °C	-	1.80	2.25	
		G-E short-circuited,	T _j =125 °C	-	1.80	-	V
V _{EC} (Note1)	Emitter cellecter veltage	(Terminal)	T _j =150 °C	-	1.80	-	
VEC	Emitter-collector voltage	I _E =100 A (Note5),	T _j =25 °C	-	1.70	2.15	
		G-E short-circuited,	T _j =125 °C	-	1.70	-	V
		(Chip)	T _j =150 °C	-	1.70	-	
t rr (Note1)	Reverse recovery time	V _{CC} =600 V, I _E =100 A, V _{GE} =±15 V,		-	-	300	ns
Q _{rr} (Note1)	Reverse recovery charge	R_G =6.2 Ω , Inductive load		=	5.3	-	μC
Eon	Turn-on switching energy per pulse	V _{CC} =600 V, I _C =I _E =100 A,		-	8.6	-	mJ
E _{off}	Turn-off switching energy per pulse	$V_{GE}=\pm 15 \text{ V}, R_{G}=6.2 \Omega, T_{j}=150$	°C,	-	10.7	-	IIIJ
E _{rr} (Note1)	Reverse recovery energy per pulse	Inductive load		=	10.2	-	mJ
R _{CC'+EE'}	Internal lead resistance	Main terminals-chip, per switch T _C =25 °C (Note4)	١,	-	-	3.5	mΩ
r _g	Internal gate resistance	Per switch		-	0	-	Ω

BRAKE PART IGBT/DIODE

Symbol	Item	Conditions	Conditions		Limits		
Symbol	item	item Conditions		Min.	Тур.	Max.	Unit
I _{CES}	Collector-emitter cut-off current	V _{CE} =V _{CES} , G-E short-circuited		-	=	1.0	mA
I _{GES}	Gate-emitter leakage current	V _{GE} =V _{GES} , C-E short-circuited		-	-	0.5	μΑ
$V_{GE(th)}$	Gate-emitter threshold voltage	I _C =5 mA, V _{CE} =10 V		5.4	6.0	6.6	V
		I _C =50 A (Note5),	T _j =25 °C	-	1.80	2.25	
		V _{GE} =15 V,	T _j =125 °C	-	2.00	-	V
M		(Terminal)	T _j =150 °C	-	2.05	-	
V_{CEsat}	Collector-emitter saturation voltage	I _C =50 A (Note5),	T _j =25 °C	-	1.70	2.15	
		V _{GE} =15 V,	T _j =125 °C	-	1.90	-	V
		(Chip)	T _j =150 °C	-	1.95	-	
Cies	Input capacitance			-	-	5.0	
Coes	Output capacitance	V _{CE} =10 V, G-E short-circuited		-	-	1.0	nF
Cres	Reverse transfer capacitance			-	-	0.08	
Q _G	Gate charge	V_{CC} =600 V, I_{C} =50 A, V_{GE} =15 \	/	-	117	-	nC
t _{d(on)}	Turn-on delay time	V _{CC} =600 V, I _C =50 A, V _{GE} =±15 V,		-	-	300	
tr	Rise time			-	-	200	
t _{d(off)}	Turn-off delay time	B 12 O Industrius load		-	-	600	ns ns
t _f	Fall time	R_G =13 Ω, Inductive load		-	-	300	
I _{RRM}	Repetitive peak reverse current	V _R =V _{RRM} , G-E short-circuited		-	-	1.0	mA
		I _E =50 A (Note5),	T _j =25 °C	-	1.80	2.25	
		G-E short-circuited,	T _j =125 °C	-	1.80	-	V
M	Forward valtage	(Terminal)	T _j =150 °C	-	1.80	-	
V_{F}	Forward voltage	I _E =50 A (Note5),	T _j =25 °C	-	1.70	2.15	
		G-E short-circuited,	T _j =125 °C	-	1.70	-	V
		(Chip)	T _j =150 °C	-	1.70	-	
t _{rr}	Reverse recovery time	V _{CC} =600 V, I _E =50 A, V _{GE} =±15	V _{CC} =600 V, I _E =50 A, V _{GE} =±15 V,		-	300	ns
Qrr	Reverse recovery charge	R _G =13 Ω, Inductive load		-	2.7	-	μC
Eon	Turn-on switching energy per pulse	V _{CC} =600 V, I _C =I _E =50 A,		-	5.5	-	m l
E _{off}	Turn-off switching energy per pulse	$V_{GE}=\pm 15 \text{ V}, R_{G}=13 \Omega, T_{i}=150 \text{ °C},$		-	5.3	-	mJ
Err	Reverse recovery energy per pulse	Inductive load		-	4.5	-	mJ
r _g	Internal gate resistance	-		-	0	-	Ω

< IGBT MODULES > CM100RX-24S HIGH POWER SWITCHING USE INSULATED TYPE

ELECTRICAL CHARACTERISTICS (cont; $T_j=25$ °C, unless otherwise specified) NTC THERMISTOR PART

Symbol	Item	Conditions		Llait		
		Conditions	Min.	Тур.	Max.	Unit
R ₂₅	Zero-power resistance	T _C =25 °C (Note4)	4.85	5.00	5.15	kΩ
ΔR/R	Deviation of resistance	R ₁₀₀ =493 Ω, T _C =100 °C (Note4)	-7.3	-	+7.8	%
B _(25/50)	B-constant	Approximate by equation (Note7)	-	3375	-	K
P ₂₅	Power dissipation	T _C =25 °C (Note4)	-	-	10	mW

THERMAL RESISTANCE CHARACTERISTICS

Symbol	ltore	Conditions		Limits		
	Item	Conditions	Min.	Тур.	Max.	Unit
R _{th(j-c)Q}		Junction to case, per Inverter IGBT	-	-	0.20	K/W
R _{th(j-c)D}	Thermal resistance (Note4)	Junction to case, per Inverter DIODE	-	-	0.29	IV VV
R _{th(j-c)Q}	Thermal resistance	Junction to case, per Brake IGBT	-	-	0.35	K/W
R _{th(j-c)D}		Junction to case, per Brake DIODE	-	-	0.63	I IVVV
R _{th(c-s)}	Contact thermal resistance (Note4)	Case to heat sink, per 1 module,		15	-	K/kW
		Thermal grease applied (Note7)	_	13		K/KVV

MECHANICAL CHARACTERISTICS

Cumbal	ltom	Conditions			Unit		
Symbol	Item	Conditions	Conditions		Тур.	Max.	Onit
M _t	Mounting torque	Main terminals	M 5 screw	2.5	3.0	3.5	N⋅m
Ms	Mounting torque	Mounting to heat sink	M 5 screw	2.5	3.0	3.5	N∙m
ds	Creepage distance	Terminal to terminal		10.25	-	-	mm
us		Terminal to base plate		12.32	-	-	mm
d	Clearance	Terminal to terminal		10.28		-	mm
d _a		Terminal to base plate		10.85	-	-	
m	mass	-		-	370	-	g
ес	Flatness of base plate	On the centerline X, Y (Note8)		±0	-	+100	μm

Note1. Represent ratings and characteristics of the anti-parallel, emitter-collector free wheeling diode (DIODE).

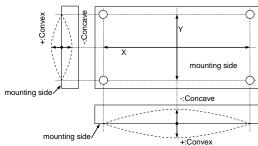
- 2. Junction temperature (T_j) should not increase beyond T_{jmax} rating.
- 3. Pulse width and repetition rate should be such that the device junction temperature (T_j) dose not exceed T_{jmax} rating.
- 4. Case temperature (T_C) and heat sink temperature (T_s) are defined on the each surface (mounting side) of base plate and heat sink just under the chips. Refer to the figure of chip location.
- Pulse width and repetition rate should be such as to cause negligible temperature rise.
 Refer to the figure of test circuit.

6.
$$B_{(25/50)} = ln(\frac{R_{25}}{R_{50}})/(\frac{1}{T_{25}} - \frac{1}{T_{50}})$$
,

 R_{25} : resistance at absolute temperature T_{25} [K]; T_{25} =25 [°C]+273.15=298.15 [K]

 R_{50} : resistance at absolute temperature T_{50} [K]; T_{50} =50 [°C]+273.15=323.15 [K]

- 7. Typical value is measured by using thermally conductive grease of λ =0.9 W/(m·K).
- 8. The base plate (mounting side) flatness measurement points (X, Y) are as follows of the following figure.



9. Use the following screws when mounting the printed circuit board (PCB) on the stand offs. " ϕ 2.6×10 or ϕ 2.6×12 self tapping screw"

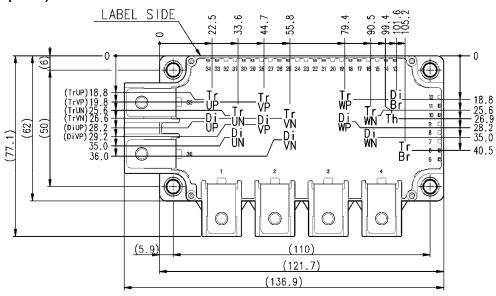
The length of the screw depends on the thickness (t1.6~t2.0) of the PCB.

RECOMMENDED OPERATING CONDITIONS

Symbol	Item	Conditions		Limits			Unit
Symbol	item			Min.	Тур.	Max.	Offic
Vcc	(DC) Supply voltage	Applied across P-N terminals	Applied across P-N terminals		600	850	V
V_{GEon}	Gate (-emitter drive) voltage	Applied across GB-EB/ G*P-E*P/G*N-E*N (*=U, V, W) terminals		13.5	15.0	16.5	V
D	External gate resistance	Per switch	Inverter IGBT	6.2	-	62	Ω
R_{G}	External gate resistance	Brake IGBT		13	-	130	1 12

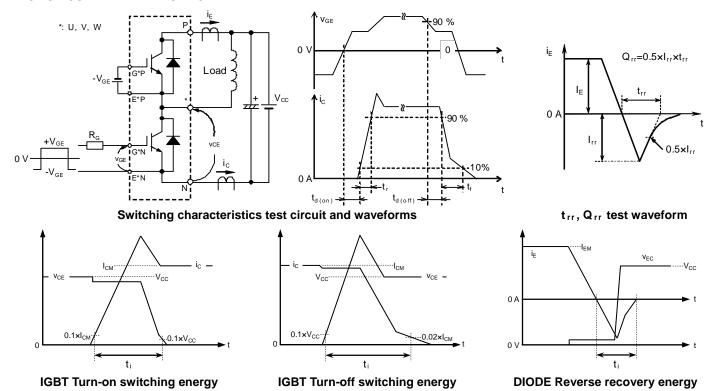
CHIP LOCATION (Top view)

Dimension in mm, tolerance: ±1 mm



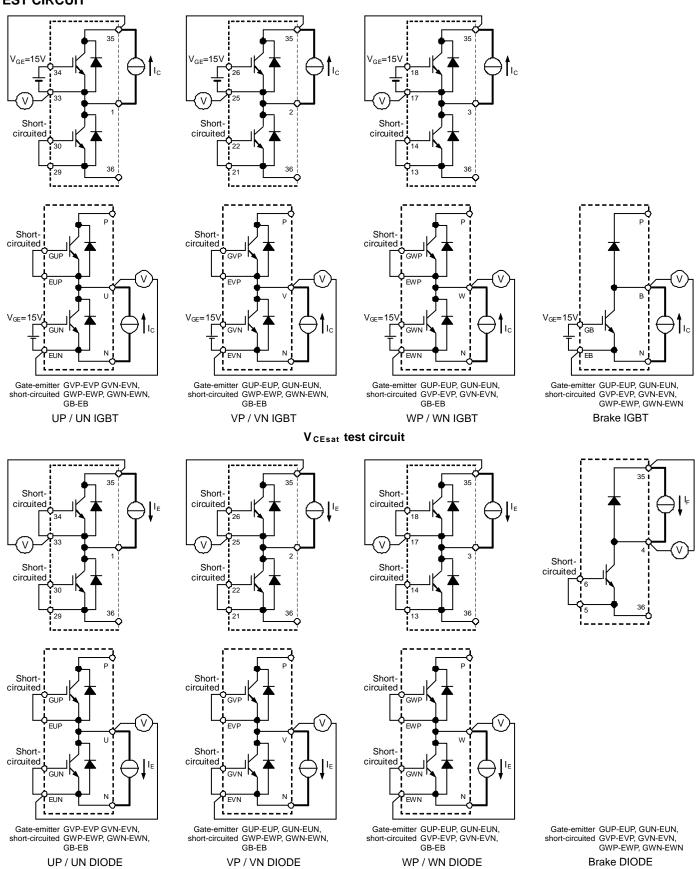
Tr*P/Tr*N/TrBr: IGBT, Di*P/Di*N: DIODE (*=U/V/W), DiBr: BRAKE DIODE, Th: NTC thermistor

TEST CIRCUIT AND WAVEFORMS



Turn-on / Turn-off switching energy and Reverse recovery energy test waveforms (Integral time instruction drawing)

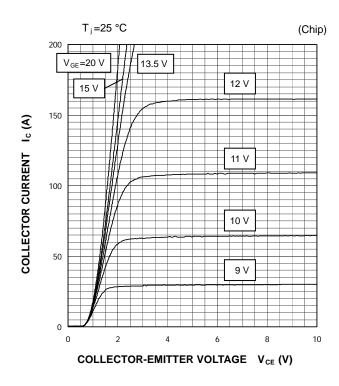
TEST CIRCUIT



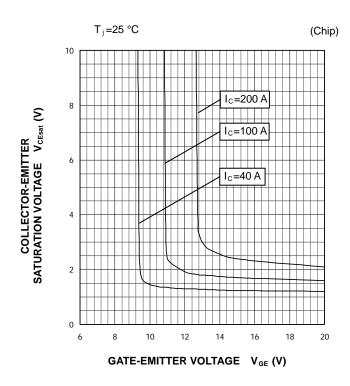
V_{EC} / V_F test circuit

PERFORMANCE CURVES INVERTER PART

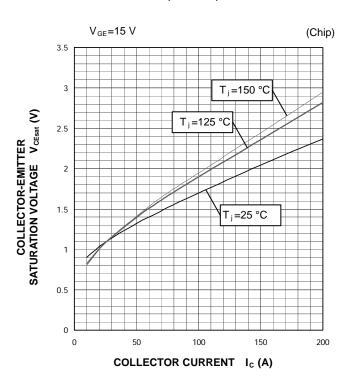
OUTPUT CHARACTERISTICS (TYPICAL)



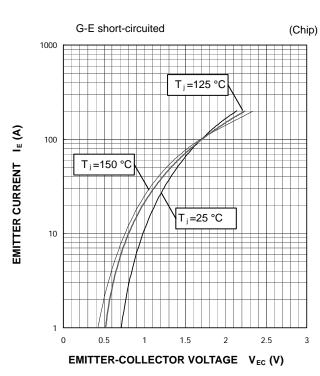
COLLECTOR-EMITTER SATURATION VOLTAGE CHARACTERISTICS (TYPICAL)



COLLECTOR-EMITTER SATURATION VOLTAGE CHARACTERISTICS (TYPICAL)



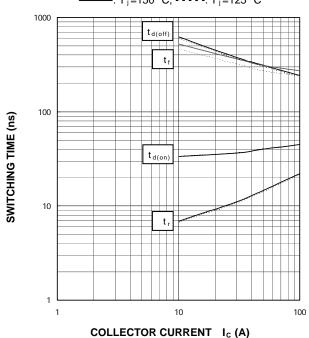
FREE WHEELING DIODE FORWARD CHARACTERISTICS (TYPICAL)



PERFORMANCE CURVES

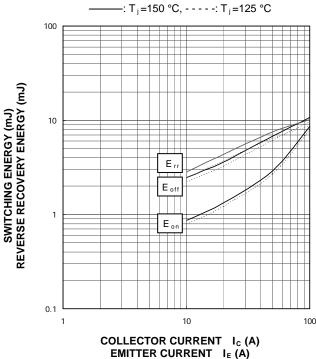
INVERTER PART

HALF-BRIDGE SWITCHING CHARACTERISTICS (TYPICAL)



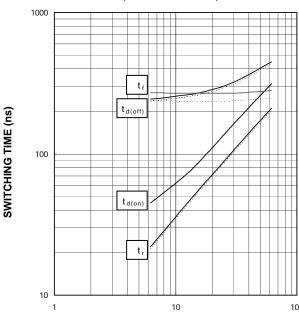
HALF-BRIDGE SWITCHING CHARACTERISTICS

(TYPICAL) V_{CC} =600 V, V_{GE} =±15 V, R_{G} =6.2 Ω , INDUCTIVE LOAD, PER PULSE



HALF-BRIDGE SWITCHING CHARACTERISTICS (TYPICAL)

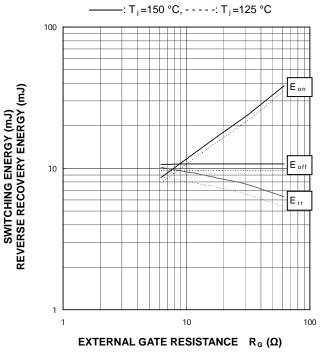
 V_{CC} =600 V, V_{GE} =±15 V, I_{C} =100 A, INDUCTIVE LOAD ...: T_{j} =150 °C, - - - - : T_{j} =125 °C



EXTERNAL GATE RESISTANCE $R_{G}(\Omega)$

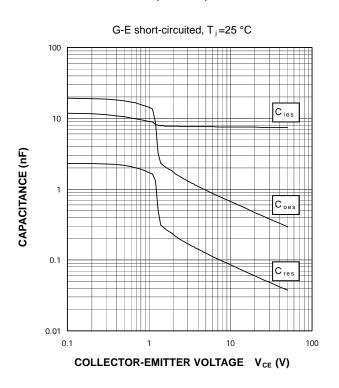
HALF-BRIDGE SWITCHING CHARACTERISTICS

(TYPICAL) V_{CC} =600 V, V_{GE} =±15 V, I_{C}/I_{E} =100 A, INDUCTIVE LOAD, PER PULSE

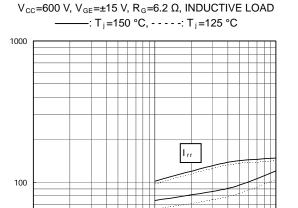


PERFORMANCE CURVES INVERTER PART

CAPACITANCE CHARACTERISTICS (TYPICAL)

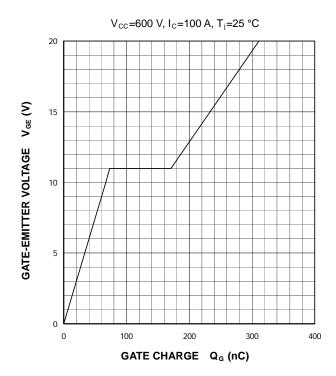


FREE WHEELING DIODE REVERSE RECOVERY CHARACTERISTICS (TYPICAL)



trr (ns), Irr (A)

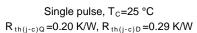
GATE CHARGE CHARACTERISTICS (TYPICAL)

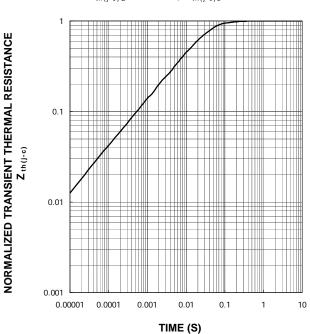


TRANSIENT THERMAL IMPEDANCE CHARACTERISTICS (MAXIMUM)

EMITTER CURRENT I_E (A)

100

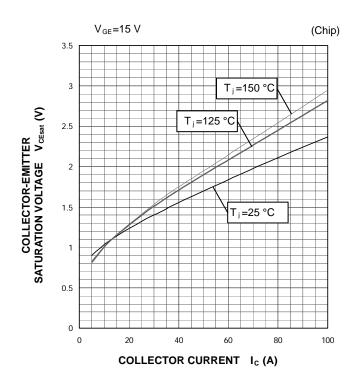




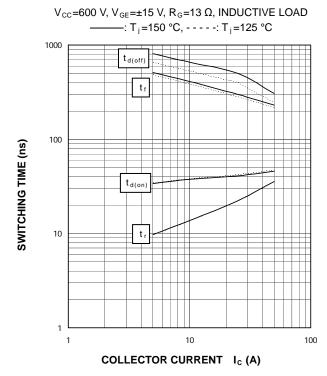
PERFORMANCE CURVES

BRAKE PART

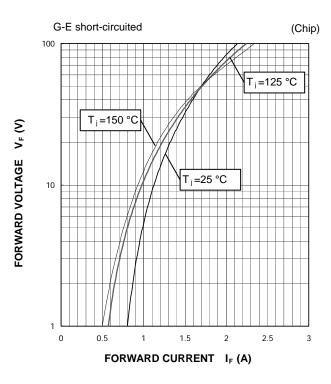
COLLECTOR-EMITTER SATURATION VOLTAGE CHARACTERISTICS (TYPICAL)



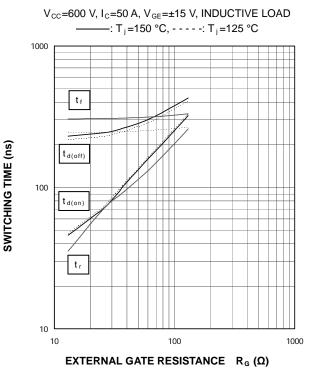
HALF-BRIDGE SWITCHING CHARACTERISTICS (TYPICAL)



CLAMP DIODE FORWARD CHARACTERISTICS (TYPICAL)



HALF-BRIDGE SWITCHING CHARACTERISTICS (TYPICAL)

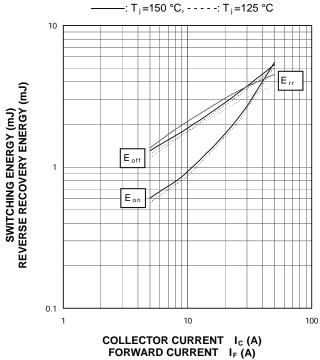


PERFORMANCE CURVES

BRAKE PART

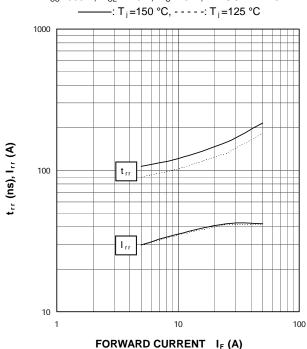
HALF-BRIDGE **SWITCHING CHARACTERISTICS**

(TYPICAL) $V_{\text{CC}}{=}600 \text{ V}, \, V_{\text{GE}}{=}{\pm}15 \text{ V}, \, R_{\text{G}}{=}13 \, \Omega,$ INDUCTIVE LOAD, PER PULSE



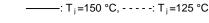
CLAMP DIODE REVERSE RECOVERY CHARACTERISTICS (TYPICAL)

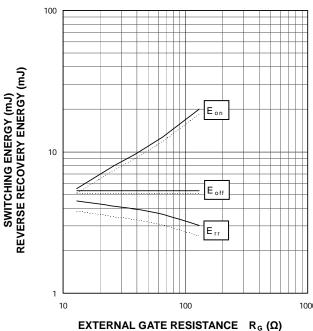
 $V_{\text{CC}}\text{=}600~\text{V},\,V_{\text{GE}}\text{=}\pm15~\text{V},\,R_{\text{G}}\text{=}13~\Omega,\,\text{INDUCTIVE LOAD}$ -: T_i=150 °C, - - - - : T_i=125 °C



HALF-BRIDGE **SWITCHING CHARACTERISTICS** (TYPICAL)

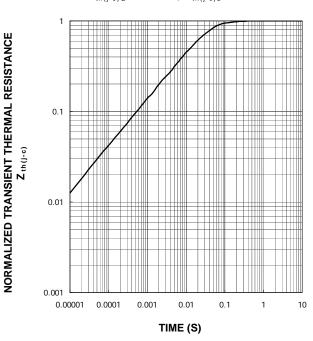
 V_{CC} =600 V, I_C/I_F =50 A, V_{GE} =±15 V, INDUCTIVE LOAD, PER PULSE





TRANSIENT THERMAL IMPEDANCE CHARACTERISTICS (MAXIMUM)

Single pulse, T_C=25 °C R $_{th(j-c)Q}$ =0.35 K/W, R $_{th(j-c)D}$ =0.63 K/W

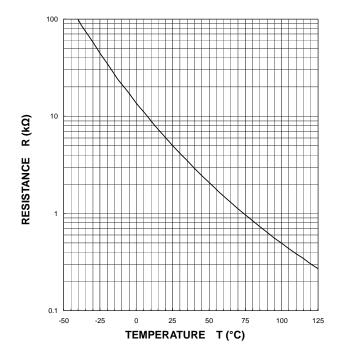


< IGBT MODULES > CM100RX-24S HIGH POWER SWITCHING USE INSULATED TYPE

PERFORMANCE CURVES

NTC thermistor part

TEMPERATURE CHARACTERISTICS (TYPICAL)



Keep safety first in your circuit designs!

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