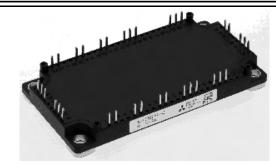


#### < IGBT MODULES >

### **CM35MXA-24S**

HIGH POWER SWITCHING USE INSULATED TYPE

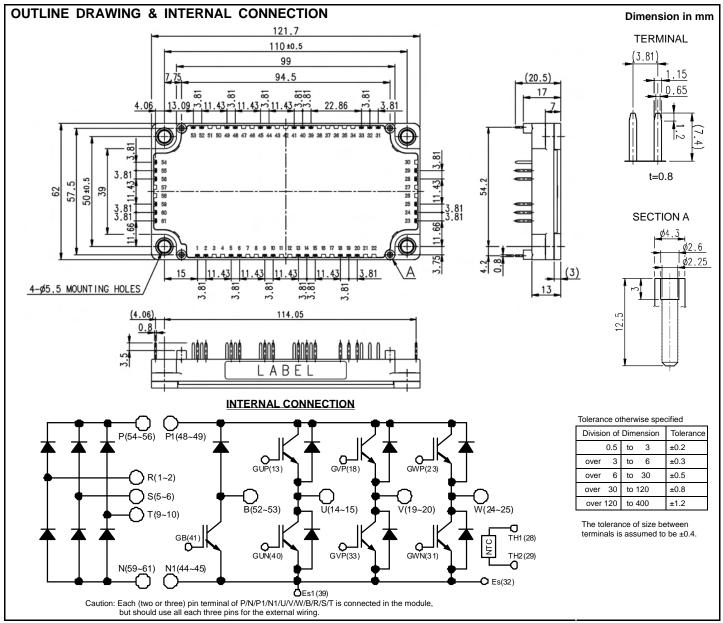


CIB (Converter+Inverter+Chopper Brake)

- Flat base Type
- Copper base plate
- •Tin plating pin terminals
- •RoHS Directive compliant
- •Recognized under UL1557, File E323585

#### **APPLICATION**

AC Motor Control, Motion/Servo Control, Power supply, etc.



# < IGBT MODULES > CM35MXA-24S HIGH POWER SWITCHING USE INSULATED TYPE

## ABSOLUTE MAXIMUM RATINGS (T $_{\rm j}$ =25 °C, unless otherwise specified) INVERTER PART IGBT/DIODE

Symbol	Item	Conditions	Rating	Unit
V <sub>CES</sub>	Collector-emitter voltage	G-E short-circuited	1200	V
$V_{GES}$	Gate-emitter voltage	C-E short-circuited	± 20	V
Ic	Collector current	DC, T <sub>C</sub> =125 °C (Note2, 4)	35	^
I <sub>CRM</sub>	Collector current	Pulse, Repetitive (Note3)	70	A
P <sub>tot</sub>	Total power dissipation	T <sub>C</sub> =25 °C (Note2, 4)	355	W
I <sub>E</sub> (Note1)	Conitto a commont	(Note2)	35	^
I <sub>ERM</sub> (Note1)	- Emitter current	Pulse, Repetitive (Note3)	70	A
T <sub>jmax</sub>	Maximum junction temperature	Instantaneous event (overload)	175	°C

#### BRAKE PART IGBT/DIODE

Symbol	Item	Conditions	Rating	Unit
V <sub>CES</sub>	Collector-emitter voltage	G-E short-circuited	1200	V
$V_{GES}$	Gate-emitter voltage	C-E short-circuited	± 20	V
Ic	Collector current	DC, T <sub>C</sub> =125 °C (Note2, 4)	35	^
I <sub>CRM</sub>	Collector current	Pulse, Repetitive (Note3)	70	A
P <sub>tot</sub>	Total power dissipation	T <sub>C</sub> =25 °C (Note2, 4)	355	W
$V_{RRM}$	Repetitive peak reverse voltage	G-E short-circuited	1200	V
I <sub>F</sub>	Forward current	(Note2)	35	Α
I <sub>FRM</sub>	1 Orward Current	Pulse, Repetitive (Note3)	70	
T <sub>jmax</sub>	Maximum junction temperature	Instantaneous event (overload)	175	°C

#### **CONVERTER PART DIODE**

Symbol	Item	Conditions	Rating	Unit
$V_{RRM}$	Repetitive peak reverse voltage	-	1600	V
Ea	Recommended AC input voltage	RMS	440	V
Io	DC output current	3-phase full wave rectifying, T <sub>C</sub> =125 °C (Note4)	35	Α
I <sub>FSM</sub>	Surge forward current	The sine half wave 1 cycle peak value, f=60 Hz, non-repetitive	350	А
l <sup>2</sup> t	Current square time	Value for one cycle of surge current	510	A <sup>2</sup> s
T <sub>jmax</sub>	Maximum junction temperature	Instantaneous event (overload)	150	°C

#### **MODULE**

Symbol	Item	Conditions	Rating	Unit
Visol	Isolation voltage	Terminals to base plate, RMS, f=60 Hz, AC 1 min	2500	V
T <sub>Cmax</sub>	Maximum case temperature	(Note4)	125	°C
T <sub>jop</sub>	Operating junction temperature	Continuous operation (under switching)	-40 ~ +150	°C
T <sub>stg</sub>	Storage temperature	-	-40 ~ +125	C

#### **MECHANICAL CHARACTERISTICS**

Symbol	Item	Conditions		Limits			
Symbol	item	Conditions	Min.	Тур.	Max.	Unit	
Ms	Mounting torque	Mounting to heat sink M 5 screw	2.5	3.0	3.5	N∙m	
d	Creepage distance	Terminal to terminal	6.47	-	-	mm	
d <sub>s</sub>		Terminal to base plate	14.27	-	-		
d	Clearance	Terminal to terminal	6.47	-	-		
d <sub>a</sub>		Terminal to base plate	12.33	-	-	mm	
m	mass	-	-	300	-	g	
ес	Flatness of base plate	On the centerline X, Y (Note5)	±0	-	+100	μm	

# < IGBT MODULES > CM35MXA-24S HIGH POWER SWITCHING USE INSULATED TYPE

## ELECTRICAL CHARACTERISTICS (T $_{j}$ =25 °C, unless otherwise specified) INVERTER PART IGBT/DIODE

Symbol	ltom	Conditions			Limits		Unit
Symbol	Item	Conditions		Min.	Тур.	Max.	Offic
I <sub>CES</sub>	Collector-emitter cut-off current	V <sub>CE</sub> =V <sub>CES</sub> , G-E short-circuited		-	-	1.0	mA
I <sub>GES</sub>	Gate-emitter leakage current	V <sub>GE</sub> =V <sub>GES</sub> , C-E short-circuited		-	-	0.5	μΑ
V <sub>GE(th)</sub>	Gate-emitter threshold voltage	I <sub>C</sub> =3.5 mA, V <sub>CE</sub> =10 V		5.4	6.0	6.6	V
		I <sub>C</sub> =35 A (Note6),	T <sub>j</sub> =25 °C	-	1.80	2.25	
		V <sub>GE</sub> =15 V,	T <sub>j</sub> =125 °C	-	2.00	-	V
\/	Collector amitter acturation valtage	(Terminal)	T <sub>j</sub> =150 °C	-	2.05	-	
$V_{CEsat}$	Collector-emitter saturation voltage	I <sub>C</sub> =35 A (Note6),	T <sub>j</sub> =25 °C	-	1.70	2.15	
		V <sub>GE</sub> =15 V,	T <sub>j</sub> =125 °C	-	1.90	-	V
		(Chip)	T <sub>j</sub> =150 °C	-	1.95	-	
Cies	Input capacitance			-	-	3.5	
Coes	Output capacitance	V <sub>CE</sub> =10 V, G-E short-circuited		-	-	0.7	nF
Cres	Reverse transfer capacitance		-	-	0.06	1	
Q <sub>G</sub>	Gate charge	V <sub>CC</sub> =600 V, I <sub>C</sub> =35 A, V <sub>GE</sub> =15 V			82	-	nC
t <sub>d(on)</sub>	Turn-on delay time	V <sub>CC</sub> =600 V, I <sub>C</sub> =35 A, V <sub>GE</sub> =±15 V,		-	-	300	200
tr	Rise time			-	-	200	
t <sub>d(off)</sub>	Turn-off delay time	B. 40.0 lastesticates			-	600	- ns -
t <sub>f</sub>	Fall time	$R_G$ =18 Ω, Inductive load		-	-	300	
		I <sub>E</sub> =35 A (Note6) , T <sub>j</sub> =25 °C		-	1.80	2.25	
		G-E short-circuited,	T <sub>j</sub> =125 °C		1.80	-	V
(Note1)		(Terminal)	T <sub>i</sub> =150 °C	-	1.80	-	
$V_{\text{EC}}^{ (\text{Note1})}$	Emitter-collector voltage	I <sub>E</sub> =35 A (Note6),	T <sub>i</sub> =25 °C	-	1.70	2.15	
		G-E short-circuited,	T <sub>j</sub> =125 °C		1.70	-	V
		(Chip)	T <sub>j</sub> =150 °C		1.70	-	1
t <sub>rr</sub> (Note1)	Reverse recovery time	$V_{CC}$ =600 V, $I_{E}$ =35 A, $V_{GE}$ =±15	V,	-	-	300	ns
Q <sub>rr</sub> (Note1)	Reverse recovery charge	R <sub>G</sub> =18 Ω, Inductive load		ı	1.9	-	μC
Eon	Turn-on switching energy per pulse	V <sub>CC</sub> =600 V, I <sub>C</sub> =I <sub>E</sub> =35 A,		-	4.2	-	I
E <sub>off</sub>	Turn-off switching energy per pulse	$V_{GE}=\pm 15 \text{ V}, R_{G}=18 \Omega, T_{i}=150 \text{ °C},$		-	3.7	-	mJ
E <sub>rr</sub> (Note1)	Reverse recovery energy per pulse	Inductive load		-	3.5	-	mJ
R <sub>CC'+EE'</sub>	Internal lead resistance	Main terminals-chip, per switch,  T <sub>C</sub> =25 °C (Note4)		-	-	5.7	mΩ
r <sub>g</sub>	Internal gate resistance	Per switch		-	0	-	Ω

#### **BRAKE PART IGBT/DIODE**

Committee of	Item	Conditions	Conditions		Limits		
Symbol	item	Conditions		Min.	Тур.	Max.	Unit
I <sub>CES</sub>	Collector-emitter cut-off current	V <sub>CE</sub> =V <sub>CES</sub> , G-E short-circuited		-	-	1.0	mA
I <sub>GES</sub>	Gate-emitter leakage current	V <sub>GE</sub> =V <sub>GES</sub> , C-E short-circuited		-	-	0.5	μΑ
$V_{GE(th)}$	Gate-emitter threshold voltage	$I_C=3.5$ mA, $V_{CE}=10$ V		5.4	6.0	6.6	V
	Collector-emitter saturation voltage	$I_C=35 A$ (Note6),	T <sub>j</sub> =25 °C	-	1.80	2.25	
		V <sub>GE</sub> =15 V,	T <sub>j</sub> =125 °C	-	2.00	-	V
V		(Terminal)	T <sub>j</sub> =150 °C	-	2.05	-	
V <sub>CEsat</sub>		$I_C=35 A$ (Note6),	T <sub>j</sub> =25 °C	-	1.70	2.15	
		V <sub>GE</sub> =15 V,	T <sub>j</sub> =125 °C	-	1.90	-	V
		(Chip)	T <sub>j</sub> =150 °C	-	1.95	-	
Cies	Input capacitance			-	-	3.5	
Coes	Output capacitance	V <sub>CE</sub> =10 V, G-E short-circuited		-	-	0.7	nF
Cres	Reverse transfer capacitance	1		-	-	0.06	
Q <sub>G</sub>	Gate charge	$V_{CC}$ =600 V, $I_{C}$ =35 A, $V_{GE}$ =15 V		-	82	-	nC

# < IGBT MODULES > CM35MXA-24S HIGH POWER SWITCHING USE INSULATED TYPE

## ELECTRICAL CHARACTERISTICS (cont.; $T_j=25$ °C, unless otherwise specified) BRAKE PART IGBT/DIODE

Symbol	Item	Conditions		Limits			Unit
Symbol	item	Conditions		Min.	Тур.	Max.	Offic
t <sub>d(on)</sub>	Turn-on delay time	V -600 V I -35 A V -115	\/	-	-	300	
tr	Rise time	$V_{CC}$ =600 V, $I_{C}$ =35 A, $V_{GE}$ =±15	ν,	-	-	200	no
t <sub>d(off)</sub>	Turn-off delay time	D 19 O Industive lead		-	-	600	ns
t <sub>f</sub>	Fall time	$R_G$ =18 Ω, Inductive load		-	-	300	
I <sub>RRM</sub>	Reverse current	V <sub>R</sub> =V <sub>RRM</sub> , G-E short-circuited		-	-	1.0	mA
V		I <sub>F</sub> =35 A (Note6),	T <sub>j</sub> =25 °C	-	1.80	2.25	
		G-E short-circuited,	T <sub>j</sub> =125 °C	-	1.80	-	V
	Forward voltage	(Terminal)	T <sub>j</sub> =150 °C	-	1.80	-	
$V_{F}$		I <sub>F</sub> =35 A (Note6),	T <sub>j</sub> =25 °C	-	1.70	2.15	
		G-E short-circuited,	T <sub>j</sub> =125 °C	-	1.70	-	V
		(Chip)	T <sub>j</sub> =150 °C	-	1.70	-	]
trr	Reverse recovery time	V <sub>CC</sub> =600 V, I <sub>F</sub> =35 A, V <sub>GE</sub> =±15	V,	-	-	300	ns
Q <sub>rr</sub>	Reverse recovery charge	R <sub>G</sub> =18 Ω, Inductive load		-	1.9	-	μC
Eon	Turn-on switching energy per pulse	V <sub>CC</sub> =600 V, I <sub>C</sub> =I <sub>F</sub> =35 A,		-	4.2	-	I
E <sub>off</sub>	Turn-off switching energy per pulse	$V_{GE}=\pm 15 \text{ V}, R_{G}=18 \Omega, T_{j}=150 \text{ °C},$		-	3.7	-	mJ
Err	Reverse recovery energy per pulse	Inductive load		-	3.5	-	mJ
r <sub>g</sub>	Internal gate resistance	-		-	0	-	Ω

#### **CONVERTER PART DIODE**

Symbol	ltem	Conditions	Limits			Unit
			Min.	Тур.	Max.	Offic
I <sub>RRM</sub>	Reverse current	V <sub>R</sub> =V <sub>RRM</sub> , T <sub>j</sub> =150 °C	-	-	4.0	mA
V <sub>F</sub> (Terminal)	Forward voltage	I <sub>F</sub> =35 A <sup>(Note6)</sup>	-	1.2	1.6	V

#### NTC THERMISTOR PART

Symbol	Item	Conditions		Unit		
	item		Min.	Тур.	Max.	Offic
R <sub>25</sub>	Zero-power resistance	T <sub>C</sub> =25 °C (Note4)	4.85	5.00	5.15	kΩ
ΔR/R	Deviation of resistance	R <sub>100</sub> =493 Ω, T <sub>C</sub> =100 °C (Note4)	-7.3	-	+7.8	%
B <sub>(25/50)</sub>	B-constant	Approximate by equation (Note7)	-	3375	-	K
P <sub>25</sub>	Power dissipation	T <sub>C</sub> =25 °C (Note4)	-	-	10	mW

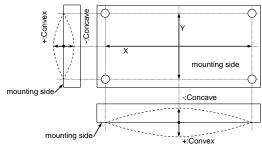
#### THERMAL RESISTANCE CHARACTERISTICS

Symbol	Item	Conditions	Limits			Unit
Symbol	item	Conditions	Min.	Тур.	Max.	Offic
R <sub>th(j-c)Q</sub>		Junction to case, per Inverter IGBT	-	=	0.42	K/W
R <sub>th(j-c)D</sub>	Thermal resistance (Note4)	Junction to case, per Inverter DIODE	-	-	0.69	IV/VV
R <sub>th(j-c)Q</sub>		Junction to case, per Brake IGBT	-	-	0.42	K/W
R <sub>th(j-c)D</sub>		Junction to case, per Brake DIODE	-	-	0.69	TV/VV
R <sub>th(j-c)D</sub>		Junction to case, per Converter DIODE	-	-	0.45	K/W
R <sub>th(c-s)</sub> Contact thermal resista	Contact thormal registance (Note4)	Case to heat sink, per 1 module,		15		K/kW
	Contact thermal resistance	Thermal grease applied (Note8)	-	13	-	I IVKVV

## < IGBT MODULES > CM35MXA-24S HIGH POWER SWITCHING USE INSULATED TYPE

Note1. Represent ratings and characteristics of the anti-parallel, emitter-collector free wheeling diode (DIODE).

- 2. Junction temperature ( $T_j$ ) should not increase beyond  $T_{jmax}$  rating.
- 3. Pulse width and repetition rate should be such that the device junction temperature (T<sub>i</sub>) dose not exceed T<sub>imax</sub> rating.
- 4. Case temperature (T<sub>C</sub>) and heat sink temperature (T<sub>s</sub>) are defined on the each surface (mounting side) of base plate and heat sink just under the chips. Refer to the figure of chip location.
- 5. The base plate (mounting side) flatness measurement points (X, Y) are as follows of the following figure.



- Pulse width and repetition rate should be such as to cause negligible temperature rise. Refer to the figure of test circuit.
- 7.  $B_{(25/50)} = ln(\frac{R_{25}}{R_{50}})/(\frac{1}{T_{25}} \frac{1}{T_{50}})$ ,

 $R_{25}$ : resistance at absolute temperature  $T_{25}$  [K];  $T_{25}$  =25 [°C]+273.15=298.15 [K]

 $R_{50}$ : resistance at absolute temperature  $T_{50}$  [K];  $T_{50}$ =50 [°C]+273.15=323.15 [K]

- 8. Typical value is measured by using thermally conductive grease of λ=0.9 W/(m·K).
- 9. Use the following screws when mounting the printed circuit board (PCB) on the stand offs.

"φ2.6×10 or φ2.6×12 self tapping screw"

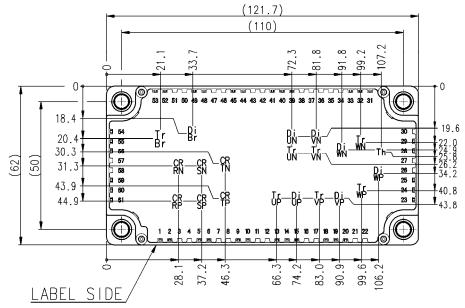
The length of the screw depends on the thickness (t1.6~t2.0) of the PCB.

#### RECOMMENDED OPERATING CONDITIONS

TTEOO!!!!!	DED OF ERAFING COMPITIONS						
Symbol	Item	Conditions	Conditions		Limits		
Symbol		Conditions		Min.	Тур.	Max.	Unit
V <sub>cc</sub>	(DC) Supply voltage	Applied across P-N/P1-N1 terminals		=	600	850	V
V <sub>GEon</sub>	Gate (-emitter drive) voltage	Applied across GB-Es/ G*P-*/G*N-Es(*=U, V, W) terminals		13.5	15.0	16.5	V
D. Futamal nata	External data registence	Per switch	Inverter IGBT	18	-	180	Ω
R <sub>G</sub>	R <sub>G</sub> External gate resistance	Brake IGBT		18	-	180	] 12

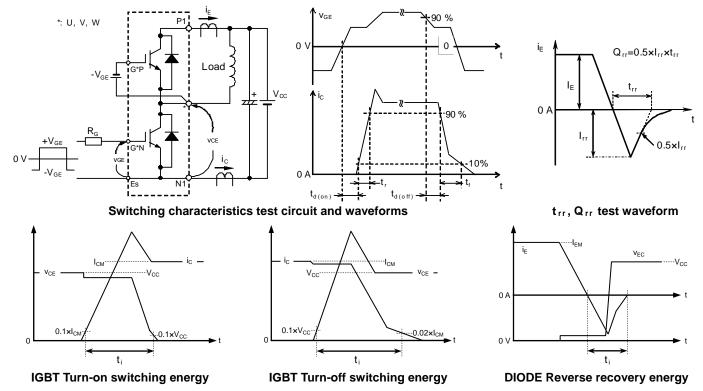
#### **CHIP LOCATION (Top view)**

Dimension in mm, tolerance: ±1 mm

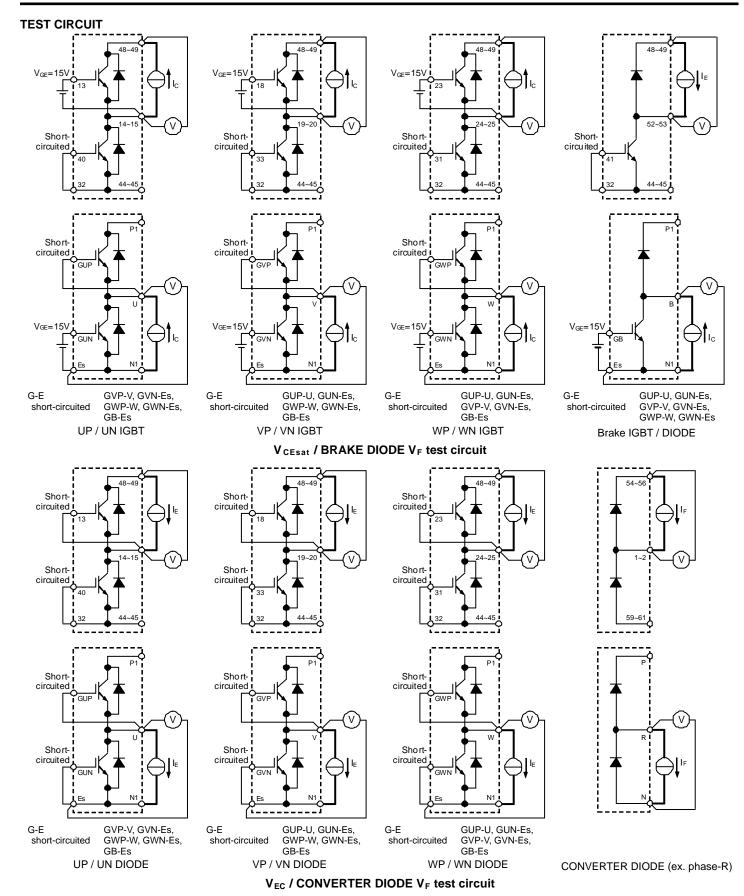


Tr\*P/Tr\*N/TrBr: IGBT, Di\*P/Di\*N: DIODE (\*=U/V/W), DiBr: BRAKE DIODE, CR\*P/CR\*N: CONVERTER DIODE (\*=R/S/T), Th: NTC thermistor

#### **TEST CIRCUIT AND WAVEFORMS**



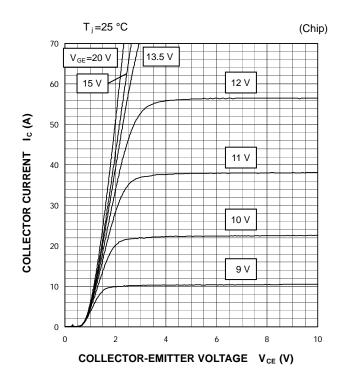
Turn-on / Turn-off switching energy and Reverse recovery energy test waveforms (Integral time instruction drawing)



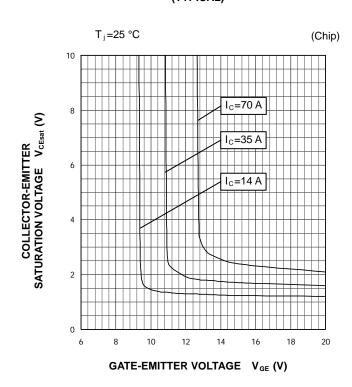
<sup>\*</sup> In the above test circuit, should use all three main pin terminals (P1/N1/P/N/U/V/W) for connection with the terminals and the current source.

**INVERTER PART** 

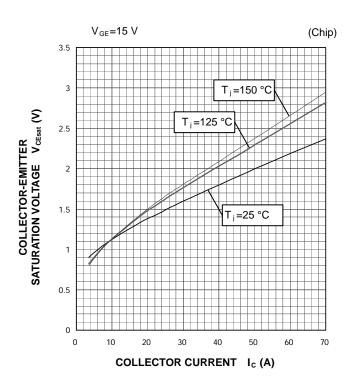
## OUTPUT CHARACTERISTICS (TYPICAL)



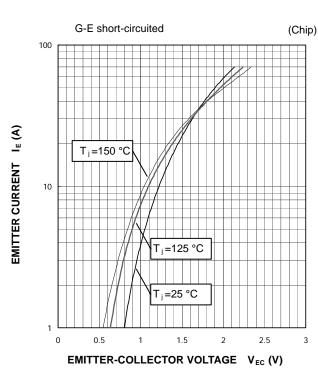
#### COLLECTOR-EMITTER SATURATION VOLTAGE CHARACTERISTICS (TYPICAL)



#### COLLECTOR-EMITTER SATURATION VOLTAGE CHARACTERISTICS (TYPICAL)



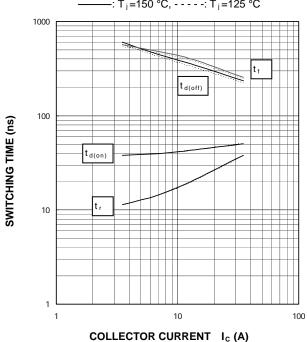
#### FREE WHEELING DIODE FORWARD CHARACTERISTICS (TYPICAL)



#### **INVERTER PART**

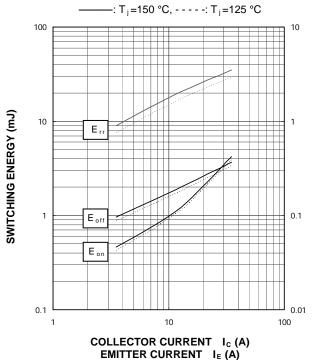
#### HALF-BRIDGE SWITCHING CHARACTERISTICS (TYPICAL)

 $V_{CC}$ =600 V,  $V_{GE}$ =±15 V,  $R_{G}$ =18 Ω, INDUCTIVE LOAD  $T_{i}$ =150 °C, ----:  $T_{i}$ =125 °C



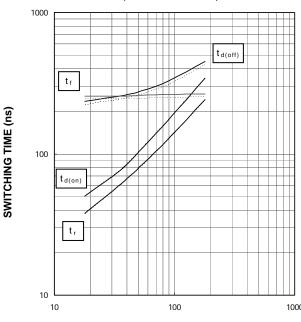
#### HALF-BRIDGE SWITCHING CHARACTERISTICS (TYPICAL)

 $V_{CC}$ =600 V,  $V_{GE}$ =±15 V,  $R_{G}$ =18  $\Omega$ , INDUCTIVE LOAD, PER PULSE



#### HALF-BRIDGE SWITCHING CHARACTERISTICS (TYPICAL)

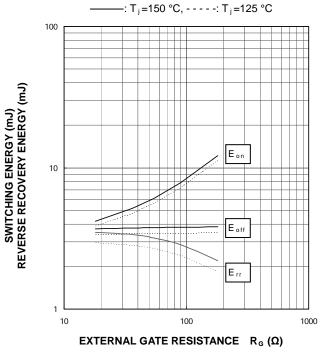
V<sub>CC</sub>=600 V, V<sub>GE</sub>=±15 V, I<sub>C</sub>=35 A, INDUCTIVE LOAD ———: T<sub>j</sub>=150 °C, - - - - : T<sub>j</sub>=125 °C



EXTERNAL GATE RESISTANCE  $R_{G}(\Omega)$ 

#### HALF-BRIDGE SWITCHING CHARACTERISTICS (TYPICAL)

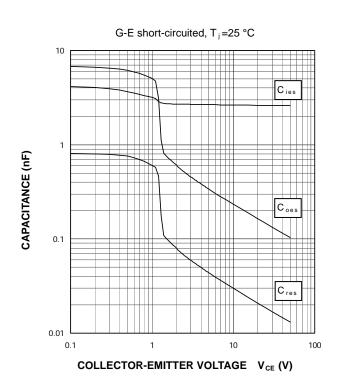
V<sub>CC</sub>=600 V, V<sub>GE</sub>=±15 V, I<sub>C</sub>=35 A, INDUCTIVE LOAD, PER PULSE



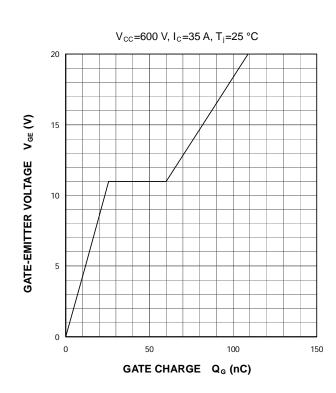
REVERSE RECOVERY ENERGY (mJ)

#### **INVERTER PART**

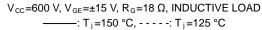
### CAPACITANCE CHARACTERISTICS (TYPICAL)

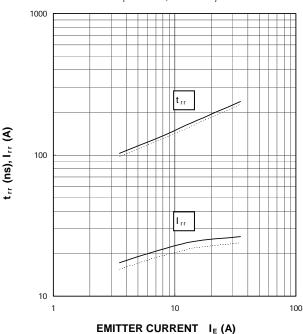


## GATE CHARGE CHARACTERISTICS (TYPICAL)



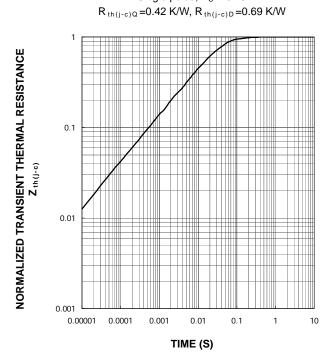
## FREE WHEELING DIODE REVERSE RECOVERY CHARACTERISTICS (TYPICAL)





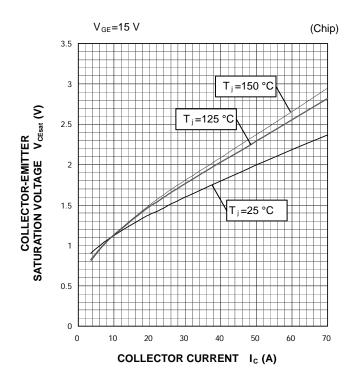
## TRANSIENT THERMAL IMPEDANCE CHARACTERISTICS (MAXIMUM)

Single pulse, T<sub>C</sub>=25 °C

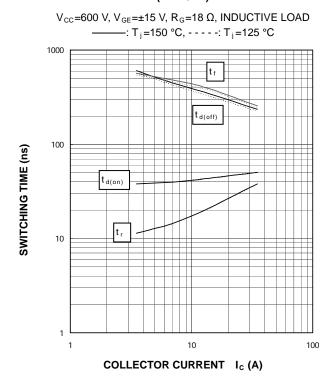


#### **BRAKE PART**

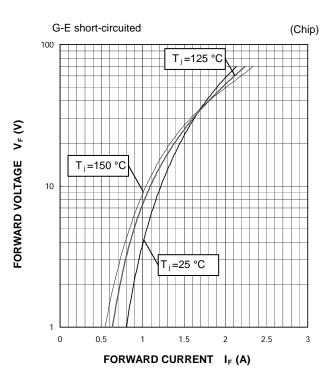
#### COLLECTOR-EMITTER SATURATION VOLTAGE CHARACTERISTICS (TYPICAL)



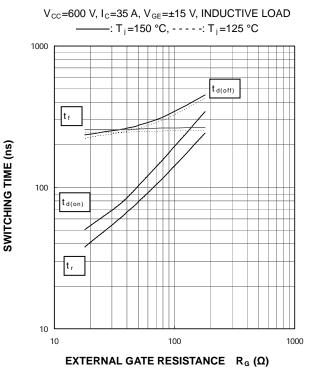
#### HALF-BRIDGE SWITCHING CHARACTERISTICS (TYPICAL)



#### CLAMP DIODE FORWARD CHARACTERISTICS (TYPICAL)



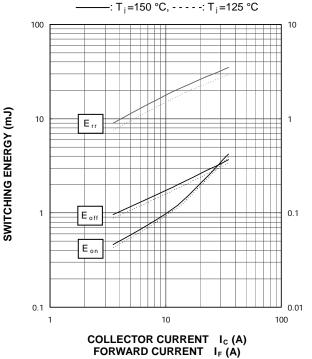
#### HALF-BRIDGE SWITCHING CHARACTERISTICS (TYPICAL)



#### **BRAKE PART**

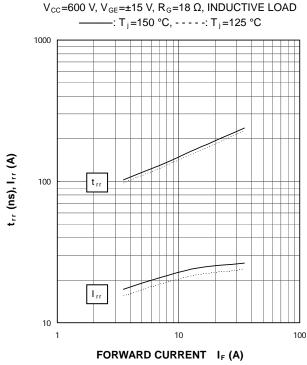
#### HALF-BRIDGE SWITCHING CHARACTERISTICS (TYPICAL)

 $V_{CC}$ =600 V,  $V_{GE}$ =±15 V,  $R_{G}$ =18  $\Omega$ , INDUCTIVE LOAD, PER PULSE



## CLAMP DIODE

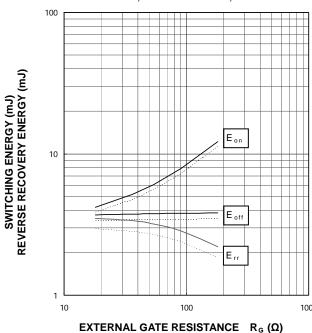
## REVERSE RECOVERY CHARACTERISTICS (TYPICAL)



#### HALF-BRIDGE SWITCHING CHARACTERISTICS (TYPICAL)

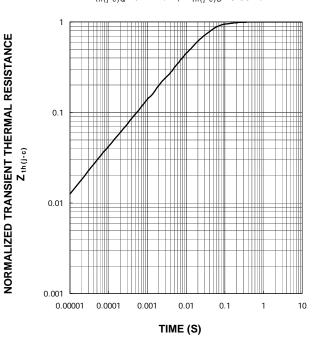
 $V_{CC}$ =600 V,  $I_{C}/I_{F}$ =35 A,  $V_{GE}$ =±15 V, INDUCTIVE LOAD, PER PULSE





### TRANSIENT THERMAL IMPEDANCE CHARACTERISTICS (MAXIMUM)

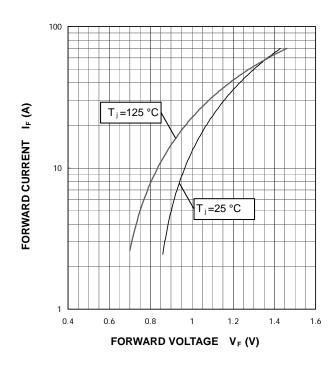
Single pulse, T  $_{\rm C}$  =25 °C R  $_{\rm th(j-c)Q}$  =0.42 K/W, R  $_{\rm th(j-c)D}$  =0.69 K/W



REVERSE RECOVERY ENERGY (mJ)

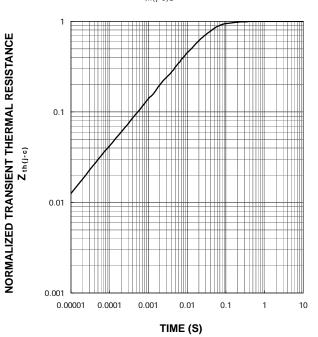
#### **CONVERTER PART**

CONVERTER DIODE FORWARD CHARACTERISTICS (TYPICAL)



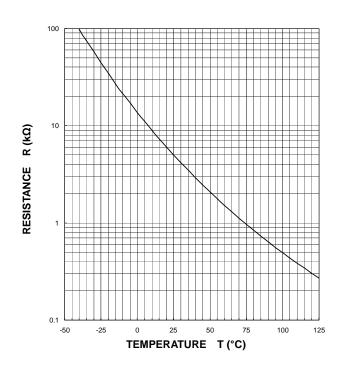
## TRANSIENT THERMAL IMPEDANCE CHARACTERISTICS (MAXIMUM)

Single pulse,  $T_C=25$  °C  $R_{th(j-c)D}=0.45$  K/W



#### NTC thermistor part

TEMPERATURE CHARACTERISTICS (TYPICAL)



#### Keep safety first in your circuit designs!

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