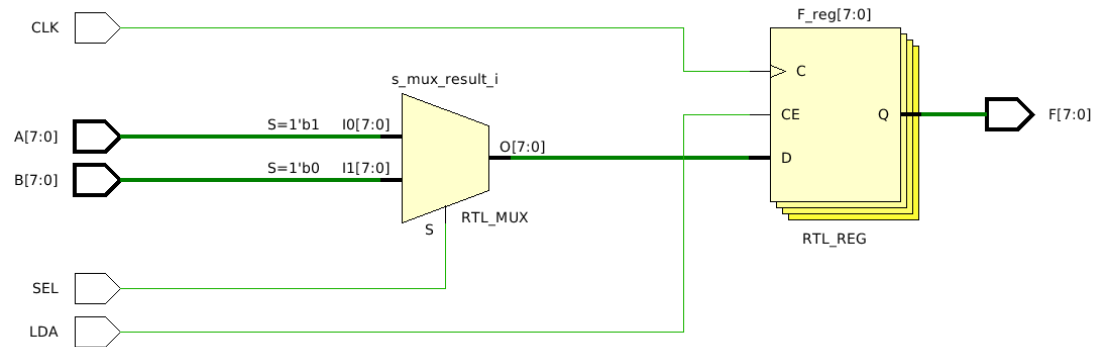
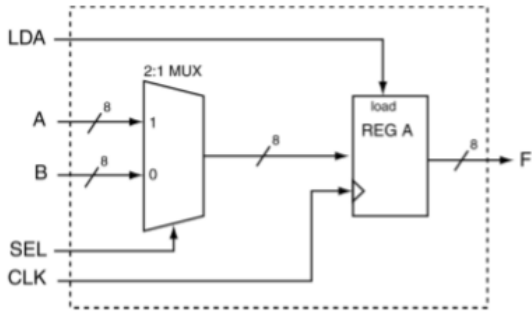
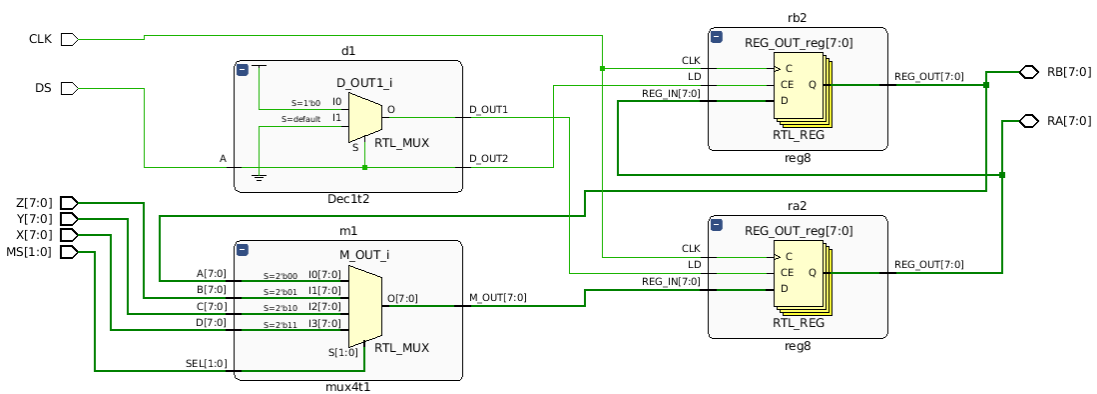
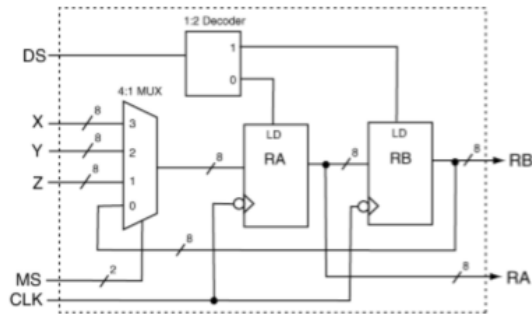


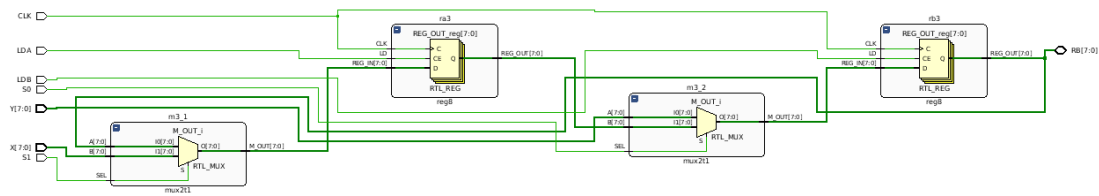
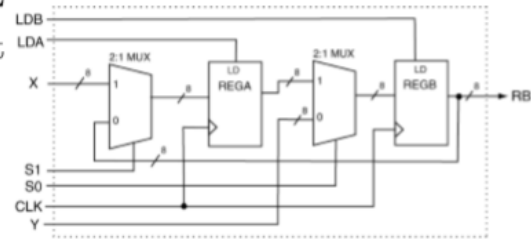
EXERCISE 1. Provide a VHDL model that can be used to implement the following circuit.



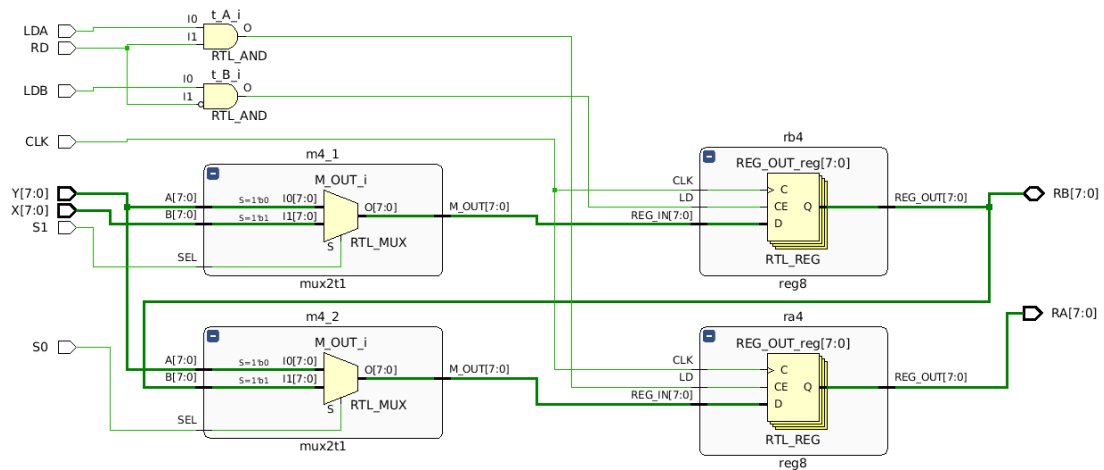
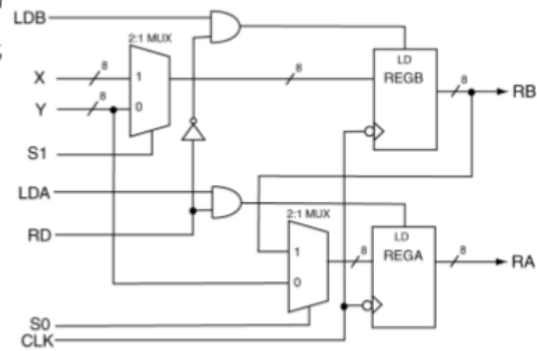
EXERCISE 2. Provide a VHDL model that can be used to implement the following circuit.



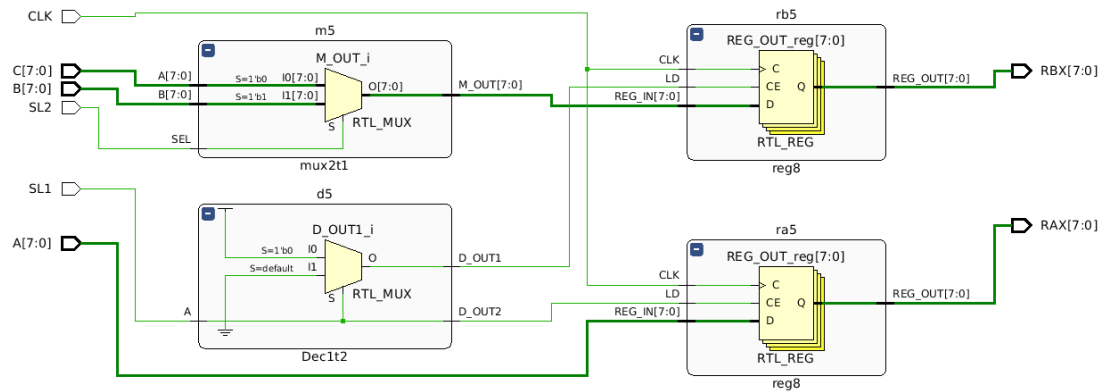
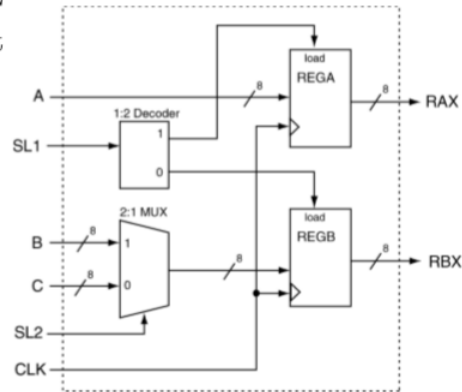
EXERCISE 3. Provide a VHDL model that can be used to implement the following circuit.



EXERCISE 4. Provide a VHDL model that can be used to implement the following circuit.



EXERCISE 5. Provide a VHDL model that can be used to implement the following circuit.



EXERCISE 6. Provide a VHDL model that can be used to implement the following circuit.

