Dedicated Pin	44-Pin PLCC	44-Pin TQFP	100-Pin TQFP
INPUT/GCLK1	43	37	87
INPUT/GCLRn	1	39	89
INPUT/OE1	44	38	88
INPUT/OE2/GCLK2	2	40	90
TDI (1)	7	1	4
TMS (1)	13	7	15
TCK (1)	32	26	62
TDO (1)	38	32	73
GNDINT	22, 42	16, 36	38, 86
GNDIO	10, 17, 30, 36	4, 11, 24, 30	11, 26, 33, 43, 53, 59, 65, 74, 78, 95
VCCINT (3.3 V Only)	3, 23	17, 41	39, 91
VCCIO (2.5 V or 3.3 V)	15, 35	9, 29	3, 18, 34, 51, 66, 82
No Connect (N.C.)	–	–	1, 2, 5, 7, 22, 24, 27, 28, 49, 50, 55, 70, 72, 77
Total User I/O Pins (2)	34	34	66

LAB	MC	44-Pin PLCC	44-Pin TQFP	100-Pin TQFP
A	1	12	6	14
A	2	–	–	13
A	3	11	5	12
A	4	9	3	10
A	5	8	2	9
A	6	–	–	8
A	7	–	–	6
A	8	7 (1)	1 (1)	4 (1)
A	9	–	–	100
A	10	–	–	99
A	11	6	44	98
A	12	–	–	97
A	13	–	–	96
A	14	5	43	94
A	15	–	–	93
A	16	4	42	92
B	17	21	15	37
B	18	–	–	36
B	19	20	14	35
B	20	19	13	–
B	21	18	12	32
B	22	–	–	31
B	23	–	–	30
B	24	–	–	29
B	25	16	10	25
B	26	–	–	23
B	27	–	–	21
B	28	–	–	20
B	29	–	–	19
B	30	14	8	17
B	31	–	–	16
B	32	13 (1)	7 (1)	15 (1)
C	33	24	18	40
C	34	–	–	41
C	35	25	19	42
C	36	26	20	44
C	37	27	21	45
C	38	–	–	46
C	39	–	–	47
C	40	28	22	48
C	41	29	23	52
C	42	–	–	54
C	43	–	–	56
C	44	–	–	57
C	45	–	–	58
C	46	31	25	60
C	47	–	–	61
C	48	32 (1)	26 (1)	62 (1)

LAB	MC	44-Pin PLCC	44-Pin TQFP	100-Pin TQFP
D	49	33	27	63
D	50	–	–	64
D	51	34	28	–
D	52	–	–	67
D	53	37	31	68
D	54	–	–	69
D	55	–	–	71
D	56	38 (1)	32 (1)	73 (1)
D	57	39	33	75
D	48	–	–	76
D	59	–	–	79
D	60	–	–	80
D	61	–	–	81
D	62	40	34	83
D	63	–	–	84
D	64	41	35	85

Notes:
(1)   This pin may function as either a JTAG port or a user I/O pin. When the device is configured to use the JTAG ports for in-system programming, this pin is not available as a user I/O pin.
(2)   The user I/O pin count includes dedicated input pins and all I/O pins.