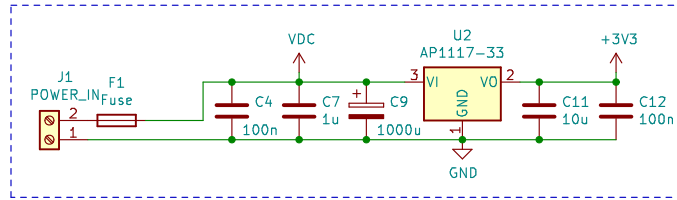
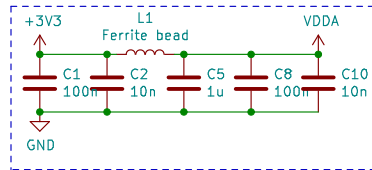


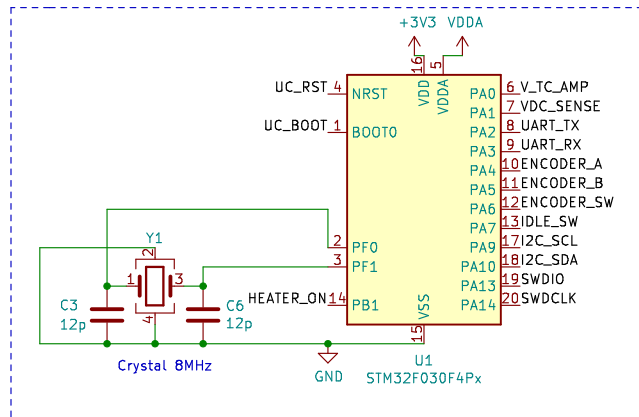
Power Input and 3V3 reg



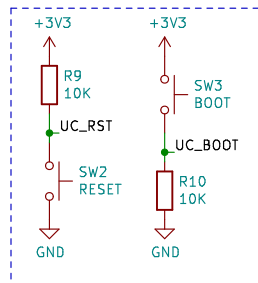
CPU power bypass and filter



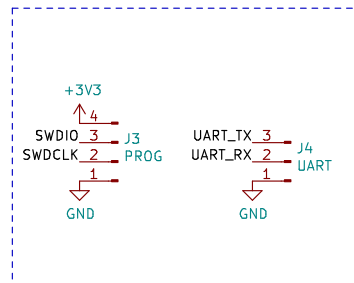
CPU



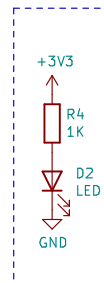
Reset / Boot buttons



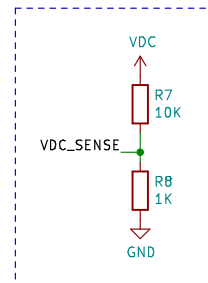
Programming / debug headers



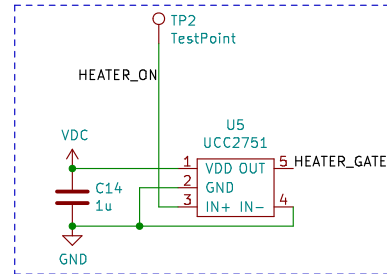
Power LED



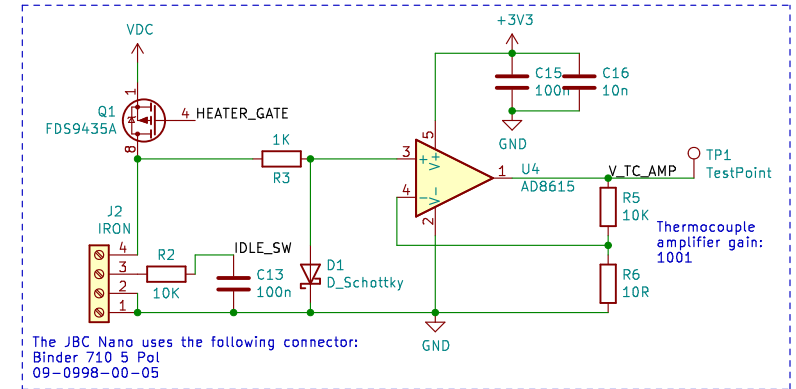
Input voltage measurement



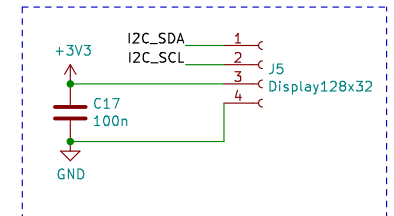
Gate driver and level shifter



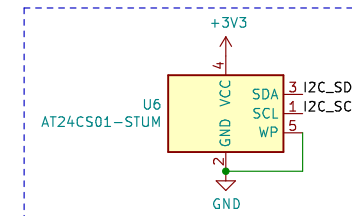
Power output and thermocouple amplifier



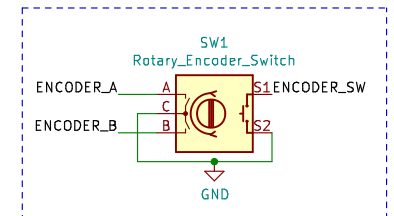
Display connector



Settings EEPROM



User input rotary encoder



Sheet: /

File: JBC_Nano_Hardware.kicad_sch

Title: JBC Nano Control PCB by Daniel Frejek (Winkelkatze)

Size: A4

Date:

KiCad E.D.A. kicad (6.0.4)

Rev: 1.1

Id: 1/1