九九一年四月十九日

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新聞稿PRESS RELEASE

括擔任 其設計系統與工具。著名的簡化指令系統計算機結構 **高地、聖荷西、蘇黎世及東京實驗室的研究發展計劃** 教授又曾負責統籌 IBM 世界各地研究實驗室包括約克鎮 進行研究工作,並在 范教授在IBM 任職 Computer Architecture) 即為該部門所發展。范 IBM 微系統主任,負責最先進微型處理機工作及 H 研究部門歷任行政要職,包

辨公室主任,主持發展數值密集計算科技,其範疇包括超 為萬國商業機器公司(IBM) 級計算機、小型超高速計算機、超級小型計算機以及先進 的工作站及私人計算機。 座教授及工科課程行政院長, 范教授於一九八九年獲中文大學聘任為訊息工程學講 數值密集計算(NIC)加入中大服務之前, 范教授 計劃

范教授對電腦科技的發展加以回額,並論述電腦對社會的有重大影響,同時為資訊的傳遞提供迅捷而方便的功能。范教授在講座中指出電腦的發展對訊息的編排及分類 (十九日) 在該校主持就職演講,主題為「資訊、通訊及 香港中文大學訊息工程學講座教授范季融教授於昨 例如私隱權 、應用進展、 人造智能及人造環境等。

Professor George J Fan, Professor of Information Engineering at The Chinese niversity of Hong Kong gave his Professorial Inaugural Lecture yesterday (April 19) on 'Information, Communication and Computers".

廿多年,分別在湯瑪士屈臣研究中

In his lecture, Professor Fan pointed out that the development of computer had made profound impact on the generation and categorization of information and facilitated the communication of information among entities. Professor Fan further reviewed the development of computer focusing on its use in supporting human endeavors and its impact Issues such as privacy, application evolution, artificial intelligence and artificial environment were also discussed.

Professor Fan was appointed as Professor of Information Engineering and Administrative Dean of Engineering of CUHK in 1989. Before joining the University, Professor Fan was Director of Numerical Intensive Computing (NIC) project office in IBM, working with senior executives on plans to emphasize the NIC area, which covers supercomputer, minisuper, supermini, advanced workstation and PC.

Prior to that Professor Fan had done research in Thomas Watson Research Centre for nearly twenty years and held several executive assignments in IBM research, including the head of the research planning organization for worldwide IBM research labs, and overseeing the technical and financial plans for labs in Yorktown Heights, San Jose, Zurich and Tokyo. He was the Director of Microsystem in IBM, responsible for the most advanced microprocessor work and design system and tools. One of the groups in this section developed the now well known RISC Computer Architecture.