= dx32+fx2+bx3+hx3+9x2+bx2+9x2+(x2+ex7+bx2+ex2+ hx2+9x+dx+fx+(2+d2+e2+9+9+h

_				
	dof	a@b@g	boeoh	cedoe
	bOh	CØJ	a odof	a sysh
	\sim			
		\	L	

```
Rot(UCI) = 0101 0010
     Sub(Ra(ucu)) = 000/ 1010
      W(2) = 10111101 @1000 0000 @ 0001 1010
        = 0010 0111
     WC3] = WCI) &WC2] = 80100101 @ 8010 0111
       = 0000 0010
     W[4] = W[2] @ RCON(2) @ Sub(Rot(UC3D))
     Rot (UC3)) = 0010 0000
     Sub(Rot(WE3))= 1010 1001
     W[4] = 001001110 0010000 1010 1001
      = 1011 1110
     W[5] = W(3)@W[4] = 00000010@1011110
      = 1011 1100
 K = 1011 1101 0010 0101
  K1= 0010 0111 0000 0010
  K2 = 1011 1110 1011 1100
 K= 101/110/00/00/00/00/00/00/11/0000 00/0 10/1/110
     1011 1101
ii)
      1011/10100100101 -> 111/11001000001
```

iii) AkoosrionsioAccesik, oMCiosrioNSioAkz

= CT, = 1100 1111 1000 0101

CT, DAL = U111000100111001@ 1011111010111100