

PRODUCT : LCD MODULE
MODEL NO . : QD281801
SUPPLIER :QDtech
DATE :March21,2018

# **SPECIFICATION**

Revion:1.1

# QD281801

For Customer's Acceptance			
Approved by	Comment		

	Signature	Date
Prepared by		
Checked by		
Approved by		

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## 1 General Description

**QD281801** is a transmissive type a-Si TFT-LCD (amorphous silicon thin film transistor liquid crystal display) module, which is composed of a TFT-LCD panel, a driver circuit a backlight unit, The panel size is 2.8 inch and thresolution is 240x320. High image quality a-Si TFT LCD module. Partial-screen display function is available. Sleep and Stand-by modes are available for power saving.

### 1.1 Features

No	Item	Specification	Remark
1	Display Mode	Normally Black	
2	Screen Size	2.8inch (diagonal)	
3	Resolution	240XRGBX320	
4	Color Number	262K	
5	Color Arrangement	RGB-stripe	
6	Driver IC ILI9341V		
7	Back Light	White LED*4	
8	Viewing Direction	12O'CLOCK	
9	Interface	SPI 4W	
10	Surface Treatment	UV Cut	

## 1.2 Application

- Mobile phone.
- Portable multimedia device.

## **2** Outline Dimension

## The mechanical detail is shown in Fig. 1 and summarized in Table 1 below.

Parameter	Specifications	Unit
Outline dimensions	50 (W) x69.2(H) x3.5+-0.1(D) (LCM,no include FPC)	mm
Active area	43.2(W) x57.6(H)	mm
Resolution	240(H)RGBx 320(V) dots	-
Dot size	0.153(H) ×0.153	mm
Module brightness	260	cd/m²

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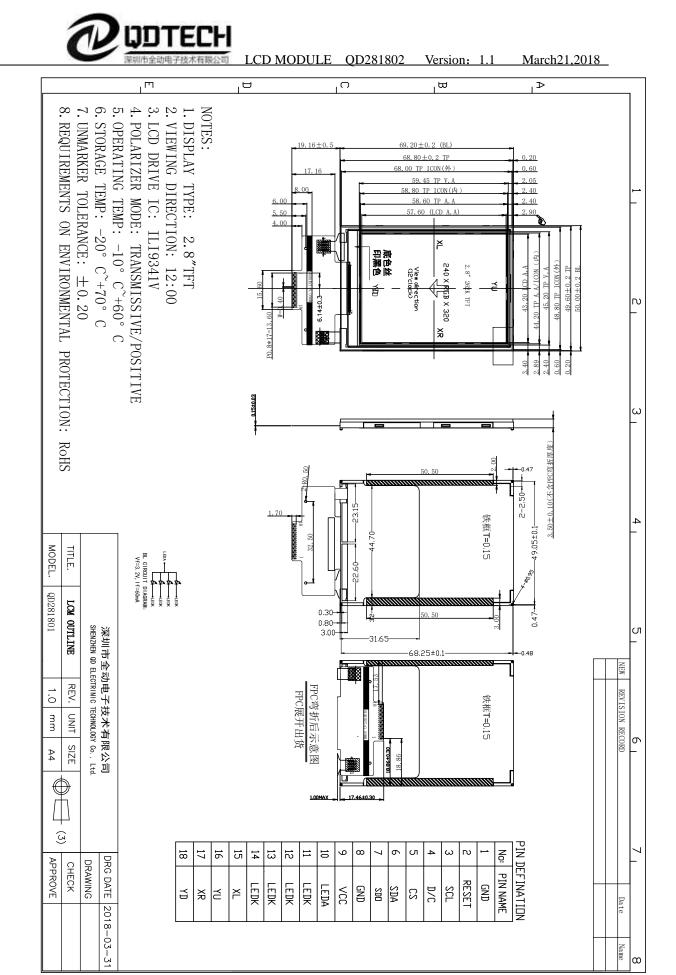


Figure 1: Module specification of the module

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## 3 Electrical Characteristics

## 3.1 TFT-LCD Module

Item	Item Symbo -		Value	Unit	Notes	
Item	Symbo	Min	Тур	Max	Omt	Notes
Supply Voltage for logic	Vcc	2.5	2.8	3.3	V	
Supply voltage for logic	Vci	2.5	2.8	3.3	v	
TFT Gate ON Voltage	VGH *	11.5		19	V	
TFT Gate OFF Voltage	VGL	-15.5		-7	V	
Operating temperature	Тор	-20		+70	$^{\circ}$ C	
Storage temperature	Tst	-30		+80	$^{\circ}$	

Ta=25  $^{\circ}$ C

## 3.2 Back-Light Unit

Item	Symbol	Min.	Тур.	Max.	Unit	Remark
Current	IF		60	80	mA	
Forward voltage	VF	3.0	3.2	3.4	V	IF=80mA
Chaoma	X	0.240		0.28		VF=3.2V
Chroma	Y	0.250		0.29		
Brightness	L	4500			Cd/m2	
Uniformity	UBL	80			%	

- 12 LED
- The luminous intensity of LED is strongly dependent on the driving current.
- It is recommended the input of backlight to be constant current rather than constant voltage.

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## **TFT-LCM** Interface Specification

1	GND	Ground				
2	RESET	Reset signal input Pin				
3	SCL	Second Data lane in 2 data lane serial interface.				
4	D/C	This pin is used to be serial interface clock.				
5	CS	Chip select input pin.				
6	SDA	The data is latched on the rising edge of the SCL signal.				
7	SDO	The data is outputted on the falling edge of the SCL signal				
8	GND	Ground				
9	VCC	Power supply input for LCM:2.8V				
10	LED-A	Anode pin of backlight				
11~14	LED-K	Cathode pin OF backlight				
15	XL	X-				
16	YU	Y+				
17	XR	X+				
18	YD	Y-				

## 6 Description of Interface' Signal

## **6.1 RGB Interface Timing Charateristics**

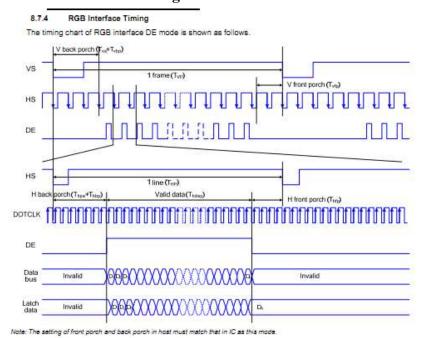


Figure 28 Timing Chart of Signals in RGB Interface DE Mode

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## **6.2 DC Characteristics**

### 7.2 DC Characteristics

Parameter	Symbol	Condition	S	pecification	on	Unit	Related
Farameter	Symbol	Condition	MIN.	TYP.	MAX.	Unit	Pins
		Power & Operation	n Voltage				
System Voltage	VDD	Operating voltage	2.5	2.8	4.8	٧	
Interface Operation Voltage	VDDI	I/O Supply Voltage	1.65	1.8	3.3	٧	
Gate Driver High Voltage	VGH		11.5	8	18	V	Note 4
Gate Driver Low Voltage	VGL		-7.6		-12	V	8
Gate Driver Supply Voltage		VGH-VGL	258		30	V	Note 5
		Input / Outp	ut	ci 1			
Logic-High Input Voltage	VIH		0.7VDDI	8 8	VDDI	V	Note 1
Logic-Low Input Voltage	VIL		VSS		0.3VDDI	V	Note 1
Logic-High Output Voltage	VOH	IOH = -1.0mA	0.8VDDI		VDDI	V	Note 1
Differential Input High Threshold Voltage	VIT+			0	50	mV	
Differential Input Low Threshold Voltage	VIT-		-50	0		mV	MIPI_CLK
Single-ended Receiver Input Operation Voltage Range	VIR		0.5		1.2	٧	** ***
Logic-Low Output Voltage	VOL	IOL = +1.0mA	VSS	S S	0.2VDDI	V	Note 1
Logic-High Input Current	IIH	VIN = VDDI		8 3	1	uА	Note 1
Logic-Low Input Current	IIL	VIN = VSS	-1			uA	Note 1
Input Leakage Current	BL	IOH = -1.0mA	-0.1		0.1	uА	Note 1
		VCOM Volta	ge	C1		,	0.1
VCOM amplitude	VCOM			VSS		V	
	30 e	Source Driv	er	G S			10a -07
Gamma Reference Voltage(Positive)	VAP		4.4		6.4	٧	
Gamma Reference Voltage(Negative)	VAN		-2.6		-4.6	V	8
Source Output Settling Time	Tr	Below with 99% precision			10	us	Note 2

Table 2 Basic DC Characteristics

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## **6.3 AC Characteristics**

### RGB Interface Characteristics :

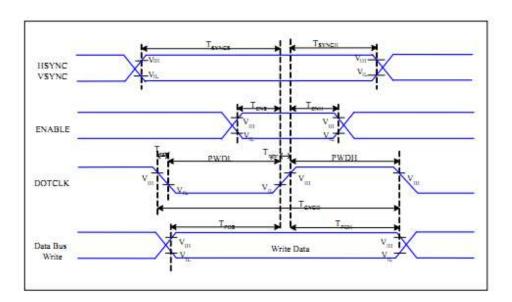


Figure 4 RGB Interface Timing Characteristics

VDDI=1.8,VDD=2.8, AGND=DGND=0V, Ta=25 ℃

Signal	Symbol	Parameter	MIN	MAX	Unit	Description
HSYNC, VSYNC	T <sub>SYNCS</sub>	VSYNC, HSYNC Setup Time	5	@ () @	ns	
ENTER E	Tens	Enable Setup Time	5	12 "	ns	
ENABLE	T <sub>ENH</sub>	Enable Hold Time	5	3 3	ns	
	PWDH	DOTCLK High-level Pulse Width	13	2 12 13	ns	
	PWDL	DOTCLK Low-level Pulse Width	13	· ·	ns	
DOTCLK	Toyco	DOTCLK Cycle Time	28	2 × 3	ns	
	Trghr, Trghf	DOTCLK Rise/Fall time		15	ns	
	Teos	PD Data Setup Time	5	.53	ns	
DB	Трон	PD Data Hold Time	5	2 <u>.</u> 5	ns	

Table 7 18/16 Bits RGB Interface Timing Characteristics

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#### **Reset Timing** 6.4

#### 7.5.6 Reset Timing:

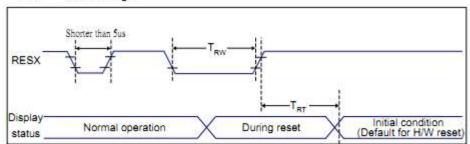


Figure 10 Reset Timing

VDDI=1.8, VDD=2.8, AGND=DGND=0V, Ta=25 ℃

Related Pins	Symbol	Parameter	MIN	MAX	Unit
	TRW	Reset pulse duration	10	, E	UB
RESX	TRT	81513	2	5 (Note 1, 5)	ms
	IRI	Reset cancel	20	120(Note 1, 6, 7)	ms

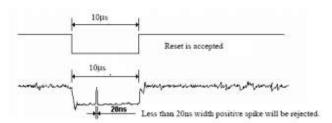
Table 10 Reset Timing

#### Notes:

- 1. The reset cancel includes also required time for loading ID bytes, VCOM setting and other settings from NVM (or similar device) to registers. This loading is done every time when there is HW reset cancel time (IRT) within 5 ms after a rising edge of RESX.
  - 2. Spike due to an electrostatic discharge on RESX line does not cause irregular system reset according to the table below:

RESX Pulse	Action		
Shorter than 5us	Reset Rejected		
Longer than 9us	Reset		
Between 5us and 9us	Reset starts		

- 3. During the Resetting period, the display will be blanked (The display is entering blanking sequence, which maximum time is 120 ms, when Reset Starts in Sleep Out -mode. The display remains the blank state in Sleep In -mode.) and then return to Default condition for Hardware Reset.
  - 4. Spike Rejection also applies during a valid reset pulse as shown below:



- 5. When Reset applied during Sleep In Mode.
- 6. When Reset applied during Sleep Out Mode.
- 7. It is necessary to wait 5msec after releasing RESX before sending commands. Also Sleep Out command cannot be sent for 120msec

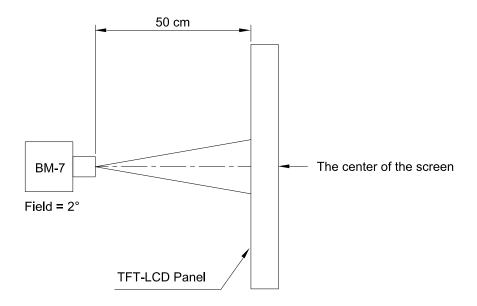
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## 7. Optical Specification

Note 1: The brightness test equipment setup

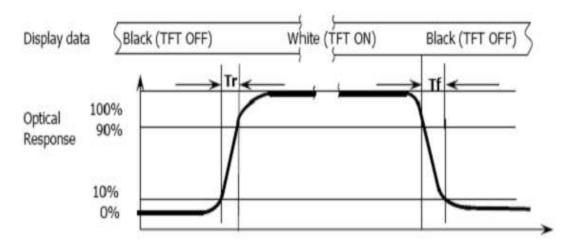
I<sub>B</sub>=60mA, Field=2° (As measuring "black" image, field=2° is the best testing condition.)



Note 2: Definition of contrast ratio (C.R)

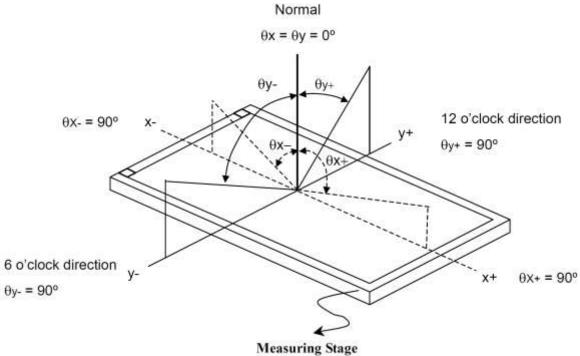
Note 3: Definition of response

time

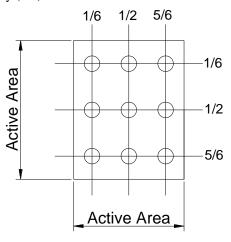


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Note 4: Definition of viewing angle



Note 5: Definition of uniformity (Un)



$$Un = \frac{Bmin}{Bmax} \times 100\%$$

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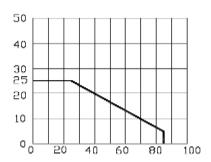


## 8 Environment Absolute Maximum Ratings

Item	Symbol	Min	Max	Unit	Remark
Operation temperature range	Тор	-20	70	$^{\circ}\! \mathbb{C}$	Ambient
Storage temperature range	Tst	-30	80	$^{\circ}\!$	Ambient

- Corrosive gas environment is not acceptable.
- TFT-LCD color will change slightly depending on environment temperature. This phenomenon is reversible. Current reduction rate of LED backlight is according to the graph indicated below:





Allowable Forward Current (mA)

## 9 Reliability Test Items

Test Item	Test Condition		
High Temperature Operation	70℃ for 24 hours		
Low Temperature Operation	-20℃ for 24 hours		
High Temperature Storage	70℃ for 24 hours		
Low Temperature Storage	-40℃ for 24 hours		
High Temperature Operation Humidity Operation	50℃, 95%RH for 48 hours		
Thermal Shock	-30°storage one hour,rise to 70° within 15s,high temperature one hour, drop to 30° within 15s, circulate ten repeatedly		
Vibration Test (No Operation)	Frequency: 10~55Hz Amplitude:1.0mm Sweep Time: 11min Test Period: 6 Cycles for each direction of X, Y, Z		
Static electricity test	Touch 6KV, air touch 10KV		

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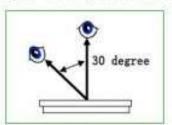


#### **10** Inspection Standard

This standard apply to TFT module specification.

1. Inspection condition:

Under daylight lamp 20~40W, product distance inspector eye 30cm.incline degree 30".



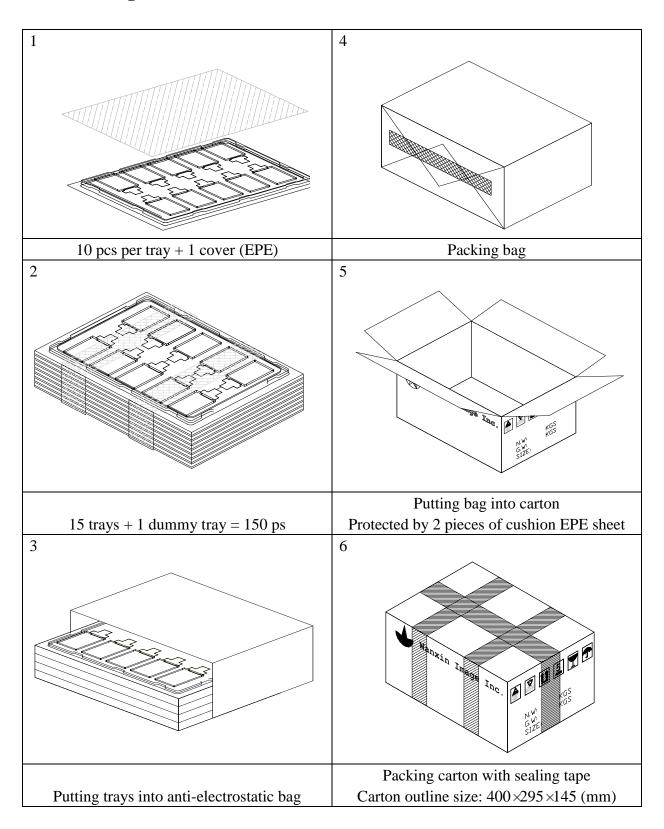
### 2. Inspection standard

NO.	Item	Inspection standard			Rate
******		Case of Dot defect is below  ① Bright Dot (whit spot): "0" ② Dark Dot (black spot): "0" (In case of Dark Dot on Main TFT LCD)  - NG if there's full Dot defect Damaged less than the size of sub-pixel is not counted as defect  - Dots darker than the size of sub-pixel are not defined as bright dot defect			
2.1 Dot	area size (mm)		cceptable number		
	Φ≤0.10		ignore		
		0.10<Ф	≤0.15	3	
		0.15<Φ	≤0.20	2	
		0.25<Ф	≤0.25	1	
		0.25<	СФ	0	
2.2 line	Si	ize (mm)	Acceptable number		
		ignore	W≤0.03	ignore	
	L≤4.0	0.03 <w≤0.04< td=""><td>2</td><td></td></w≤0.04<>	2		
		L≤4.0	0.04 <w≤0.05< td=""><td>1</td><td></td></w≤0.05<>	1	
			0.05 <w< td=""><td>Treat with dot non-conformance</td><td></td></w<>	Treat with dot non-conformance	

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## 11 Package



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### 12 Precautions

Please pay attentions to the followings as using the LCD module.

### 12.1Handling

- (a) Do not apply strong mechanical stress like drop, shock or any force to LCD module. It may cause improper operation, even damage.
- (b) Because the polarizer is very fragile and easy to be damaged, do not hit, press or rub the display surface with hard materials.
- (c) Do not put heavy or hard material on the display surface, and do not stack LCD modules.
- (d) If the display surface is dirty, please wipe the surface softly with cotton swab or clean cloth.
- (e) Avoid using Ketone type materials (e.g. Acetone), Toluene, Ethyl acid or Methyl chloride to clean the display surface. It might damage the touch panel surface permanently. The recommended solvents are water and Isopropyl alcohol.
- (f) Wipe off water droplets or oil immediately.
- (g) Protect the LCD module from ESD. It will damage the LSI and the electronic circuit.
- (h) Do not touch the output pins directly with bare hands.
- (i) Do not disassemble the LCD module.
- (j) Do not lift the FPC of Touch Panel.

### 12.2Storage

- (a) Do not leave the LCD modules in high temperature, especially in high humidity for a long time.
- (b) Do not expose the LCD modules to sunlight directly.
- (c) The liquid crystal is deteriorated by ultraviolet. Do not leave it in strong ultraviolet ray for a long time.
- (d) Avoid condensation of water. It may cause improper operation.
- (e) Please stack only up to the number stated on carton box for storage and transportation. Excessive weight will cause deformation and damage of carton box.

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## 12.3Operation

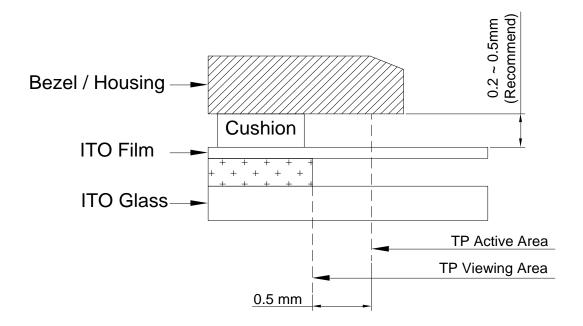
- (a) When mounting or dismounting the LCD modules, turn the power off.
- (b) Protect the LCD modules from electric shock.
- (c) The Driver IC control algorithms stated above should always obeyed to avoid damaging the LSI and electronic circuit.
- (d) Be careful to avoid mixing up the polarity of power supply for backlight.
- (e) Absolute maximum rating specified above has to be always kept in any case. Exceeding it may cause non-recoverable damage of electronic components or, nevertheless, burning.
- (f) When a static image is displayed for a long time, remnant image is likely to occur.
- (g) Be sure to avoid bending the FPC to an acute shape, it might break FPC.
- (h) Most of the touch screens have air vent to equalize the inside air pressure to the outside one. The air vent must be open and liquid contact must be avoided as the liquid may be absorbed if the liquid is accumulated near the air vent.
- (i) For the fragility of ITO film, it should avoid to use too tapering pen as the input material.

### 12.4Touch Panel Mounting Notes

- (a) If a cushion is used between bezel/housing and film must be choose as free as enough to absorb the expansion and contraction to avoid the distortion of film.
- (b) The cushion must be placed out of the Viewing Area.
- (c) Bezel/Housing edge must be posited between Key Area and Viewing Area. The edge enters the Key Area may cause unexpected input if the gap is too narrow or foreign particles like dusts exist between Bezel/Housing and ITO film.
- (d) Mounting example:

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The corner part has conductivity. Do not touch any metal part after mounting.

### **12.50thers**

- a) If the liquid crystal leaks from the panel, it should be kept away from the eyes or mouth.
- b) For the fragility of polarizer, it is recommended to attach a transparent protective plate over the display surface.
- c) It is recommended to peel off the protection film on the polarizer slowly so that the electrostatic charge can be minimized.

## 13 Records of Version

Version	Revise Date	Page	Content
A	2018-04-04	All	New released

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