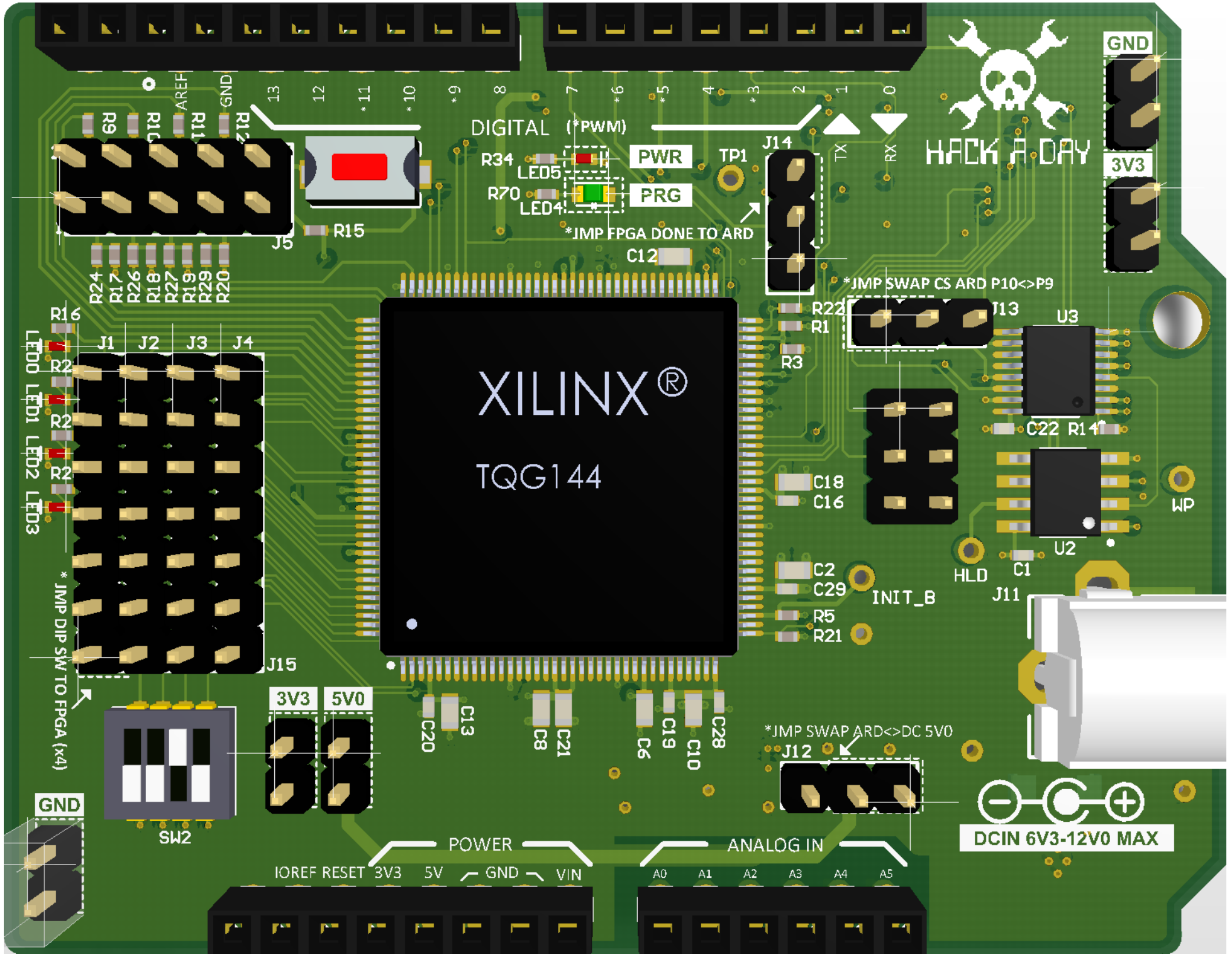
DIGITAL (*PWM)
AREF
GND
13
12
*11
*10
*9
8
7
*6
*5
4
*3
2
1
0
TX
RX
HACK A DAY
GND
3V3
R9
R10
R11
R12
J5
R15
R24
R17
R26
R18
R27
R19
R29
R20
R34
LED5
PWR
R70
LED4
PRG
TP1
J14
*JMP FPGA DONE TO ARD
C12
*JMP SWAP CS ARD P10<>P9
R22
R1
J13
U3
R3
R16
LED0
LED1
LED2
LED3
J1
J2
J3
J4
XILINX®
TQG144
C22
R14
C18
C16
WP
U2
HLD
C1
C2
C29
INIT_B
J11
R5
R21
* JMP DIP SW TO FPGA (x4)
J15
3V3
5V0
C20
C13
C8
C21
C6
C19
C10
C28
*JMP SWAP ARD<>DC 5V0
J12
GND
SW2
DCIN 6V3-12V0 MAX
POWER
IOREF RESET 3V3 5V GND VIN
ANALOG IN
A0 A1 A2 A3 A4 A5