

[illegible]

TP1 DONE TP

TP2 WP_N

TP3 HOLD_N

MISO_ARD

MOSI_ARD

SCLK_ARD

ARD_D0(RX)

ARD_D1(TX)

MUX_SEL

PROGRAM_B

ARD_D4

ARD_D5

ARD_D6

ARD_D7

ARD_D8

CS_ARD_ALT_N

CS_ARD_STD_N

ARD_D11

ARD_D12

ARD_D13

DONE_ARDx

IO0

IO1

IO2

IO3

IO4

IO5

IO6

IO7

IO8

IO9

IO10

IO11

IO12

IO13

IO14

IO15

IO16

IO17

IO18

IO19

IO20

IO21

IO22

IO23

IO24

IO25

IO26

IO27

IO28

IO29

IO30

IO31

IO32

IO33

IO34

IO35

IO36

IO37

IO38

IO39

IO40

IO41

IO42

IO43

IO44

IO45

IO46

IO47

DONE_ARDx

DONE_TP

DONE

5V0

3V3

1V2

GND

WP_N

HOLD_N

SPI_ARD

ARD_DIG

IO_HDR1

IO_HDR2

IO_HDR3

IO_HDR4

HARD_JTAG

SOFT_JTAG

FPGA_PROG

HARD_TCK

HARD_TDI

HARD_TDO

HARD_TMS

SOFT_TCK

SOFT_TDI

SOFT_TDO

SOFT_TMS

INIT_B

PROGRAM_B

DONE

TP4

INIT_B

CONN HARD_JTAG TO 7

PAD ARRAYS COL 2-4, FPGA GPIO

4X L

JMP TO

LEDS

J5

HARD TCK	1	2	SOFT TMS
HARD TDI	3	4	SOFT TCK
HARD TDO	5	6	SOFT TDI
HARD TMS	7	8	SOFT TDO
3V3	9	10	GND

FPGA IO CONNECTORS

J3

IO2
IO6
IO10
IO14
IO18
IO22

J4

IO3
IO7
IO11
IO15
IO19
IO23

J2

IO1
IO5
IO9
IO13
IO17
IO21

LEDs and Resistors

LED0	R16	270R	IO0
LED1	R23	270R	IO4
LED2	R25	270R	IO8
LED3	R28	270R	IO12

J1A-J1F

IO16
IO20

J15

3V3
3V3
3V3
3V3

R30, R31, R32, R33 *4x 4K7

SW2

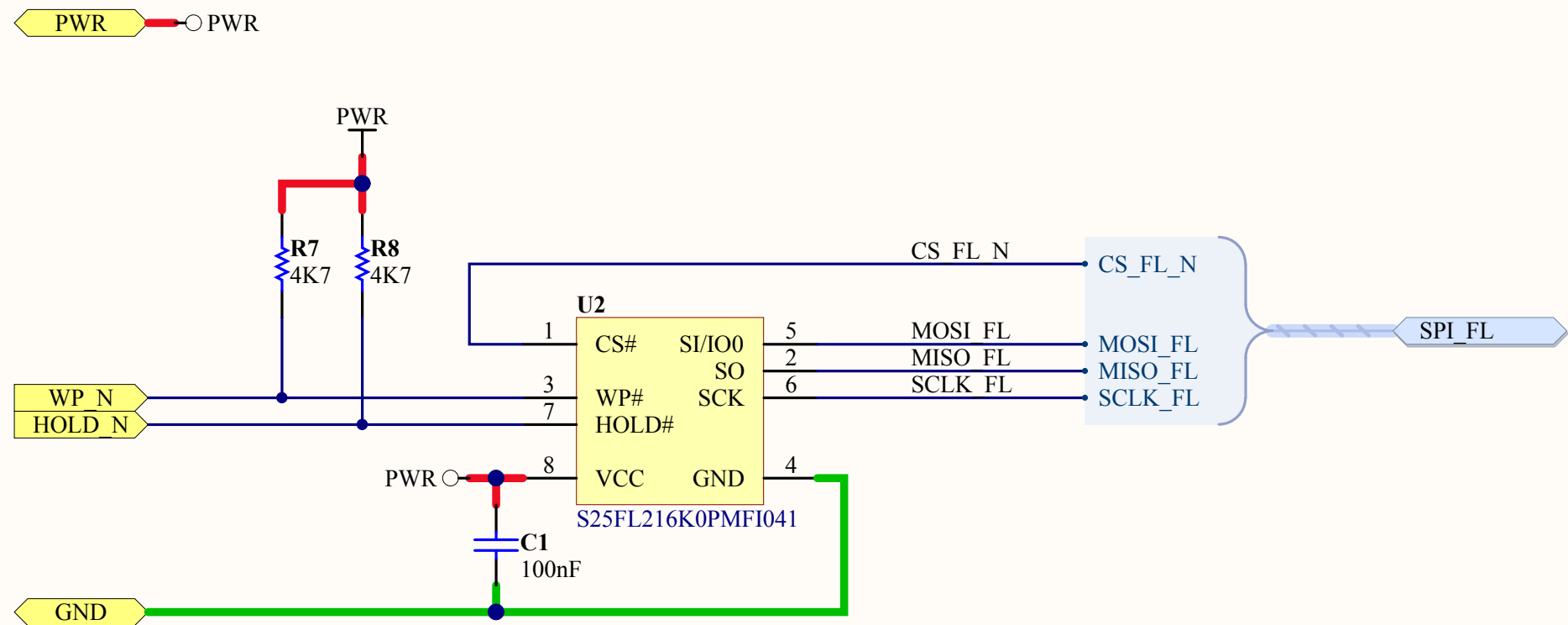
2	1
4	3
6	5
8	7

CHS-04TA

PWR2, PWR3, PWR4, PWR5, PWR6

3V3, 5V0, GND

Power Supply 2V4 to 3V7



A

A

B

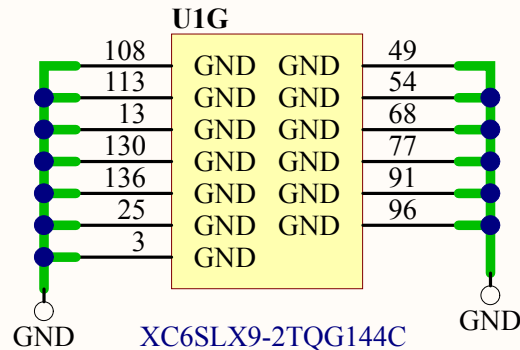
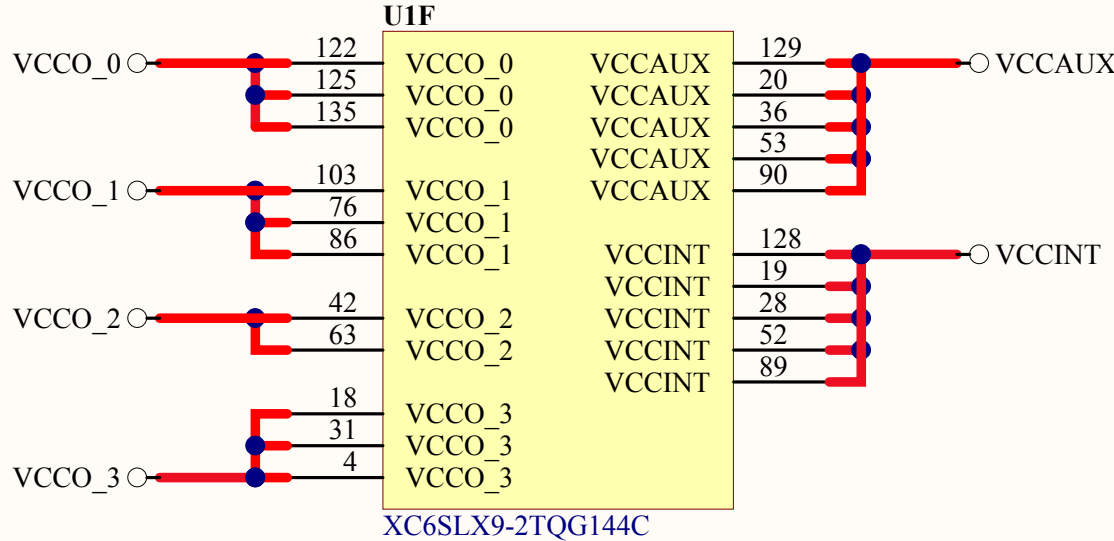
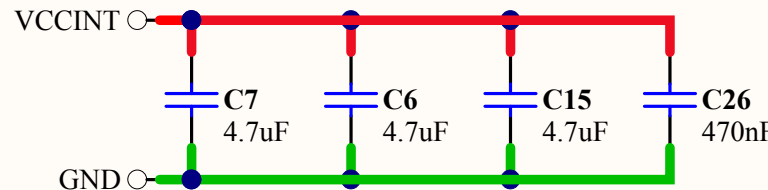
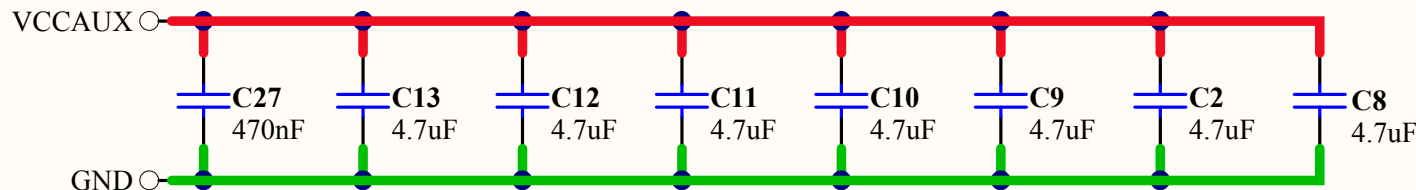
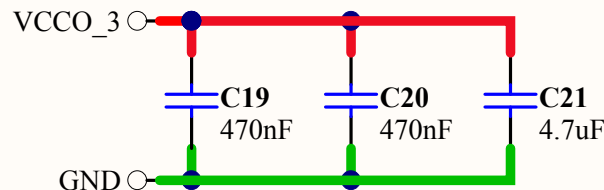
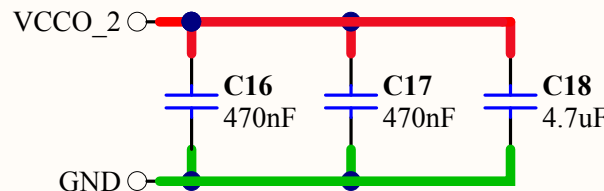
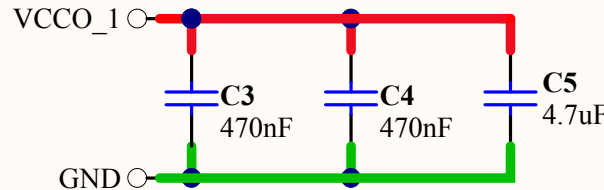
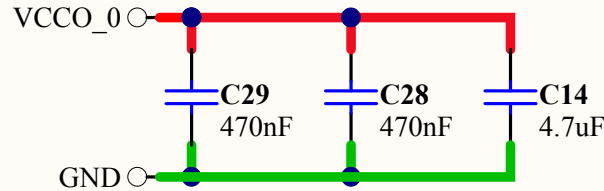
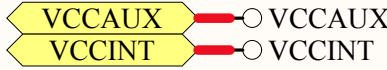
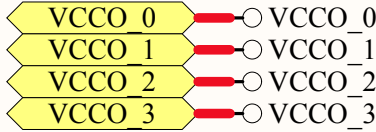
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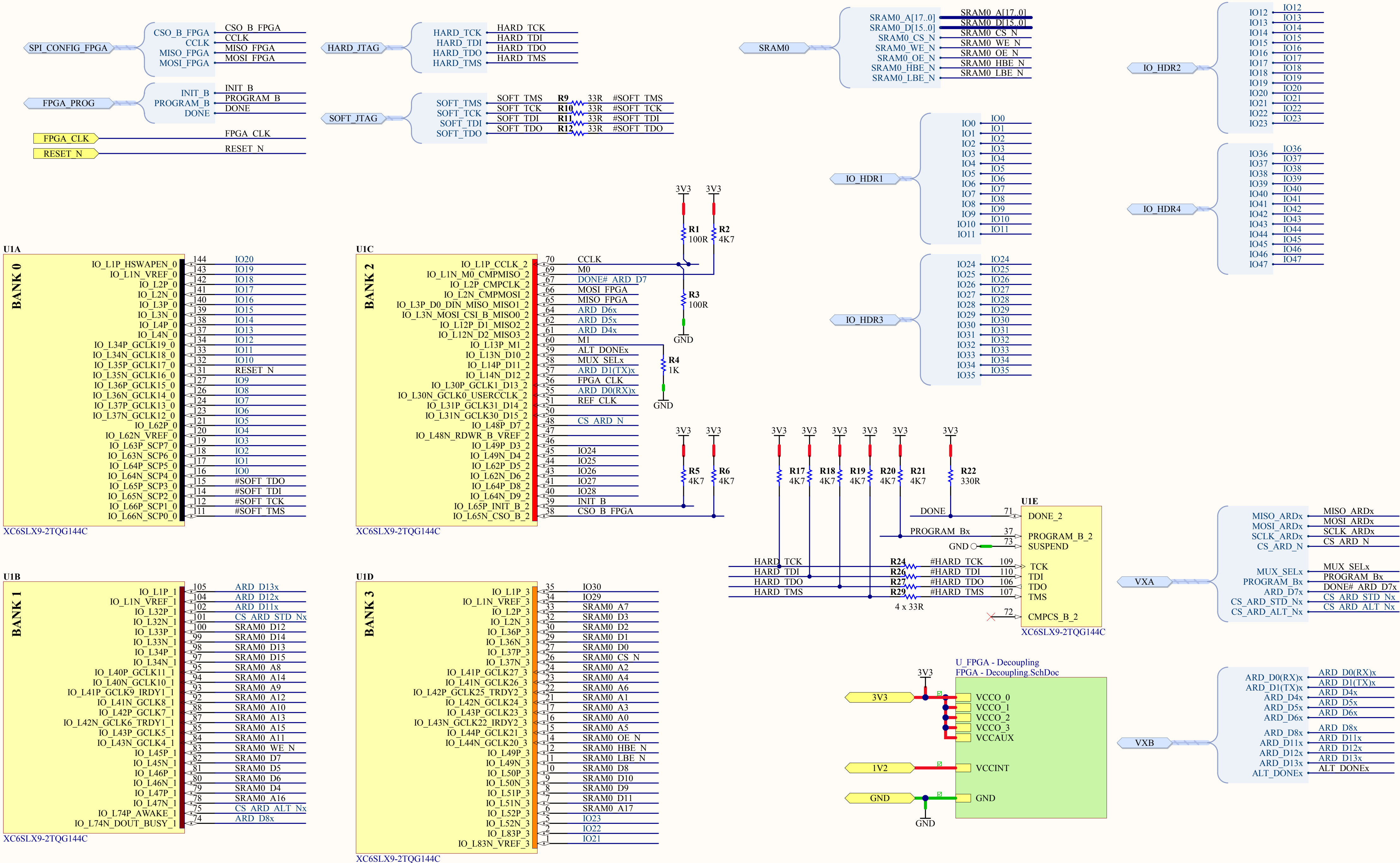
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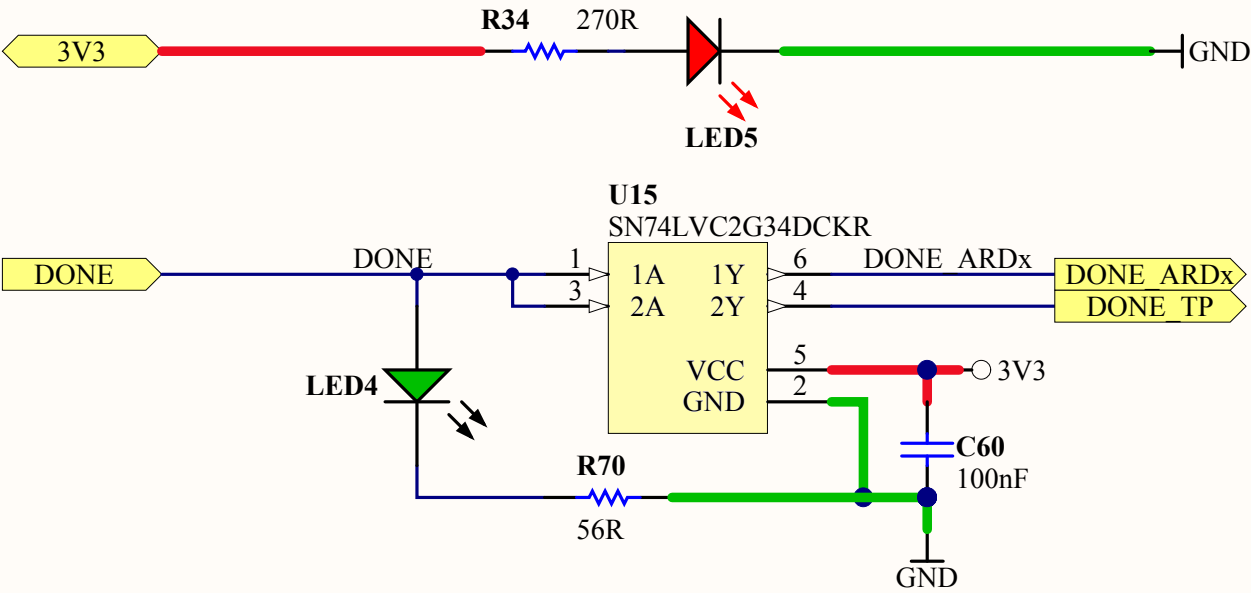
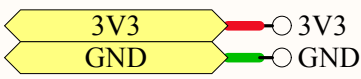
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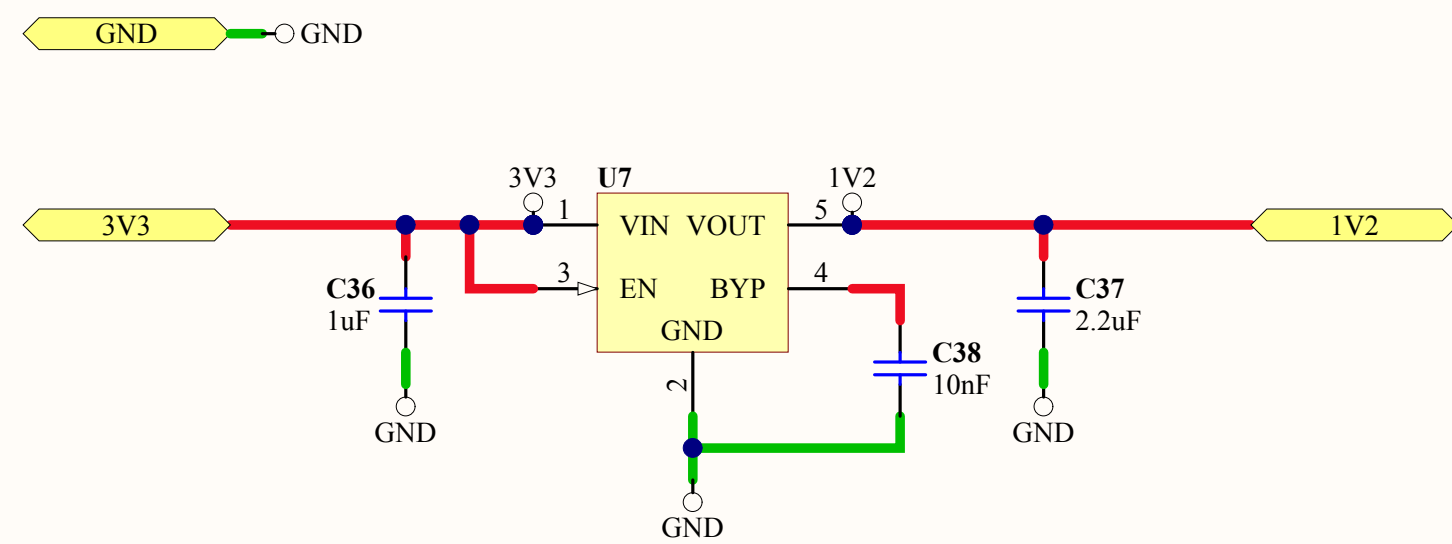
D

D

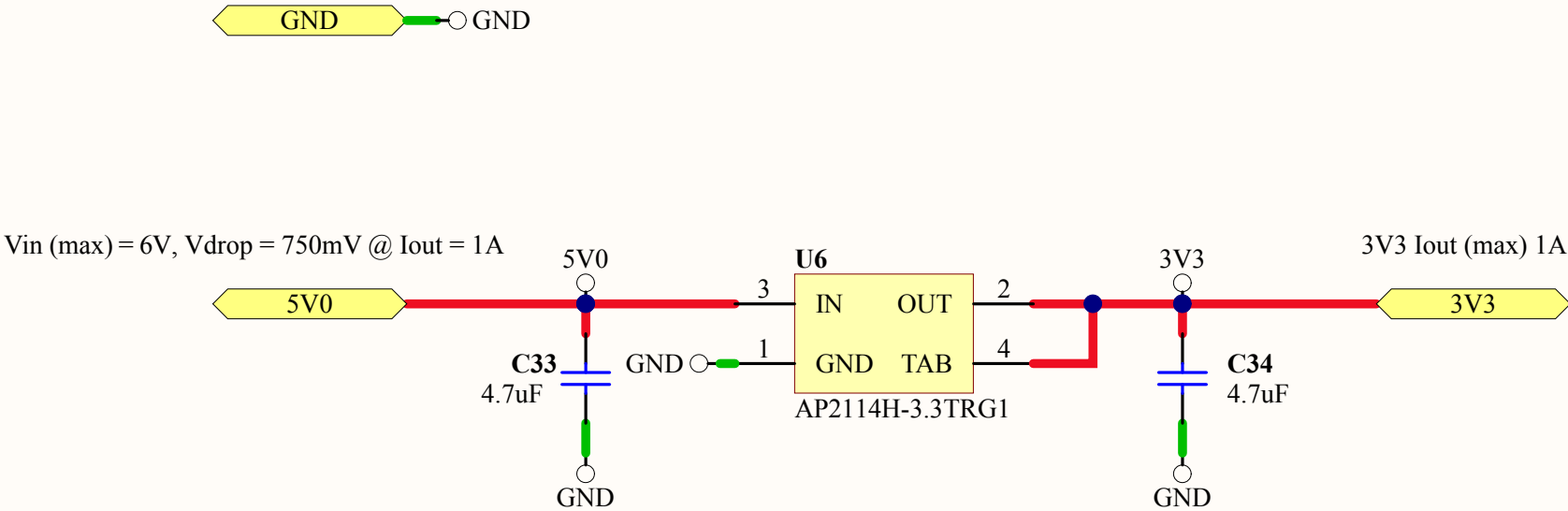


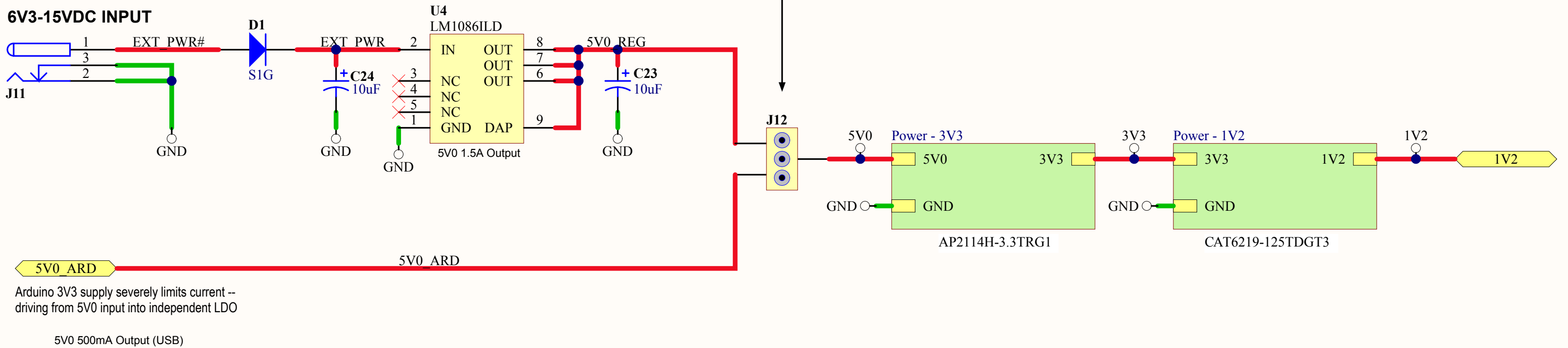


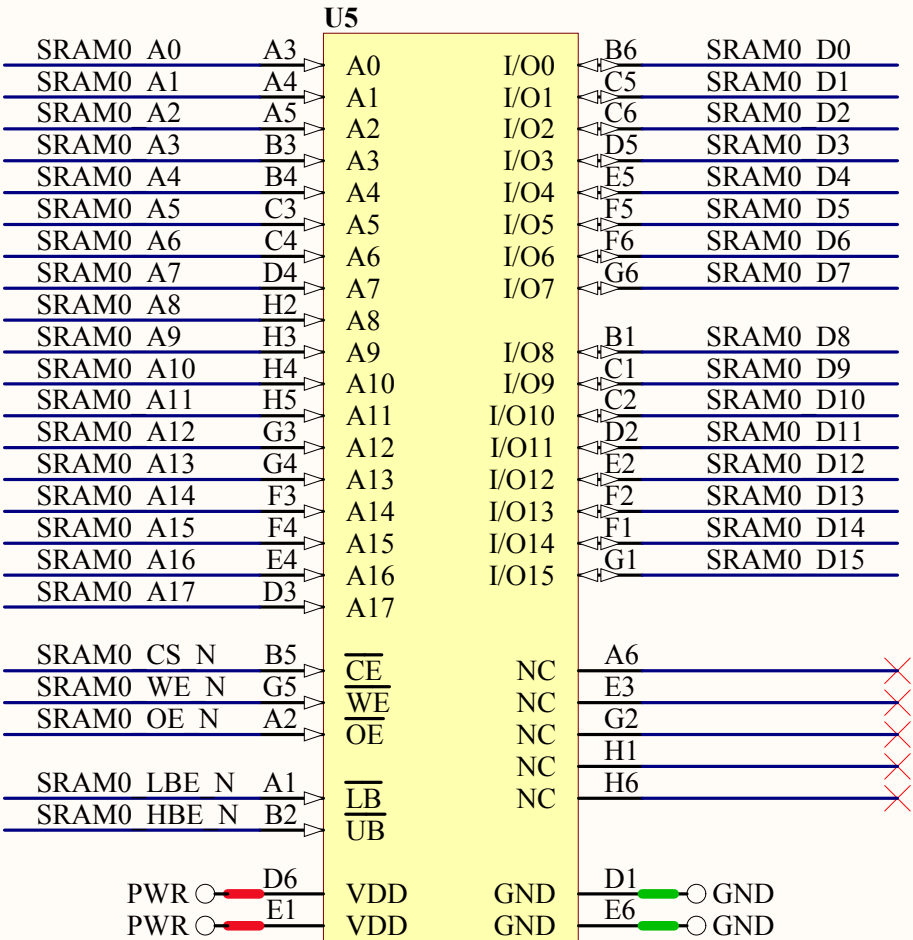
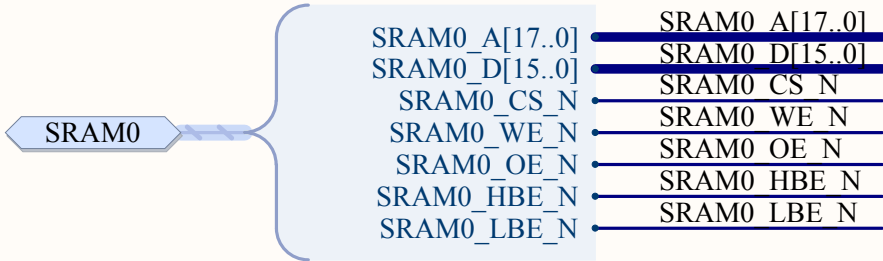




AP2114H - 3V3 Fixed Output Volage Regulator (Imax = 1A)







IS61WV12816DBLL-10BLI

