greensocs-base-vp

Generated by Doxygen 1.8.17

1 Main Page 1

2 Hierarchical Index 3

2.1 Class Hierarchy 3

3 Class Index 5

3.1 Class List 5

4 Class Documentation 7

4.1 GreenSocsPlatform Class Reference 7

4.2 hexagon_cluster Class Reference 8

4.3 hexagon_config_extensions Struct Reference 9

4.4 hexagon_config_table Struct Reference 9

Index 11

Chapter 1

Main Page

Chapter 2

Hierarchical Index

2.1 Class Hierarchy

This inheritance list is sorted roughly, but not completely, alphabetically:

hexagon_config_extensions 9
hexagon_config_table 9
sc_module
  GreenSocsPlatform 7
  hexagon_cluster 8

Chapter 3

Class Index

3.1 Class List

Here are the classes, structs, unions and interfaces with brief descriptions:

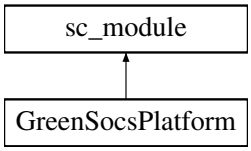
GreenSocsPlatform 7
hexagon_cluster 8
hexagon_config_extensions 9
hexagon_config_table 9

Chapter 4

Class Documentation

4.1 GreenSocsPlatform Class Reference

Inheritance diagram for GreenSocsPlatform:



Public Member Functions

- **GreenSocsPlatform** (const sc_core::sc_module_name &n)
- void **end_of_elaboration** ()

Protected Member Functions

- void **do_bus_binding** ()
- void **setup_irq_mapping** ()
- void **hexagon_config_setup** ()

Protected Attributes

- gs::async_event **event**
- cci::cci_param< unsigned > **p_arm_num_cpus**
- cci::cci_param< unsigned > **p_num_redists**
- cci::cci_param< unsigned > **p_hexagon_num_clusters**
- gs::ConfigurableBroker **m_broker**
- cci::cci_param< int > **p_quantum_ns**
- cci::cci_param< int > **p_uart_backend**
- QemuInstanceManager **m_inst_mgr**
- QemuInstanceManager **m_inst_mgr_h**
- QemuInstance & **m_qemu_inst**
- gs::Router **m_router**

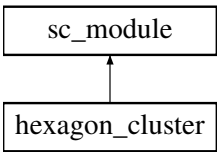
- sc_core::sc_vector< QemuCpuArmMax > **m_cpus**
- sc_core::sc_vector< hexagon_cluster > **m_hexagon_clusters**
- QemuArmGicv3 * **m_gic**
- gs::Memory **m_ram**
- gs::Memory **m_hexagon_ram**
- gs::Memory **m_rom**
- GlobalPeripheralInitiator * **m_global_peripheral_initiator_arm**
- Uart **m_uart**
- IPCC **m_ipcc**
- QemuVirtioMMIONet **m_virtio_net_0**
- QemuVirtioMMIOBlk **m_virtio_blk_0**
- gs::Memory **m_fallback_mem**
- gs::Loader **m_loader**
- QemuArmSmmu * **m_smmu** =nullptr
- qtb * **m_qtb**
- hexagon_config_table * **cfgTable**
- hexagon_config_extensions * **cfgExtensions**

The documentation for this class was generated from the following file:

- /home/thomas/Documents/GreenSocs/build-platforme/qualcomm_vp/src/main.cc

4.2 hexagon_cluster Class Reference

Inheritance diagram for hexagon_cluster:



Public Member Functions

- **hexagon_cluster** (const sc_core::sc_module_name &n, QemuInstanceManager &m_inst_mgr, gs::↩ Router<> &parent_router)

Public Attributes

- cci::cci_param< unsigned > **p_hexagon_num_threads**
- cci::cci_param< uint32_t > **p_hexagon_start_addr**
- QemuInstance & **m_qemu_hex_inst**
- gs::Router **m_router**

The documentation for this class was generated from the following file:

- /home/thomas/Documents/GreenSocs/build-platforme/qualcomm_vp/src/main.cc

4.3 hexagon_config_extensions Struct Reference

Public Attributes

- uint32_t **cfgbase**
- uint32_t **cfgtable_size**
- uint32_t **l2tcm_size**
- uint32_t **l2vic_base**
- uint32_t **l2vic_size**
- uint32_t **csr_base**
- uint32_t **qtmr_rg0**
- uint32_t **qtmr_rg1**

The documentation for this struct was generated from the following file:

- /home/thomas/Documents/GreenSocs/build-platforme/qualcomm_vp/src/main.cc

4.4 hexagon_config_table Struct Reference

Public Attributes

- uint32_t **l2tcm_base**
- uint32_t **reserved**
- uint32_t **subsystem_base**
- uint32_t **etm_base**
- uint32_t **l2cfg_base**
- uint32_t **reserved2**
- uint32_t **l1s0_base**
- uint32_t **axi2_lowaddr**
- uint32_t **streamer_base**
- uint32_t **clade_base**
- uint32_t **fastl2vic_base**
- uint32_t **jtlb_size_entries**
- uint32_t **coproc_present**
- uint32_t **ext_contexts**
- uint32_t **vtcm_base**
- uint32_t **vtcm_size_kb**
- uint32_t **l2tag_size**
- uint32_t **l2ecomem_size**
- uint32_t **thread_enable_mask**
- uint32_t **eccreg_base**
- uint32_t **l2line_size**
- uint32_t **tiny_core**
- uint32_t **l2itcm_size**
- uint32_t **l2itcm_base**
- uint32_t **clade2_base**
- uint32_t **dtm_present**
- uint32_t **dma_version**
- uint32_t **hvx_vec_log_length**
- uint32_t **core_id**
- uint32_t **core_count**

- uint32_t **hmx_int8_spatial**
- uint32_t **hmx_int8_depth**
- uint32_t **v2x_mode**
- uint32_t **hmx_int8_rate**
- uint32_t **hmx_fp16_spatial**
- uint32_t **hmx_fp16_depth**
- uint32_t **hmx_fp16_rate**
- uint32_t **hmx_fp16_acc_frac**
- uint32_t **hmx_fp16_acc_int**
- uint32_t **acd_preset**
- uint32_t **mnd_preset**
- uint32_t **l1d_size_kb**
- uint32_t **l1i_size_kb**
- uint32_t **l1d_write_policy**
- uint32_t **vtcm_bank_width**
- uint32_t **reserved3**
- uint32_t **reserved4**
- uint32_t **reserved5**
- uint32_t **hmx_cvt_mpy_size**
- uint32_t **axi3_lowaddr**

The documentation for this struct was generated from the following file:

- /home/thomas/Documents/GreenSocs/build-platforme/qualcomm_vp/src/main.cc

Index

GreenSocsPlatform, 7

hexagon_cluster, 8
hexagon_config_extensions, 9
hexagon_config_table, 9