# greensocs-base-vp

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Chapter 1

# Main Page

# Chapter 2

# Hierarchical Index

## 2.1 Class Hierarchy

This inheritance list is sorted roughly, but not completely, alphabetically:


sc_module
GreenSocsPlatform . . . . . 7

# Chapter 3

# Class Index

## 3.1 Class List

Here are the classes, structs, unions and interfaces with brief descriptions:

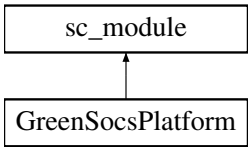
GreenSocsPlatform . . . . . 7

# Chapter 4

# Class Documentation

## 4.1 GreenSocsPlatform Class Reference

Inheritance diagram for GreenSocsPlatform:




### Public Member Functions

- **GreenSocsPlatform** (const sc_core::sc_module_name &n)

### Protected Member Functions

- void **setup_cpus** ()
- void **setup_memory_mapping** ()
- void **setup_irq_mapping** ()
- bool **load_blobs** ()
- void **do_bootloader** ()

### Protected Attributes

- gs::ConfigurableBroker **m_broker**
- cci::cci_param< int > **m_quantum_ns**
- cci::cci_param< int > **m_gdb_port**
- cci::cci_param< cci::uint64 > **m_ram_size**
- cci::cci_param< std::string > **m_ram_blob_file**
- cci::cci_param< std::string > **m_flash_blob_file**
- cci::cci_param< std::string > **m_kernel_file**
- cci::cci_param< std::string > **m_dtb_file**
- cci::cci_param< cci::uint64 > **m_addr_map_ram**
- cci::cci_param< cci::uint64 > **m_addr_map_uart**

- cci::cci_param< cci::uint64 > **m_addr_map_eth**
- QemuInstanceManager **m_inst_mgr**
- QemuInstance & **m_qemu_inst**
- sc_core::sc_vector< QemuCpuArmNeoverseN1 > **m_cpus**
- QemuArmGicv3 **m_gic**
- Router **m_router**
- Memory **m_ram**
- Memory **m_flash**
- QemuUartPl011 **m_uart**
- QemuOpencoresEth **m_eth**
- GlobalPeripheralInitiator **m_global_peripheral_initiator**

The documentation for this class was generated from the following file:

- /home/thomas/Documents/GreenSocs/build-platforme/platform-neoverse-n1/src/main.cc

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