

base-components

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## Chapter 2

# Hierarchical Index

### 2.1 Class Hierarchy

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## Chapter 3

# Class Index

### 3.1 Class List

Here are the classes, structs, unions and interfaces with brief descriptions:

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## Chapter 4

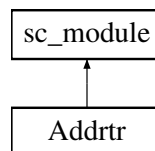
# Class Documentation

### 4.1 Addrtr Class Reference

A [Addrtr](#) component that can add addrtr to a virtual platform project to manage the various transactions.

```
#include <addrtr.h>
```

Inheritance diagram for Addrtr:



#### Public Member Functions

- **Addrtr** (const sc\_core::sc\_module\_name &nm)
- **Addrtr** (const [Addrtr](#) &)=delete

#### Public Attributes

- tlm\_utils::simple\_target\_socket< [Addrtr](#) > **front\_socket**
- tlm\_utils::simple\_initiator\_socket< [Addrtr](#) > **back\_socket**
- cci::cci\_param< uint64\_t > **offset**

### 4.1.1 Detailed Description

A [Addrtr](#) component that can add addrtr to a virtual platform project to manage the various transactions.

This component models a addrtr. It has a single multi-target socket so any other component with an initiator socket can connect to this component. It behaves as follows:

- Manages exclusive accesses, adding this addrtr as a 'hop' in the exclusive access extension (see [GreenSocs/libgsutils](#)).
- Manages connections to multiple initiators and targets with the method `add_initiator` and `add_target`.
- Allows to manage read and write transactions with `b_transport` and `transport_dbg` methods.
- Supports passing through DMI requests with the method `get_direct_mem_ptr`.
- Handles invalidation of multiple DMI pointers with the method `invalidate_direct_mem_ptr` which passes the invalidate back to *all* initiators.
- It checks for each transaction if the address is valid or not and returns an error if the address is invalid with the method `decode_address`.

The documentation for this class was generated from the following file:

- [/home/thomas/Documents/GreenSocs/build-lib/base-components/include/greensocs/base-components/misc/addrtr.h](#)

## 4.2 gs::CSVRow Class Reference

### Public Member Functions

- `std::string operator[]` (`std::size_t index`) const
- `std::size_t size` () const
- `void readNextRow` (`std::istream &str`)

The documentation for this class was generated from the following file:

- [/home/thomas/Documents/GreenSocs/build-lib/base-components/include/greensocs/base-components/loader.h](#)

## 4.3 gs::Loader< BUSWIDTH >::elf\_reader::elf32\_traits Struct Reference

### Public Types

- `typedef Elf32_Ehdr Elf_Ehdr`
- `typedef Elf32_Phdr Elf_Phdr`
- `typedef Elf32_Shdr Elf_Shdr`
- `typedef Elf32_Sym Elf_Sym`

### Static Public Member Functions

- static Elf\_Ehdr \* **elf\_getehdr** (Elf \*elf)
- static Elf\_Phdr \* **elf\_getphdr** (Elf \*elf)
- static Elf\_Shdr \* **elf\_getshdr** (Elf\_Scn \*scn)

The documentation for this struct was generated from the following file:

- /home/thomas/Documents/GreenSocs/build-lib/base-components/include/greensocs/base-components/loader.h↵

## 4.4 gs::Loader< BUSWIDTH >::elf\_reader::elf64\_traits Struct Reference

### Public Types

- typedef Elf64\_Ehdr **Elf\_Ehdr**
- typedef Elf64\_Phdr **Elf\_Phdr**
- typedef Elf64\_Shdr **Elf\_Shdr**
- typedef Elf64\_Sym **Elf\_Sym**

### Static Public Member Functions

- static Elf\_Ehdr \* **elf\_getehdr** (Elf \*elf)
- static Elf\_Phdr \* **elf\_getphdr** (Elf \*elf)
- static Elf\_Shdr \* **elf\_getshdr** (Elf\_Scn \*scn)

The documentation for this struct was generated from the following file:

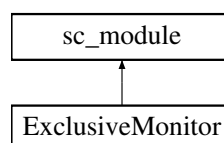
- /home/thomas/Documents/GreenSocs/build-lib/base-components/include/greensocs/base-components/loader.h↵

## 4.5 ExclusiveMonitor Class Reference

ARM-like global exclusive monitor.

```
#include <exclusive-monitor.h>
```

Inheritance diagram for ExclusiveMonitor:



## Public Member Functions

- **ExclusiveMonitor** (const sc\_core::sc\_module\_name &name)
- **ExclusiveMonitor** (const ExclusiveMonitor &)=delete

## Public Attributes

- tlm\_utils::simple\_target\_socket< ExclusiveMonitor > **front\_socket**
- tlm\_utils::simple\_initiator\_socket< ExclusiveMonitor > **back\_socket**

### 4.5.1 Detailed Description

ARM-like global exclusive monitor.

This component models an ARM-like global exclusive monitor. It connects in front of an target and monitors accesses to it. It behaves as follows:

- On an exclusive load, it internally marks the corresponding region as locked. The load is forwarded to the target.
- On an exclusive store to the same region, the region is unlocked, and the store is forwarded to the target.
- When an initiator perform an exclusive load while already owning a region, the region gets unlocked before the new one is locked.
- A regular store will unlock all intersecting regions
- If an exclusive store fails, that it, corresponds to a region which is not locked, or is locked by another initiator, or does not exactly match the store boundaries, the failure is reported into the TLM exclusive extension and the store is *not* forwarded to the target.
- DMI invalidation is performed when a region is locked.
- DMI requests are intercepted and modified accordingly to match the current locking state.
- DMI hints (the is\_dmi\_allowed() flag in transactions) is also intercepted and modified if necessary.

The documentation for this class was generated from the following file:

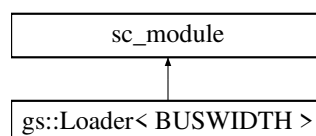
- /home/thomas/Documents/GreenSocs/build-lib/base-components/include/greensocs/base-components/misc/exclusive-monitor.h

## 4.6 gs::Loader< BUSWIDTH > Class Template Reference

A loader that cope with several formats.

```
#include <loader.h>
```

Inheritance diagram for gs::Loader< BUSWIDTH >:



## Public Member Functions

- **Loader** (sc\_core::sc\_module\_name name)
- **Loader** (sc\_core::sc\_module\_name name, std::function< void(const uint8\_t \*data, uint64\_t offset, uint64\_t len)> \_write)
- void **file\_load** (std::string filename, uint64\_t addr)  
*This function reads a file into memory and can be used to load an image.*
- void **csv\_load** (std::string filename, uint64\_t offset, std::string addr\_str, std::string value\_str, bool byte\_swap)
- void **str\_load** (std::string data, uint64\_t addr)
- void **ptr\_load** (uint8\_t \*data, uint64\_t addr, uint64\_t len)
- void **elf\_load** (const std::string &path)

## Public Attributes

- simple\_initiator\_socket\_zero< [Loader](#)< BUSWIDTH > > **initiator\_socket**

## Protected Member Functions

- void **load** (std::string name)
- void **end\_of\_elaboration** ()

### 4.6.1 Detailed Description

```
template<unsigned int BUSWIDTH = 32>
class gs::Loader< BUSWIDTH >
```

A loader that cope with several formats.

### 4.6.2 Member Function Documentation

#### 4.6.2.1 file\_load()

```
template<unsigned int BUSWIDTH = 32>
void gs::Loader< BUSWIDTH >::file_load (
    std::string filename,
    uint64_t addr ) [inline]
```

This function reads a file into memory and can be used to load an image.

#### Parameters

<i>filename</i>	Name of the file
<i>addr</i>	the address where the memory file is to be read

## Returns

size\_t

The documentation for this class was generated from the following file:

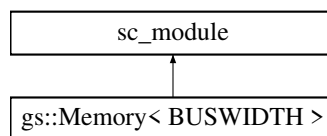
- /home/thomas/Documents/GreenSocs/build-lib/base-components/include/greensocs/base-components/loader.h

## 4.7 gs::Memory< BUSWIDTH > Class Template Reference

A memory component that can add memory to a virtual platform project.

```
#include <memory.h>
```

Inheritance diagram for gs::Memory< BUSWIDTH >:



### Public Member Functions

- **Memory** (sc\_core::sc\_module\_name name, uint64\_t \_size=0)
- void **before\_end\_of\_elaboration** ()
- **Memory** (const [Memory](#) &)=delete
- uint64\_t **size** ()  
*this function returns the size of the memory*
- uint64\_t **base** ()  
*this function returns the base address of the memory*

### Public Attributes

- [gs::Loader](#) **load**
- tlm\_utils::simple\_target\_socket< [Memory](#), BUSWIDTH > **socket**
- cci::cci\_param< bool > **p\_rom**
- cci::cci\_param< bool > **p\_dmi**
- cci::cci\_param< bool > **p\_verbose**
- cci::cci\_param< sc\_core::sc\_time > **p\_latency**
- cci::cci\_param< std::string > **p\_mapfile**

### Protected Member Functions

- virtual bool **get\_direct\_mem\_ptr** (tlm::tlm\_generic\_payload &txn, tlm::tlm\_dmi &dmi\_data)
- virtual void **b\_transport** (tlm::tlm\_generic\_payload &txn, sc\_core::sc\_time &delay)
- virtual unsigned int **transport\_dbg** (tlm::tlm\_generic\_payload &txn)
- void **cci\_ignore** (std::string name)
- void **read** (uint8\_t \*data, uint64\_t offset, uint64\_t len)
- void **write** (const uint8\_t \*data, uint64\_t offset, uint64\_t len)
- void **map** (std::string filename)

*This function maps a host file system file into the memory, such that the results of the memory will be maintained between runs. This can be useful for emulating a flash ram for instance NB the only way of having this called is via the configuration paramter mapfile.*



### 4.7.1 Detailed Description

```
template<unsigned int BUSWIDTH = 32>
class gs::Memory< BUSWIDTH >
```

A memory component that can add memory to a virtual platform project.

This component models a memory. It has a simple target socket so any other component with an initiator socket can connect to this component. It behaves as follows:

- The memory does not manage time in any way
- It is only an LT model, and does not handle AT transactions
- It does not manage exclusive accesses
- You can manage the size of the memory during the initialization of the component
- [Memory](#) does not allocate individual "pages" but a single large block
- It supports DMI requests with the method `get_direct_mem_ptr`
- DMI invalidates are not issued.

### 4.7.2 Member Function Documentation

#### 4.7.2.1 base()

```
template<unsigned int BUSWIDTH = 32>
uint64_t gs::Memory< BUSWIDTH >::base ( ) [inline]
```

this function returns the base address of the memory

#### Returns

the address of the memory of type `uint64_t`

#### 4.7.2.2 map()

```
template<unsigned int BUSWIDTH = 32>
void gs::Memory< BUSWIDTH >::map (
    std::string filename ) [inline], [protected]
```

This function maps a host file system file into the memory, such that the results of the memory will be maintained between runs. This can be useful for emulating a flash ram for instance NB the only way of having this called is via the configuration paramter `mapfile`.

## Parameters

<i>filename</i>	Name of the file
-----------------	------------------

## 4.7.2.3 size()

```
template<unsigned int BUSWIDTH = 32>
uint64_t gs::Memory< BUSWIDTH >::size ( ) [inline]
```

this function returns the size of the memory

## Returns

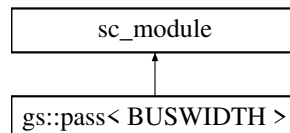
the size of the memory of type uint64\_t

The documentation for this class was generated from the following file:

- /home/thomas/Documents/GreenSocs/build-lib/base-components/include/greensocs/base-components/memory.↔  
h

## 4.8 gs::pass&lt; BUSWIDTH &gt; Class Template Reference

Inheritance diagram for gs::pass< BUSWIDTH >:



## Public Member Functions

- **pass** (const sc\_core::sc\_module\_name &nm, bool \_verbose=false)
- **pass** (const [pass](#) &)=delete

## Public Attributes

- initiator\_socket\_spying< [pass](#)< BUSWIDTH > > **initiator\_socket**
- tlm\_utils::simple\_target\_socket< [pass](#)< BUSWIDTH >, BUSWIDTH > **target\_socket**
- cci::cci\_param< bool > **p\_verbose**

The documentation for this class was generated from the following file:

- /home/thomas/Documents/GreenSocs/build-lib/base-components/include/greensocs/base-components/connectors.↔  
h

## 4.9 Path Class Reference

[Path](#) recording TLM extension.

### 4.9.1 Detailed Description

[Path](#) recording TLM extension.

extension

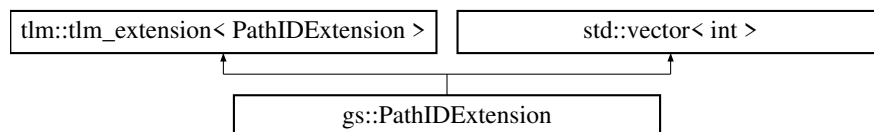
Embeds an ID field in the txn, which is populated as the network is traversed - see README.

The documentation for this class was generated from the following file:

- `/home/thomas/Documents/GreenSocs/build-lib/base-components/include/greensocs/base-components/pathid↔_extension.h`

## 4.10 gs::PathIDExtension Class Reference

Inheritance diagram for `gs::PathIDExtension`:



### Public Member Functions

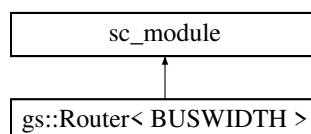
- **PathIDExtension** (const [PathIDExtension](#) &)=default
- virtual `tlm_extension_base * clone ()` const override
- virtual void **copy\_from** (const `tlm_extension_base &ext`) override

The documentation for this class was generated from the following file:

- `/home/thomas/Documents/GreenSocs/build-lib/base-components/include/greensocs/base-components/pathid↔_extension.h`

## 4.11 gs::Router< BUSWIDTH > Class Template Reference

Inheritance diagram for `gs::Router< BUSWIDTH >`:



## Public Types

- typedef multi\_passthrough\_initiator\_socket\_spying< Router< BUSWIDTH > > **initiator\_socket\_type**

## Public Member Functions

- **Router** (const sc\_core::sc\_module\_name &nm)
- **Router** (const Router &)=delete
- void **add\_target** (TargetSocket &t, const uint64\_t address, uint64\_t size, bool masked=true)
- virtual void **add\_initiator** (InitiatorSocket &i)

## Public Attributes

- initiator\_socket\_type **initiator\_socket**
- tlm\_utils::multi\_passthrough\_target\_socket< Router< BUSWIDTH > > **target\_socket**
- cci::cci\_broker\_handle **m\_broker**
- cci::cci\_param< bool > **thread\_safe**

## Protected Member Functions

- virtual void **before\_end\_of\_elaboration** ()

The documentation for this class was generated from the following file:

- /home/thomas/Documents/GreenSocs/build-lib/base-components/include/greensocs/base-components/router.h↵

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