

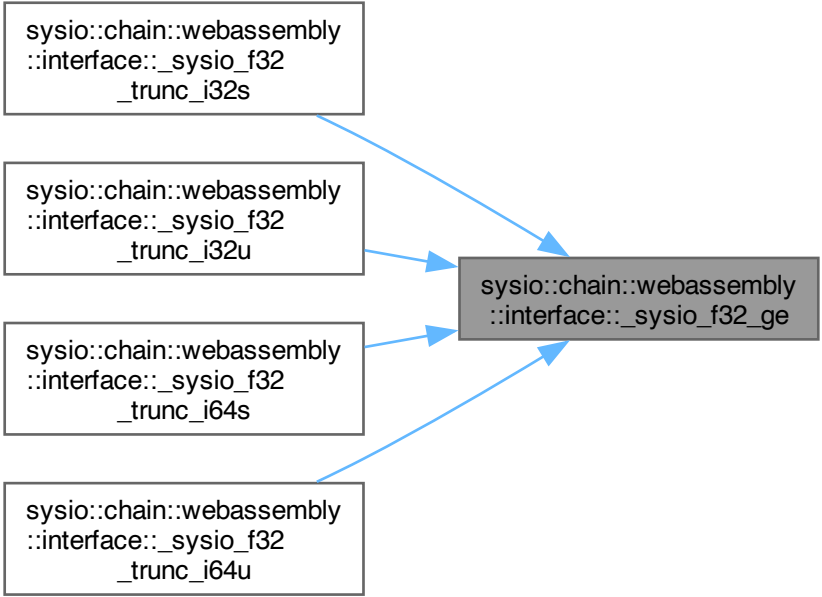
sysio::chain::webassembly
::interface::_sysio_f32
_trunc_i32s

sysio::chain::webassembly
::interface::_sysio_f32
_trunc_i32u

sysio::chain::webassembly
::interface::_sysio_f32
_trunc_i64s

sysio::chain::webassembly
::interface::_sysio_f32
_trunc_i64u

sysio::chain::webassembly
::interface::_sysio_f32_ge



```
graph LR; A["sysio::chain::webassembly<br>::interface::_sysio_f32<br>_trunc_i32s"] --> D["sysio::chain::webassembly<br>::interface::_sysio_f32_ge"]; B["sysio::chain::webassembly<br>::interface::_sysio_f32<br>_trunc_i32u"] --> D; C["sysio::chain::webassembly<br>::interface::_sysio_f32<br>_trunc_i64s"] --> D; E["sysio::chain::webassembly<br>::interface::_sysio_f32<br>_trunc_i64u"] --> D;
```