

sysio::chain::webassembly
::interface::__floatsidf

sysio::chain::webassembly
::interface::_sysio_i32_to_f64

i32_to_f64

```
graph LR; A["sysio::chain::webassembly::interface::__floatsidf"] --> C["i32_to_f64"]; B["sysio::chain::webassembly::interface::_sysio_i32_to_f64"] --> C;
```

The diagram illustrates a mapping or implementation relationship. On the left, two white rectangular boxes with black borders contain the names of functions or interfaces. The top box contains 'sysio::chain::webassembly::interface::__floatsidf' and the bottom box contains 'sysio::chain::webassembly::interface::_sysio_i32_to_f64'. Two blue arrows originate from the right side of these boxes and point towards a single gray rectangular box on the right. This gray box contains the text 'i32_to_f64', which represents the target function or implementation that both source functions map to.