

sysio::chain::webassembly
::interface::_sysio_f32_add

sysio::chain::webassembly
::interface::_sysio_f32
_nearest

sysio::vm::binary_parser
::parse_function_body_code

f32_add

```
graph LR; A["sysio::chain::webassembly::interface::_sysio_f32_add"] --> D["f32_add"]; B["sysio::chain::webassembly::interface::_sysio_f32_nearest"] --> D; C["sysio::vm::binary_parser::parse_function_body_code"] --> D;
```

The diagram illustrates a function mapping or dispatch mechanism. Three source functions, each enclosed in a white box with a black border, are positioned on the left. Blue arrows originate from the right side of each box and converge on a single target function, 'f32_add', which is enclosed in a gray box with a black border on the right. The top source function is 'sysio::chain::webassembly::interface::_sysio_f32_add'. The middle source function is 'sysio::chain::webassembly::interface::_sysio_f32_nearest'. The bottom source function is 'sysio::vm::binary_parser::parse_function_body_code'.