

sysio::chain::webassembly
::interface::_sysio_f32_add

sysio::chain::webassembly
::interface::_sysio_f32
_nearest

sysio::vm::binary_parser
::parse_function_body_code

f32_add

```
graph LR; A["sysio::chain::webassembly::interface::_sysio_f32_add"] --> D["f32_add"]; B["sysio::chain::webassembly::interface::_sysio_f32_nearest"] --> D; C["sysio::vm::binary_parser::parse_function_body_code"] --> D;
```

The diagram illustrates a mapping or linking process. On the left, there are three white rectangular boxes with black borders, each containing a fully qualified function name. On the right, there is a single gray rectangular box with a black border containing the name 'f32_add'. Three blue arrows originate from the right side of each of the three white boxes and point towards the left side of the gray box, indicating that the functions in the white boxes are being mapped to or implemented by the 'f32_add' function.