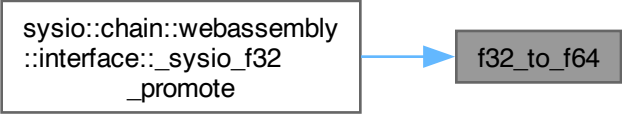


```
sysio::chain::webassembly  
::interface::_sysio_f32  
_promote
```



```
graph LR; A["sysio::chain::webassembly  
::interface::_sysio_f32  
_promote"] --> B["f32_to_f64"]
```

f32_to_f64