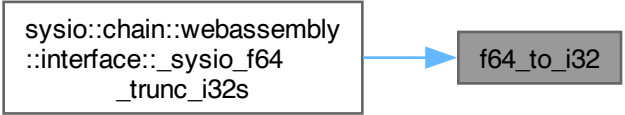


```
sysio::chain::webassembly  
::interface::_sysio_f64  
_trunc_i32s
```



```
graph LR; A["sysio::chain::webassembly  
::interface::_sysio_f64  
_trunc_i32s"] --> B["f64_to_i32"]
```

f64_to_i32