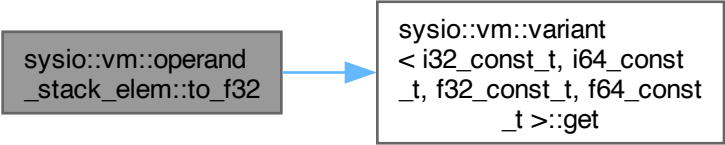


sysio::vm::operand  
\_stack\_elem::to\_f32



```
graph LR; A[sysio::vm::operand_stack_elem::to_f32] --> B[sysio::vm::variant<i32_const_t, i64_const_t, f32_const_t, f64_const_t>::get];
```

sysio::vm::variant  
< i32\_const\_t, i64\_const  
\_t, f32\_const\_t, f64\_const  
\_t >::get