

libraries/sys-vm/external
/softfloat/source/f32_isSignalingNaN.c

```
graph TD; A["libraries/sys-vm/external  
/softfloat/source/f32_isSignalingNaN.c"] --> B["stdbool.h"]; A --> C["platform.h"]; A --> D["internals.h"]; A --> E["specialize.h"]; A --> F["softfloat.h"];
```

stdbool.h

platform.h

internals.h

specialize.h

softfloat.h