

sysio::vm::machine
_code_writer::emit_else

sysio::vm::machine
_code_writer::emit_return

sysio::vm::machine
_code_writer::emit_br

```
graph LR; A["sysio::vm::machine_code_writer::emit_else"] --> C["sysio::vm::machine_code_writer::emit_br"]; B["sysio::vm::machine_code_writer::emit_return"] --> C;
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The diagram illustrates a mapping or inheritance relationship. On the left, two white rectangular boxes represent source functions: 'sysio::vm::machine_code_writer::emit_else' (top) and 'sysio::vm::machine_code_writer::emit_return' (bottom). On the right, a gray rectangular box represents a target function: 'sysio::vm::machine_code_writer::emit_br'. Two blue arrows point from the right side of each white box to the left side of the gray box, indicating that both source functions are mapped to or inherit from the target function.