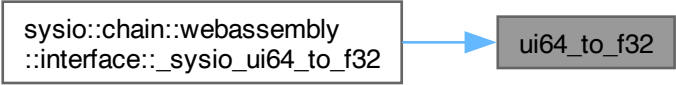


```
sysio::chain::webassembly  
::interface::_sysio_ui64_to_f32
```



```
graph LR; A["sysio::chain::webassembly  
::interface::_sysio_ui64_to_f32"] --> B["ui64_to_f32"]
```

ui64_to_f32