

sysio::chain::eosvmoc
::LLVMJIT::EmitFunctionContext
::br_if

sysio::chain::eosvmoc
::LLVMJIT::EmitFunctionContext::if_

sysio::chain::eosvmoc
::LLVMJIT::EmitFunctionContext
::select

sysio::chain::eosvmoc
::LLVMJIT::EmitFunctionContext
::coercel32ToBool

```
graph LR; A["sysio::chain::eosvmoc::LLVMJIT::EmitFunctionContext::br_if"] --> D["sysio::chain::eosvmoc::LLVMJIT::EmitFunctionContext::coercel32ToBool"]; B["sysio::chain::eosvmoc::LLVMJIT::EmitFunctionContext::if_"] --> D; C["sysio::chain::eosvmoc::LLVMJIT::EmitFunctionContext::select"] --> D;
```

The diagram illustrates a mapping or inheritance relationship. Three white rectangular boxes on the left represent source function contexts: `sysio::chain::eosvmoc::LLVMJIT::EmitFunctionContext::br_if`, `sysio::chain::eosvmoc::LLVMJIT::EmitFunctionContext::if_`, and `sysio::chain::eosvmoc::LLVMJIT::EmitFunctionContext::select`. Blue arrows point from each of these boxes to a single gray rectangular box on the right, which represents the target function context: `sysio::chain::eosvmoc::LLVMJIT::EmitFunctionContext::coercel32ToBool`.