

sysio::chain::webassembly
::interface::_sysio_f32_max

sysio::chain::webassembly
::interface::_sysio_f32_min

f32_sign_bit

```
graph LR; A["sysio::chain::webassembly::interface::_sysio_f32_max"] --> C["f32_sign_bit"]; B["sysio::chain::webassembly::interface::_sysio_f32_min"] --> C;
```

The diagram illustrates a mapping or dependency. On the left, there are two white rectangular boxes with black borders. The top box contains the text 'sysio::chain::webassembly::interface::_sysio_f32_max' and the bottom box contains 'sysio::chain::webassembly::interface::_sysio_f32_min'. On the right, there is a gray rectangular box with a black border containing the text 'f32_sign_bit'. Two blue arrows originate from the right side of the top and bottom white boxes and point towards the left side of the gray box, indicating that both source identifiers point to the 'f32_sign_bit' target.