

sysio::chain::eosvmoc
::LLVMJIT::JITModule
::compile

```
graph LR; A["sysio::chain::eosvmoc  
::LLVMJIT::JITModule  
::compile"] --> B["Errors::fatalf"]; A --> C["sysio::chain::eosvmoc  
::LLVMJIT::printModule"]
```

Errors::fatalf

sysio::chain::eosvmoc
::LLVMJIT::printModule