

sysio::chain::webassembly  
::interface::\_sysio\_f32\_add

sysio::chain::webassembly  
::interface::\_sysio\_f32  
\_nearest

sysio::vm::binary\_parser  
::parse\_function\_body\_code

f32\_add

```
graph LR; A["sysio::chain::webassembly::interface::_sysio_f32_add"] --> D["f32_add"]; B["sysio::chain::webassembly::interface::_sysio_f32_nearest"] --> D; C["sysio::vm::binary_parser::parse_function_body_code"] --> D;
```

The diagram illustrates a mapping or linking process. On the left, there are three white rectangular boxes with black borders, each containing a fully qualified function name. On the right, there is a single gray rectangular box with a black border containing the name 'f32\_add'. Three blue arrows originate from the right side of each white box and point towards the left side of the gray box, indicating that each of the three functions on the left is associated with or maps to the 'f32\_add' function on the right.