

sysio::chain::webassembly  
::interface::\_sysio\_f32\_gt

sysio::vm::binary\_parser  
::parse\_function\_body\_code

f32\_le

```
graph LR; A["sysio::chain::webassembly<br/>::interface::_sysio_f32_gt"] --> C["f32_le"]; B["sysio::vm::binary_parser<br/>::parse_function_body_code"] --> C;
```

The diagram illustrates a dependency or mapping. Two source boxes on the left point via blue arrows to a single target box on the right. The top source box contains the text 'sysio::chain::webassembly' followed by '::interface::\_sysio\_f32\_gt' on a new line. The bottom source box contains 'sysio::vm::binary\_parser' followed by '::parse\_function\_body\_code' on a new line. The target box on the right is shaded gray and contains the text 'f32\_le'.