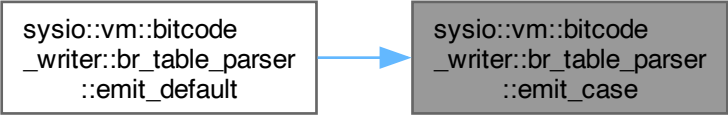


```
sysio::vm::bitcode  
_writer::br_table_parser  
::emit_default
```



```
graph LR; A["sysio::vm::bitcode  
_writer::br_table_parser  
::emit_default"] --> B["sysio::vm::bitcode  
_writer::br_table_parser  
::emit_case"]
```

```
sysio::vm::bitcode  
_writer::br_table_parser  
::emit_case
```