

sysio::chain::eosvmoc
::LLVMJIT::EmitFunctionContext
::br_if

sysio::chain::eosvmoc
::LLVMJIT::EmitFunctionContext::if_

sysio::chain::eosvmoc
::LLVMJIT::EmitFunctionContext
::select

sysio::chain::eosvmoc
::LLVMJIT::EmitFunctionContext
::coercel32ToBool

```
graph LR; A["sysio::chain::eosvmoc::LLVMJIT::EmitFunctionContext::br_if"] --> D["sysio::chain::eosvmoc::LLVMJIT::EmitFunctionContext::coercel32ToBool"]; B["sysio::chain::eosvmoc::LLVMJIT::EmitFunctionContext::if_"] --> D; C["sysio::chain::eosvmoc::LLVMJIT::EmitFunctionContext::select"] --> D;
```

The diagram illustrates a mapping or inheritance relationship. Three source function contexts on the left (white boxes) point via blue arrows to a single target function context on the right (gray box). The source contexts are for `br_if`, `if_`, and `select` instructions. The target context is for the `coercel32ToBool` instruction.