

sysio::chain::webassembly
::interface::_sysio_f32_gt

sysio::vm::binary_parser
::parse_function_body_code

f32_le

```
graph LR; A["sysio::chain::webassembly  
::interface::_sysio_f32_gt"] --> C["f32_le"]; B["sysio::vm::binary_parser  
::parse_function_body_code"] --> C;
```

The diagram illustrates a dependency or mapping. Two source components, each in a white box with a black border, have blue arrows pointing to a single target component in a gray box. The top source box contains the text 'sysio::chain::webassembly' followed by '::interface::_sysio_f32_gt' on a new line. The bottom source box contains 'sysio::vm::binary_parser' followed by '::parse_function_body_code' on a new line. The target box on the right contains the text 'f32_le'.