

sysio::chain::webassembly
::interface::_sysio_f64_max

sysio::chain::webassembly
::interface::_sysio_f64_min

f64_sign_bit

```
graph LR; A["sysio::chain::webassembly<br>::interface::_sysio_f64_max"] --> C["f64_sign_bit"]; B["sysio::chain::webassembly<br>::interface::_sysio_f64_min"] --> C;
```

The diagram illustrates a mapping or dependency. On the left, there are two white rectangular boxes with black borders. The top box contains the text 'sysio::chain::webassembly' followed by '::interface::_sysio_f64_max' on a new line. The bottom box contains the text 'sysio::chain::webassembly' followed by '::interface::_sysio_f64_min' on a new line. Two blue arrows originate from the right side of these boxes. The arrow from the top box points to the top of a gray rectangular box on the right. The arrow from the bottom box points to the bottom of the same gray box. The gray box contains the text 'f64_sign_bit'.