

## ReV-SoC on Xcelium

In order to run ReV SoC on Xcelium the programmers should follow the following steps.

1. Firstly clone the repo of ReV-SoC from  
<https://github.com/merledu/Rev-Soc>
2. Now remove the files that are not part of design, for example the standalone testbenches that are provided for each peripheral documents, images, assembly testbenches, presentation except the parameters.
3. Now make 3 directories named as rtl testbench and include
4. Add all of the design files into rtl that are .v or .sv files except el2\_def.sv
5. Now add all of the .vh and .h files to the include directory and also add el2\_def.sv into include directory.
6. Now copy and paste the contents of param.vh file at the end of el2\_def.sv and remove the param.vh file
7. Copy and paste all of the contents of pic\_map\_auto.h into the module el2\_pic\_ctrl which is in el2\_pic\_ctrl.sv file.
8. Create an flist file with .f extension and add list all of the files with proper path from the root directory. At the end of this file add command line options -access +rwc each on a separate line.
9. Finally, run the xrun command with -incdir option:  
`% xrun -f flist.f -incdir include`